Structured Electronic Design
Shannon (1948) Channel Capacity

The amount of information that can be handled by a system

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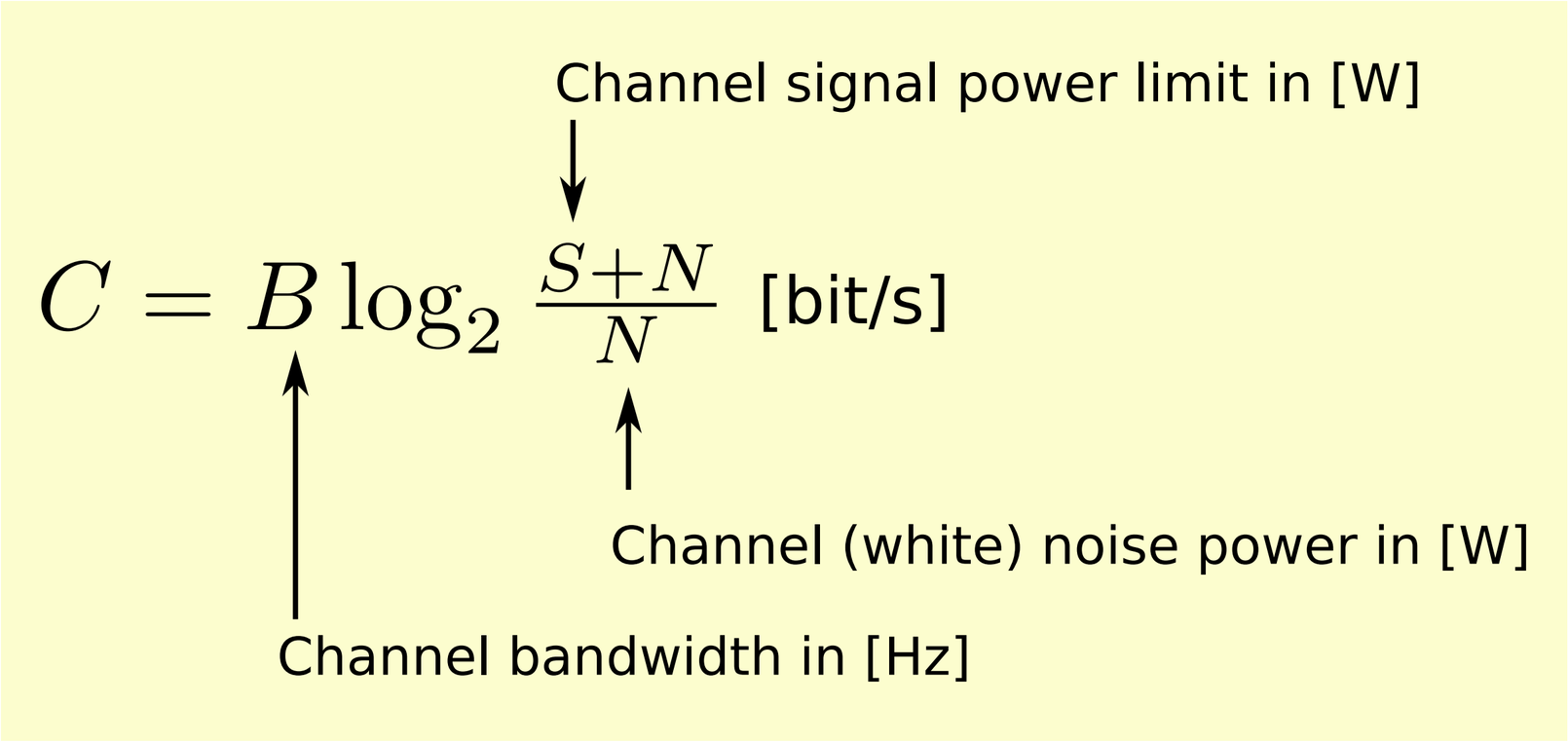

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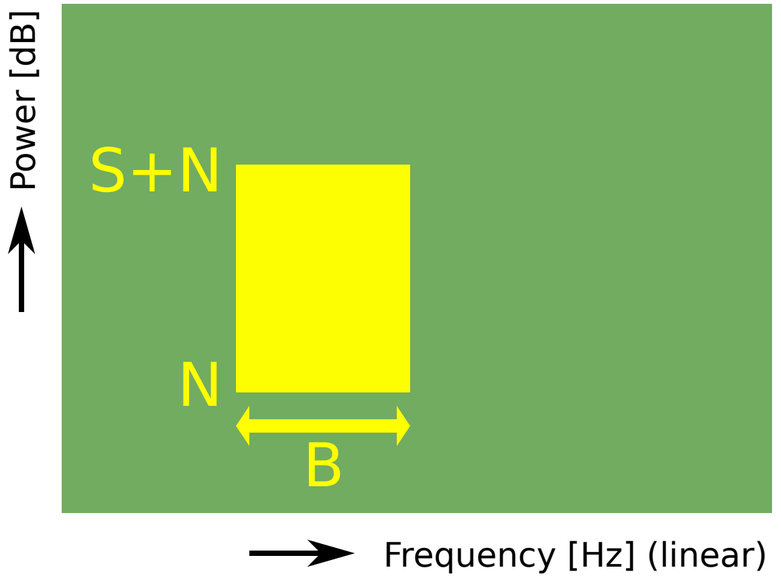
$$C = B \log_2 \frac{S+N}{N} \text{ [bit/s]}$$

Channel signal power limit in [W]

Channel (white) noise power in [W]

Channel bandwidth in [Hz]

Spectrum analyser



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Area => dB/s