

# Structured Electronic Design

## Noise in Electronic Circuits

# Noise mechanisms

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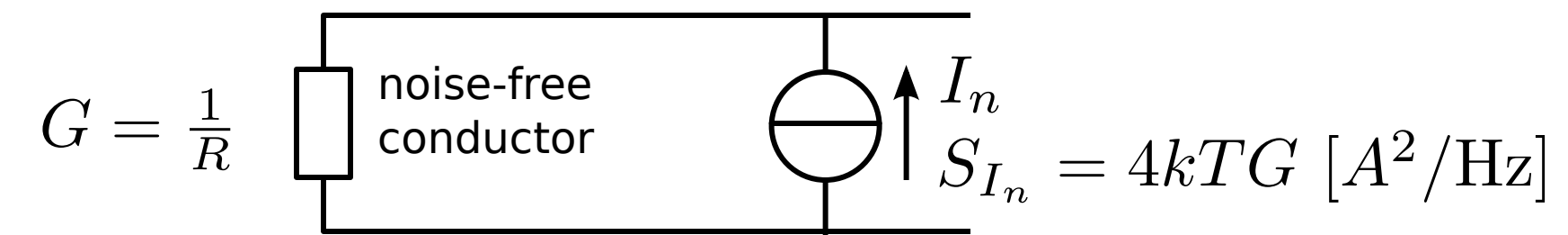
## **Thermal noise**

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Experimentally detected by Johnson (1928) and explained by Nyquist (1928).

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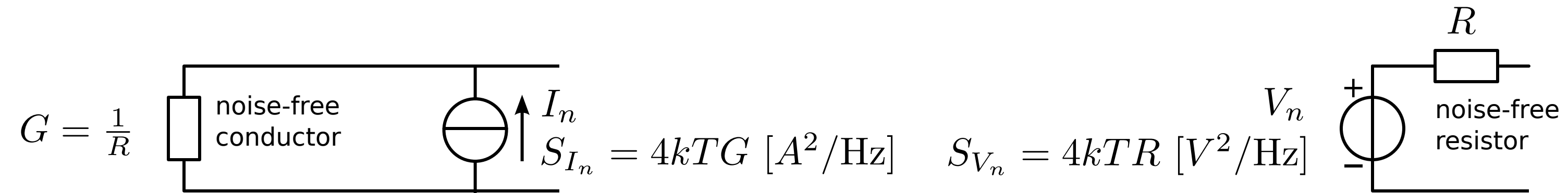
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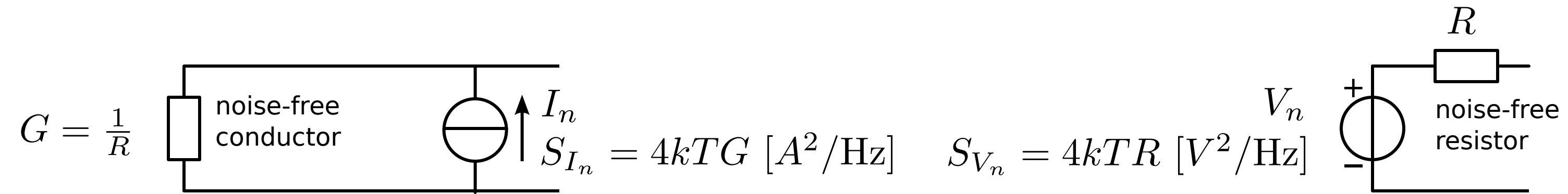
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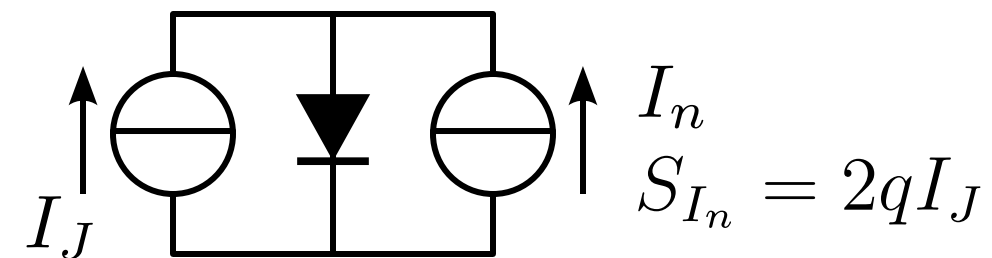
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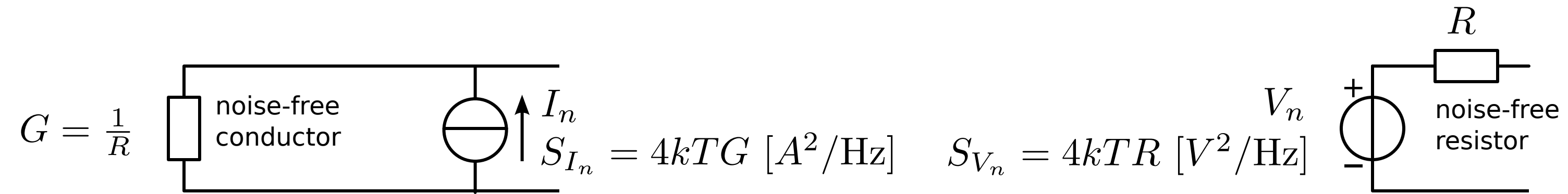
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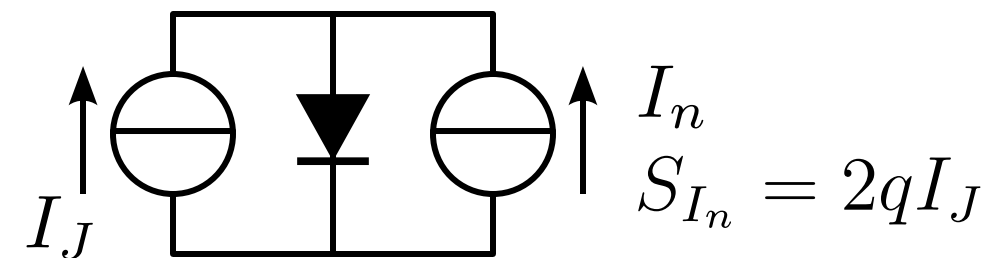
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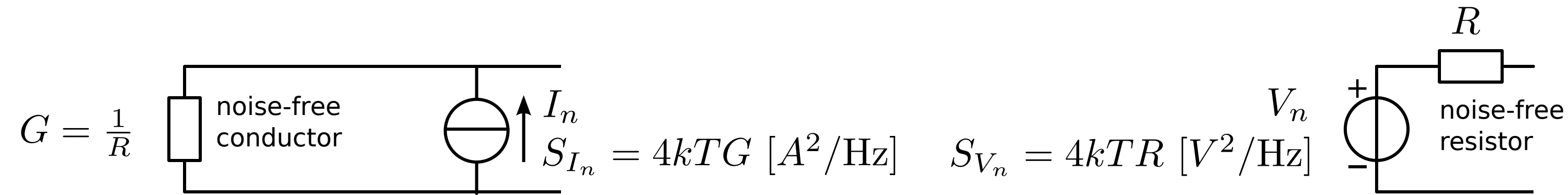
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Noise current resulting from fluctuations in conduction mechanism.

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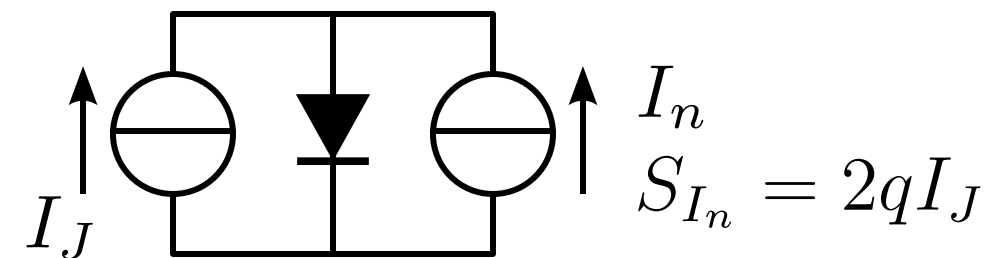
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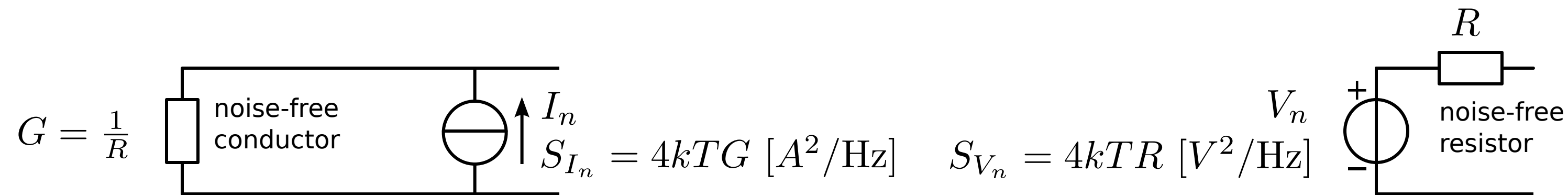
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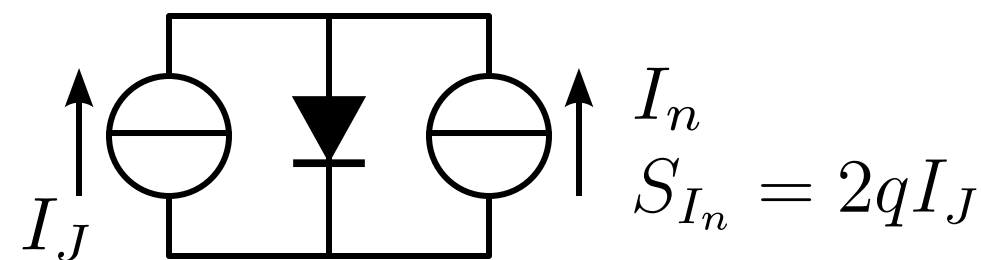
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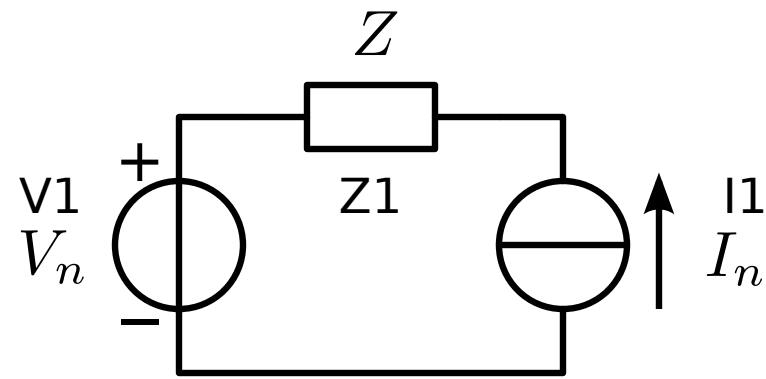
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**In resistors**  $S_{V_n} = K \frac{V_R^2}{f}$

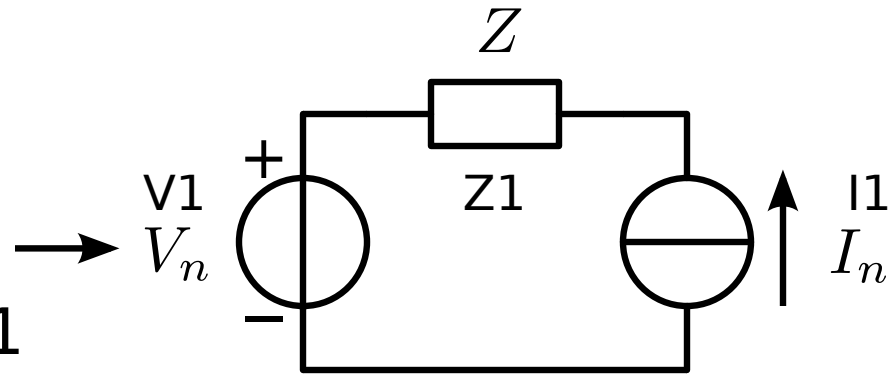
# Drawing conventions

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Fourier  
Transform  
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voltage of V1



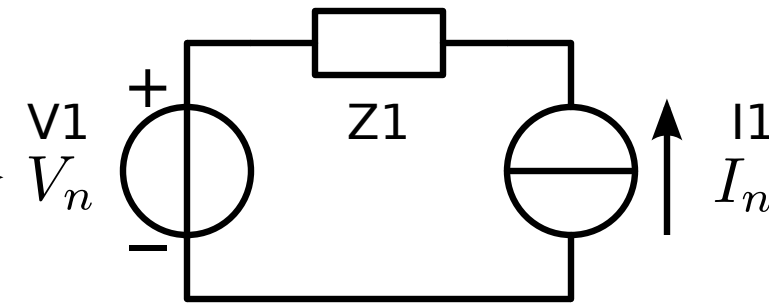
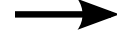
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Complex impedance of Z1

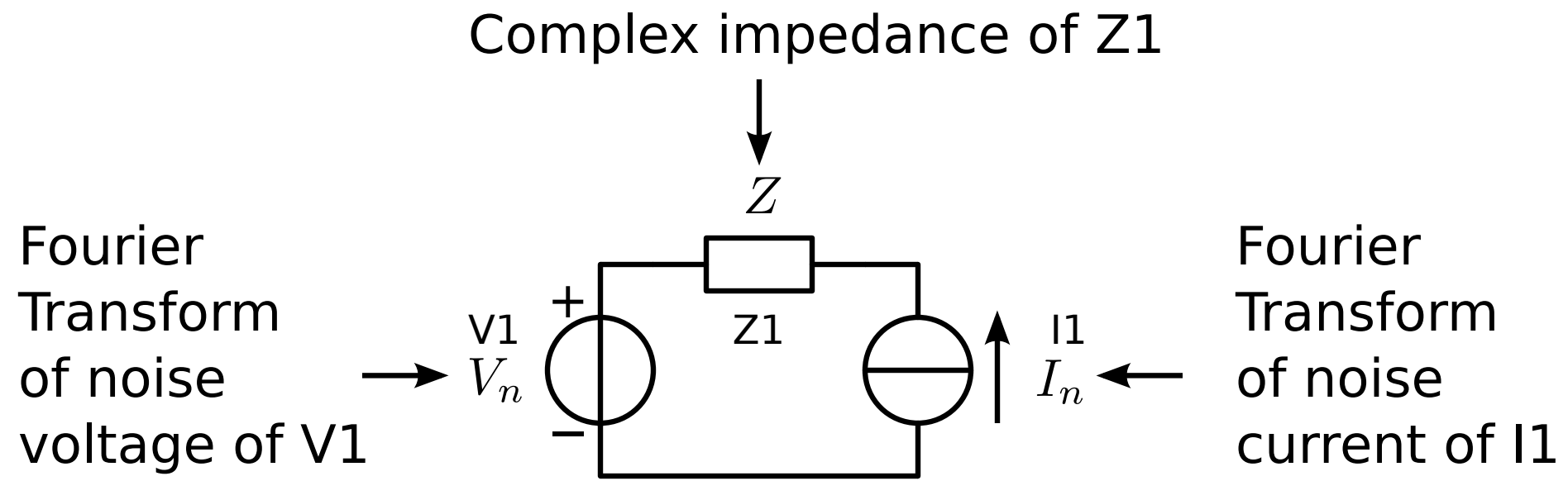


$Z$

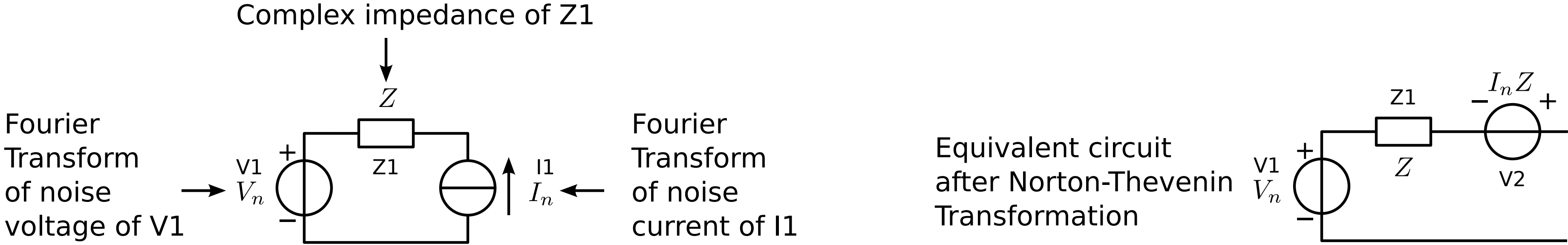
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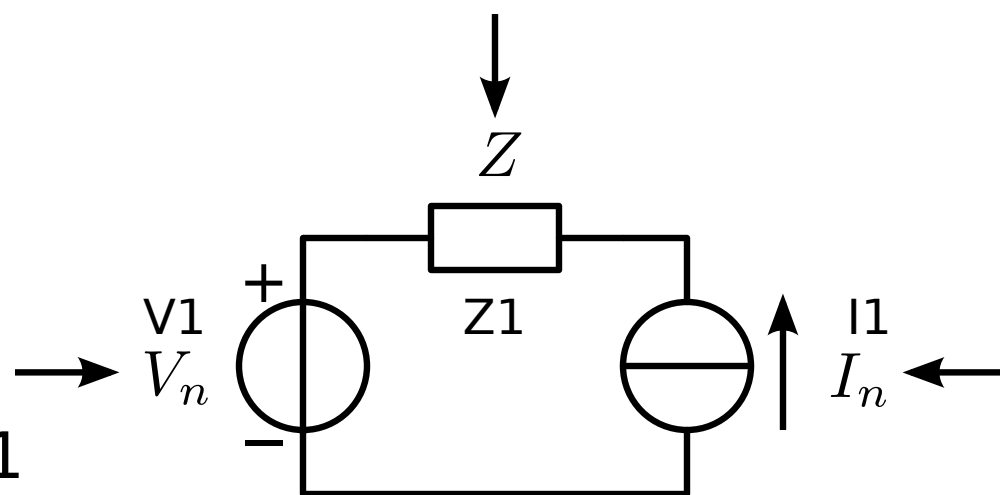
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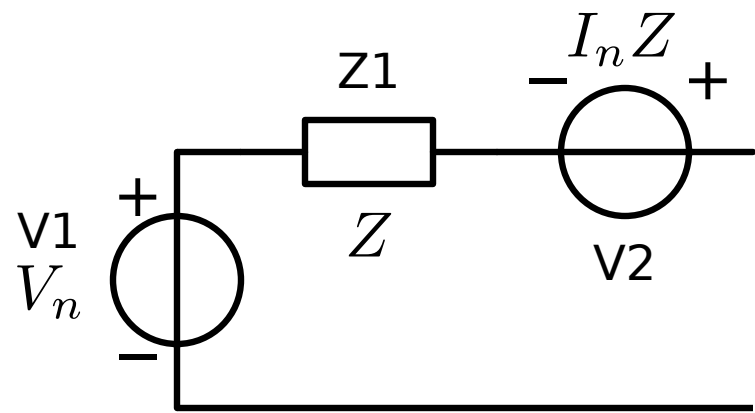
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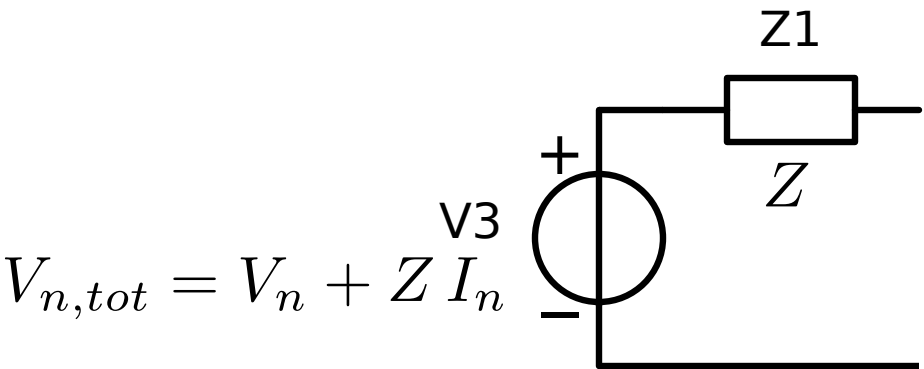


Fourier Transform of noise current of I1

Equivalent circuit after Norton-Thevenin Transformation



Equivalent circuit in which the voltage of V3 represents the total noise voltage





# Noise parameters

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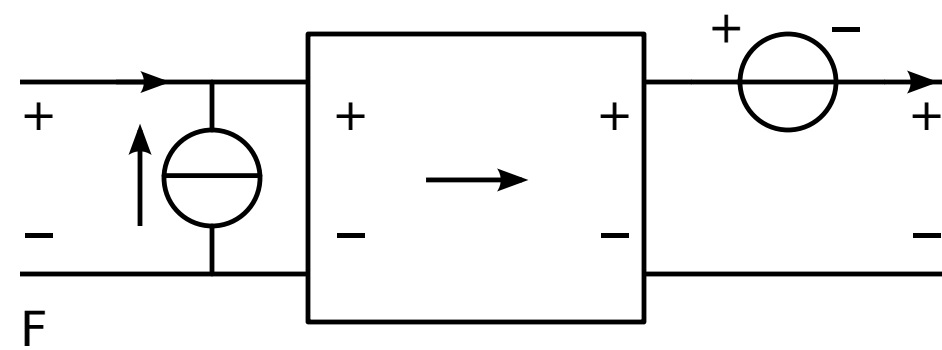
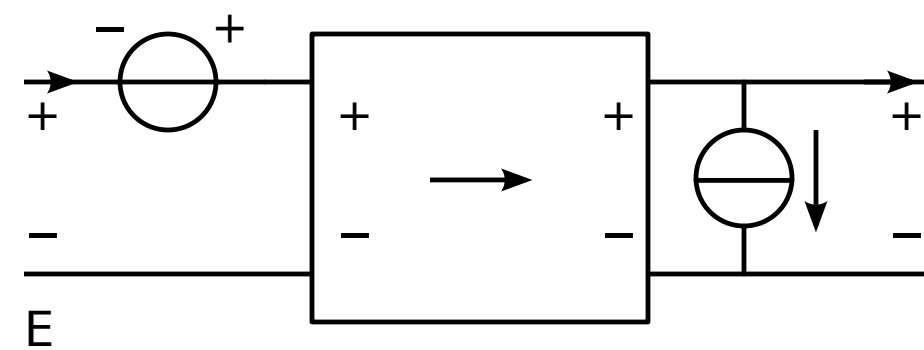
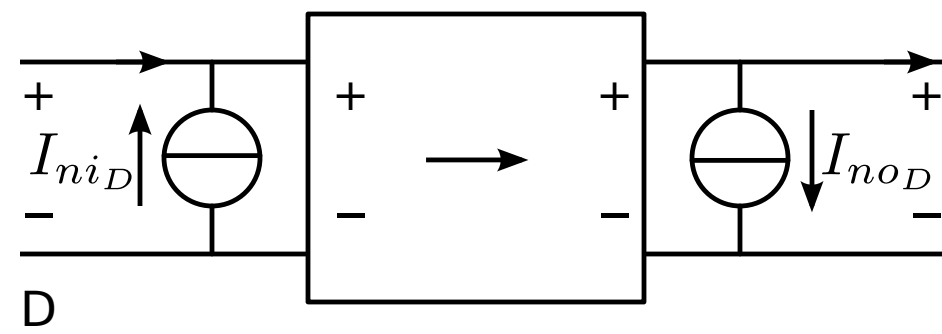
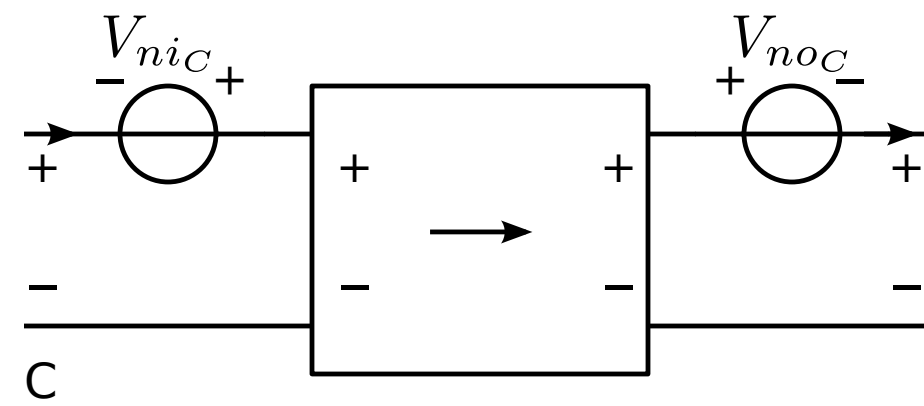
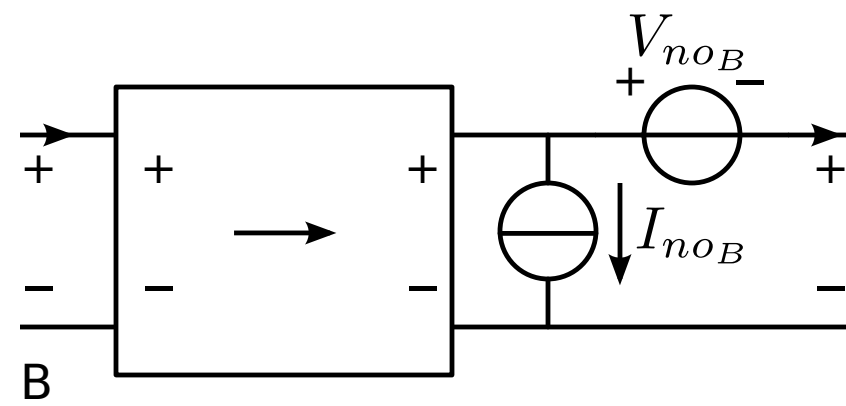
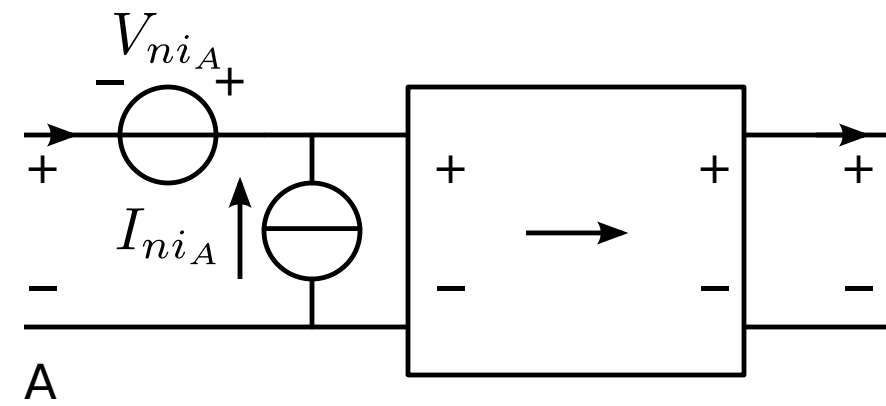
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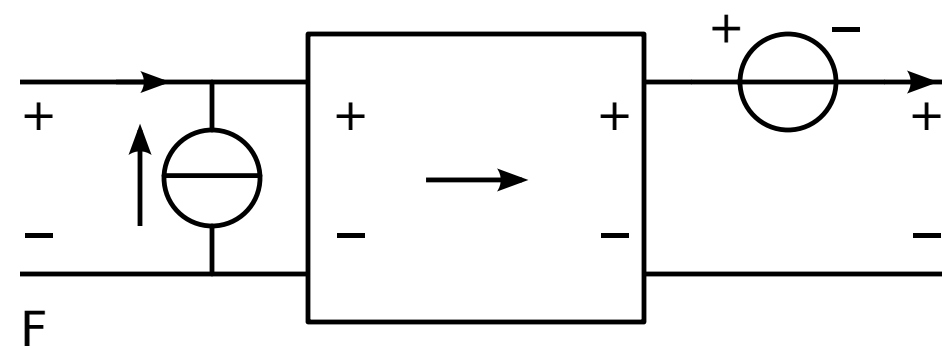
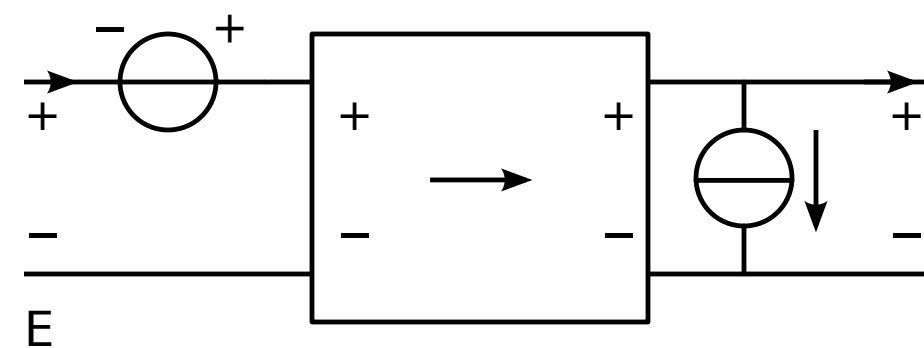
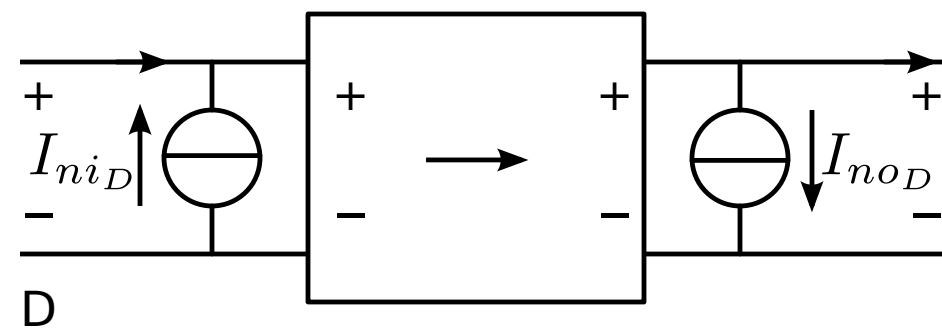
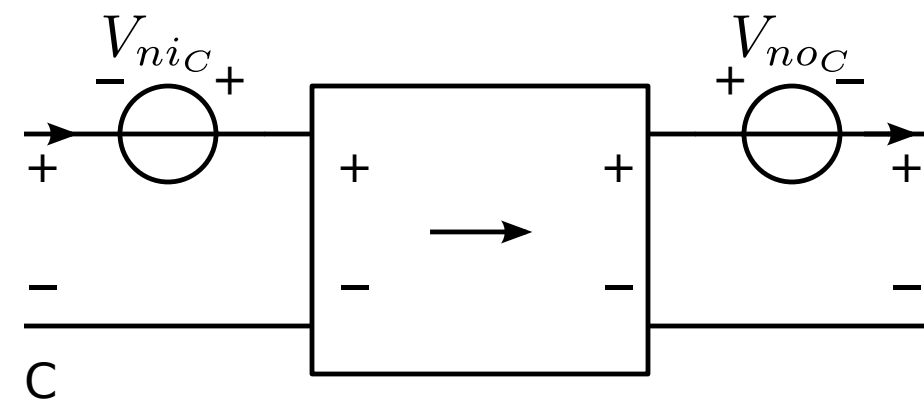
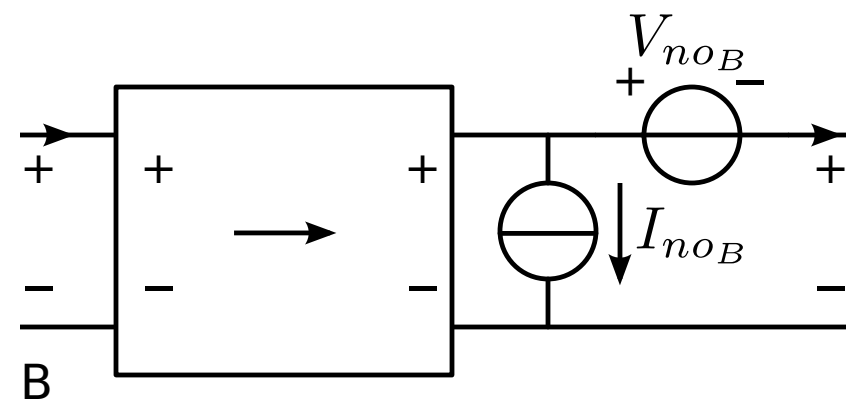
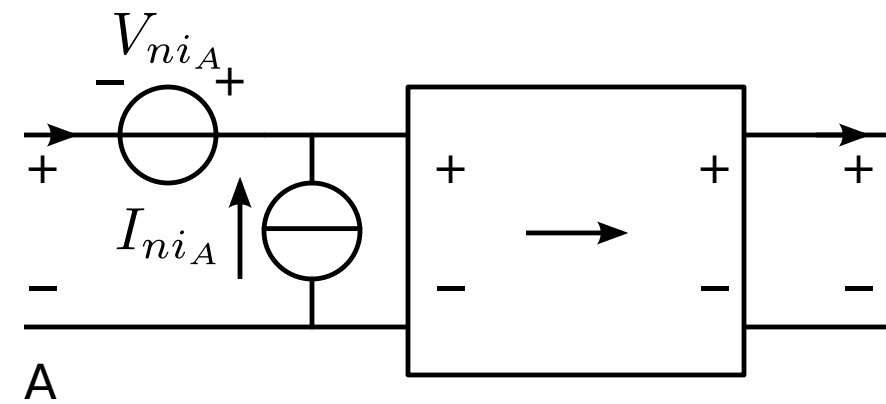


# Noisy two-ports

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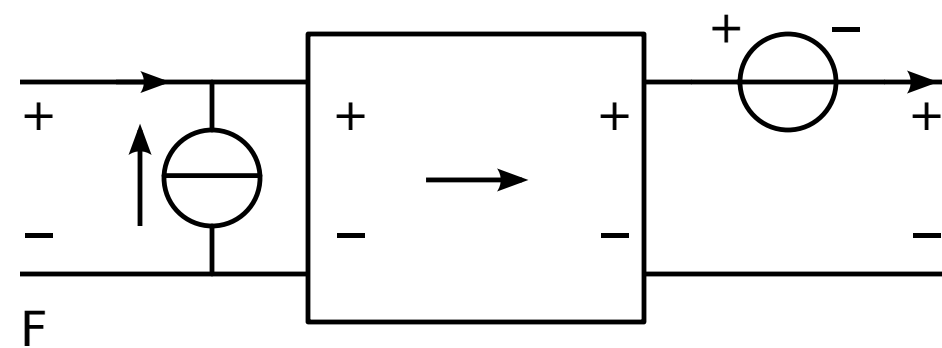
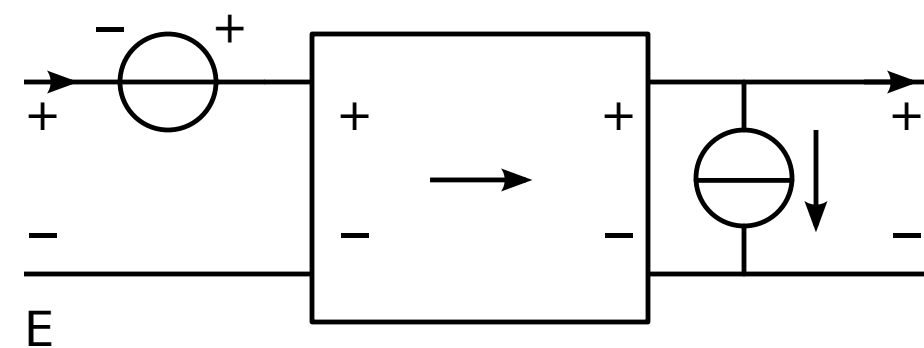
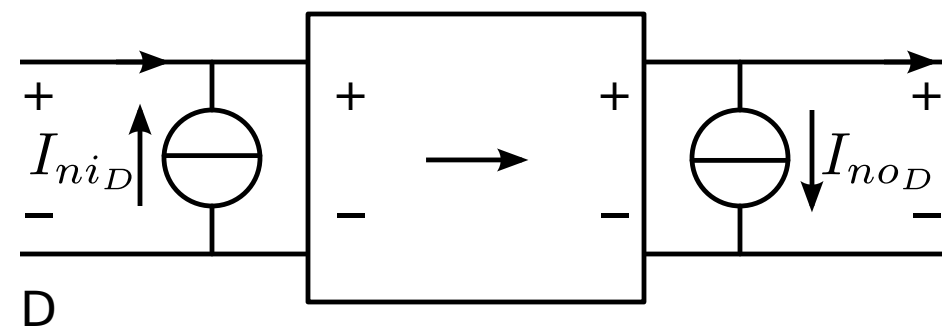
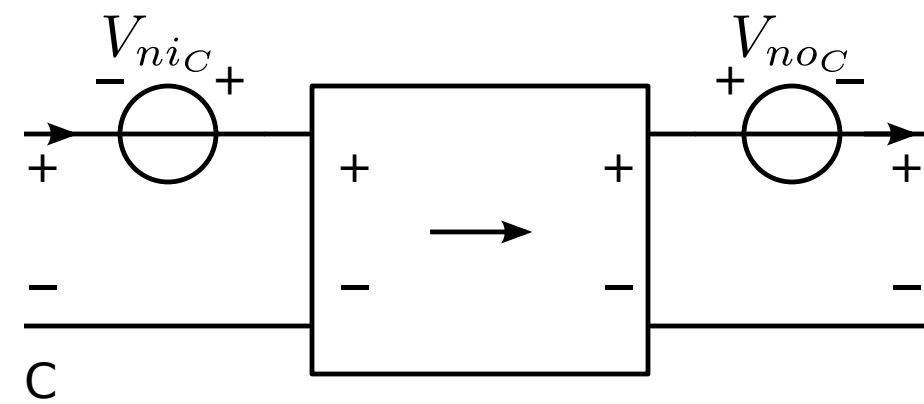
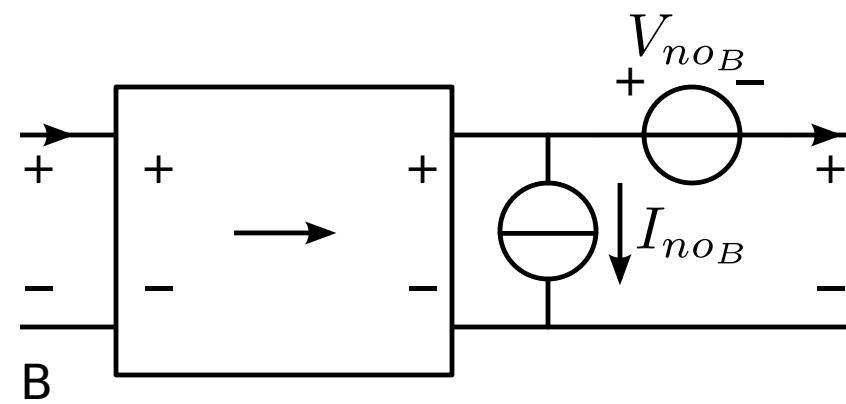
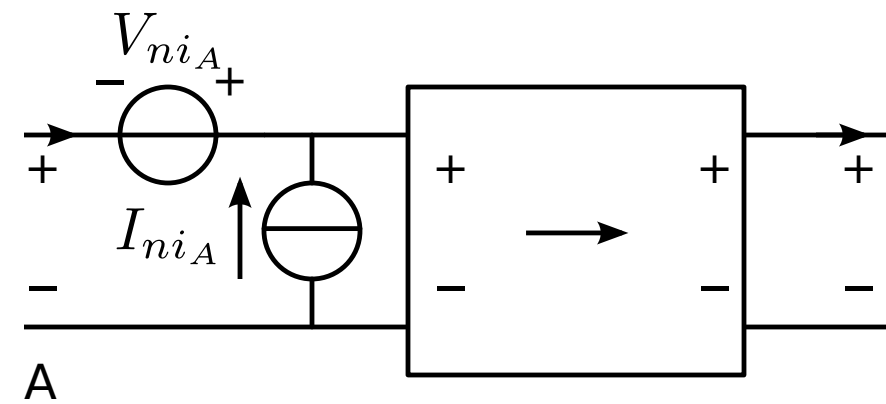
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- A noise-free two-port with two noise sources
- Six representations:
  - 4 port variables:
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## Can be translated into each other

- Example 2.9
- Example 19.2

# Amplifier noise measurement

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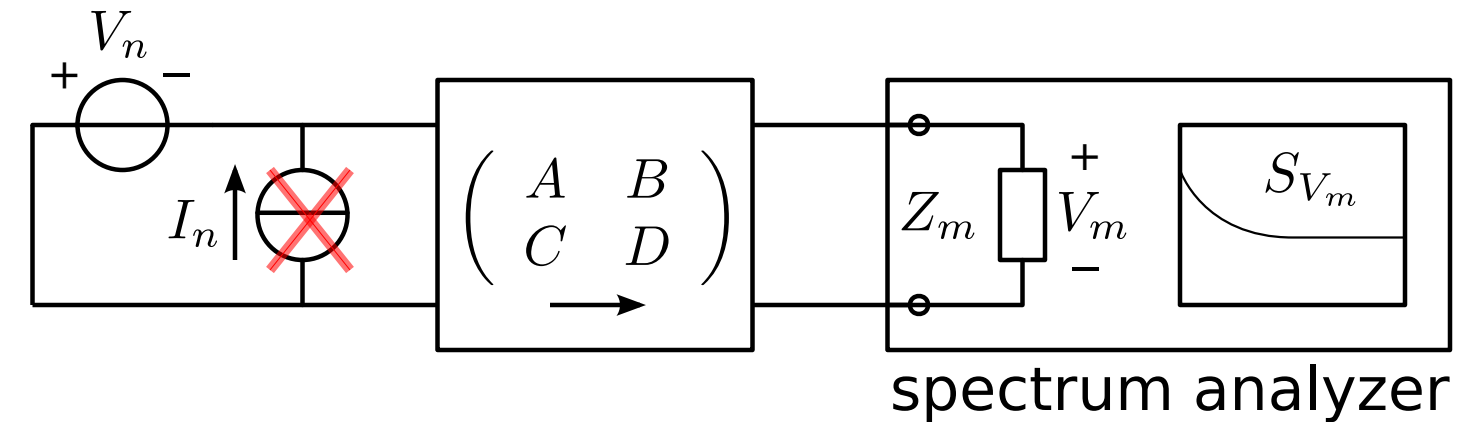
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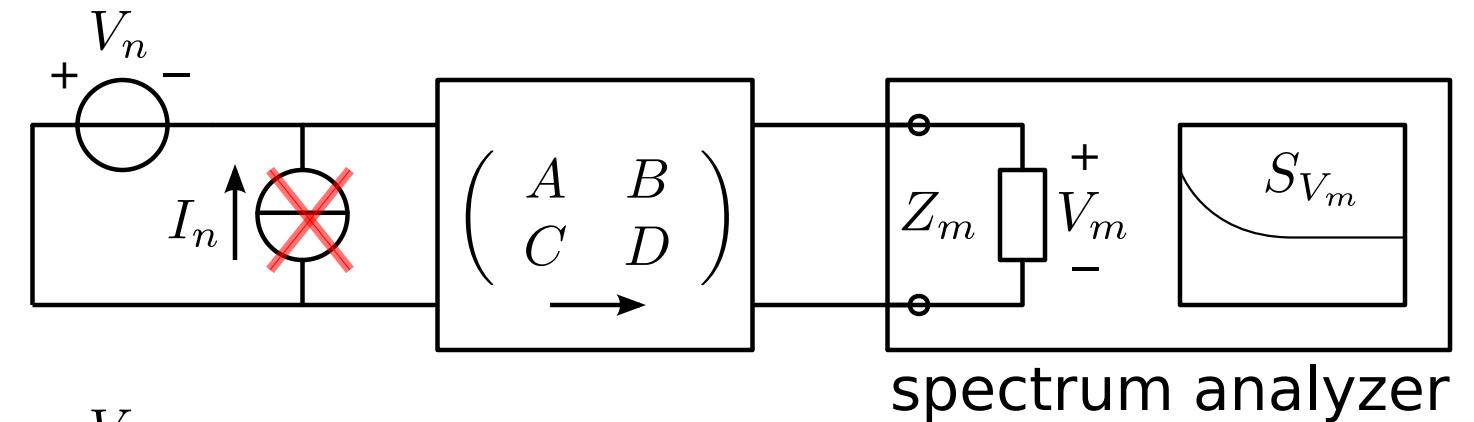


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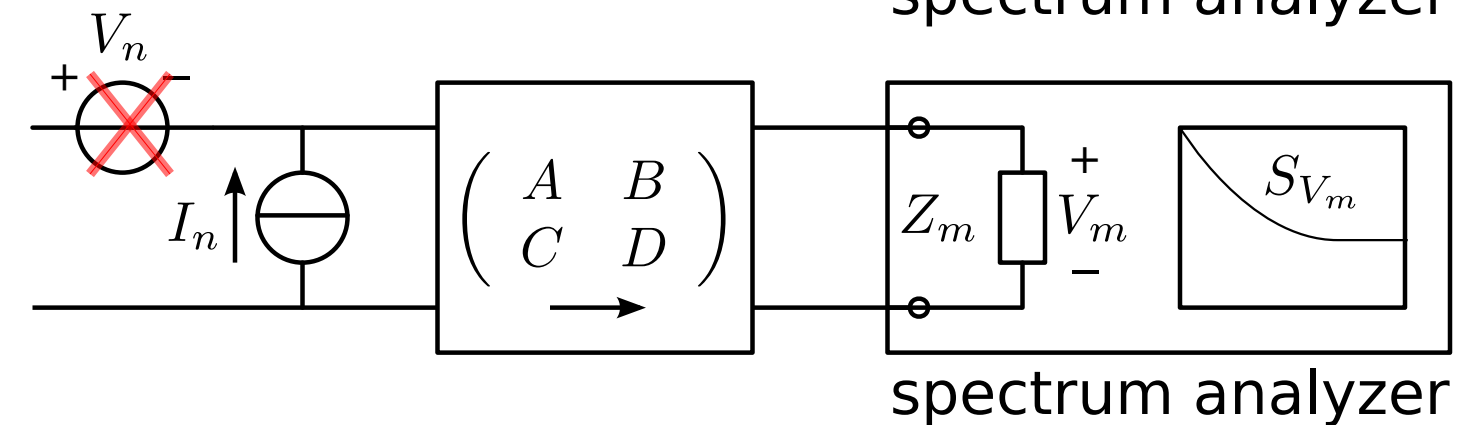
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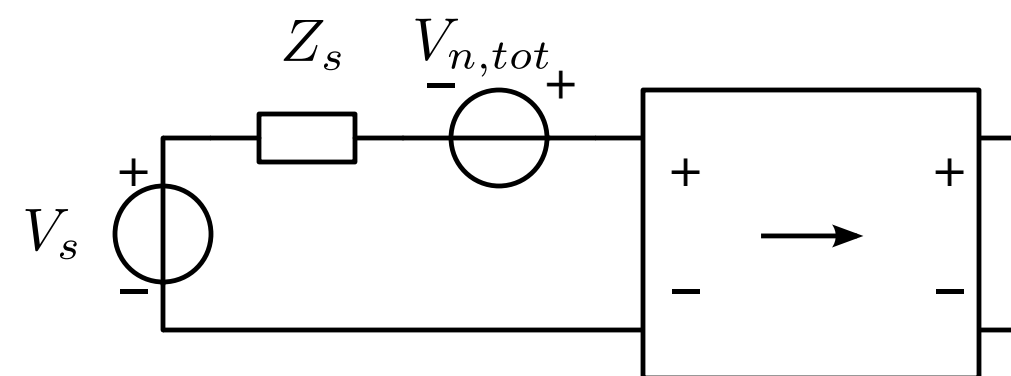
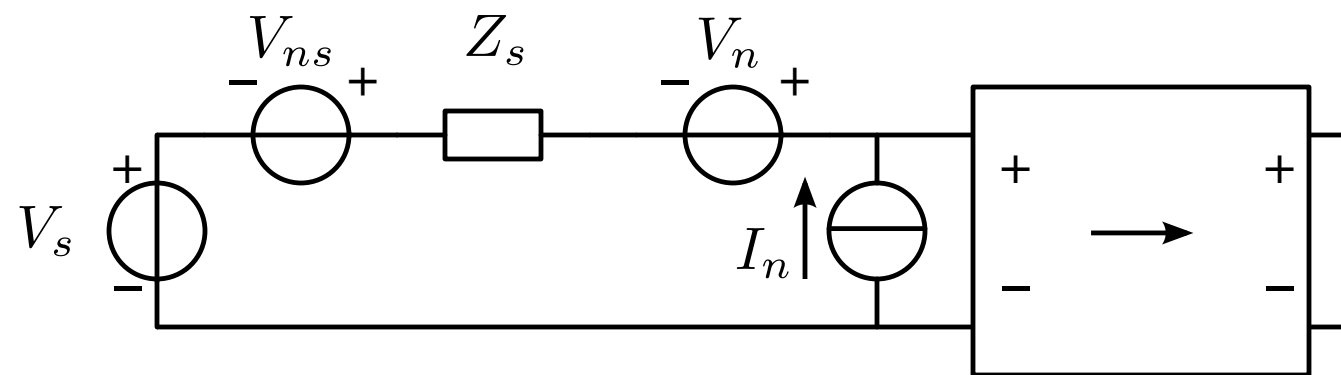
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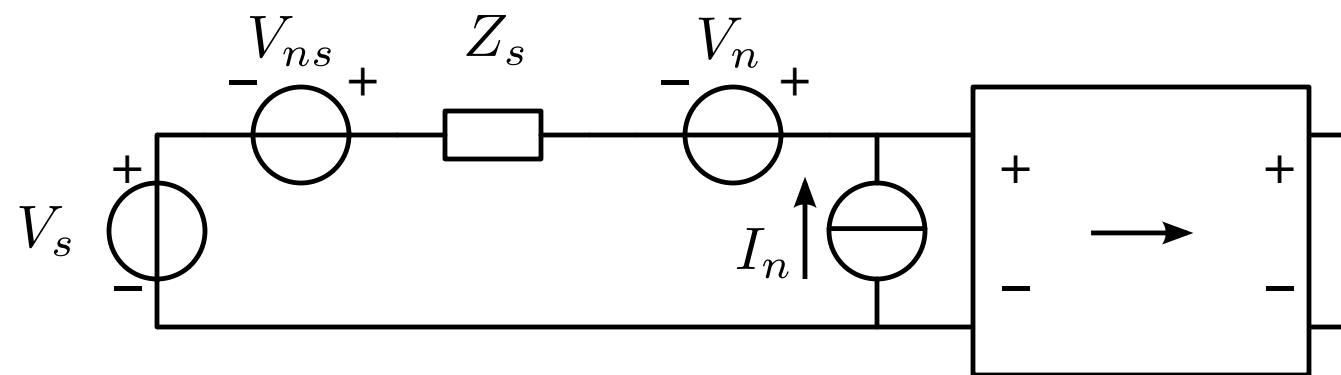


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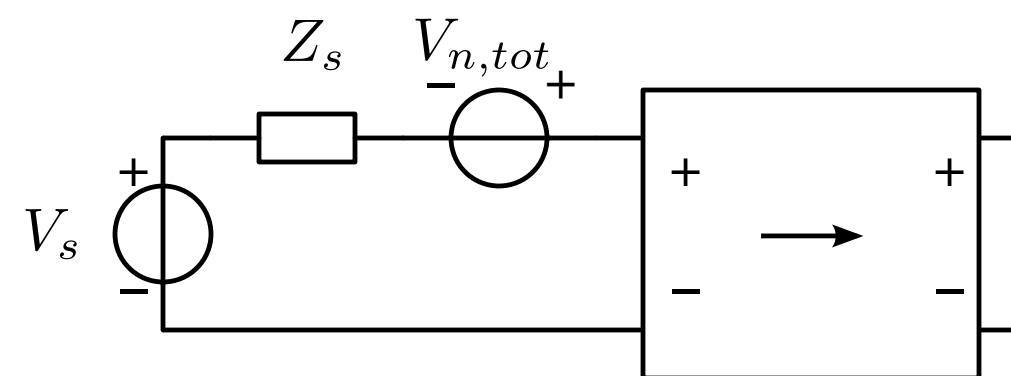
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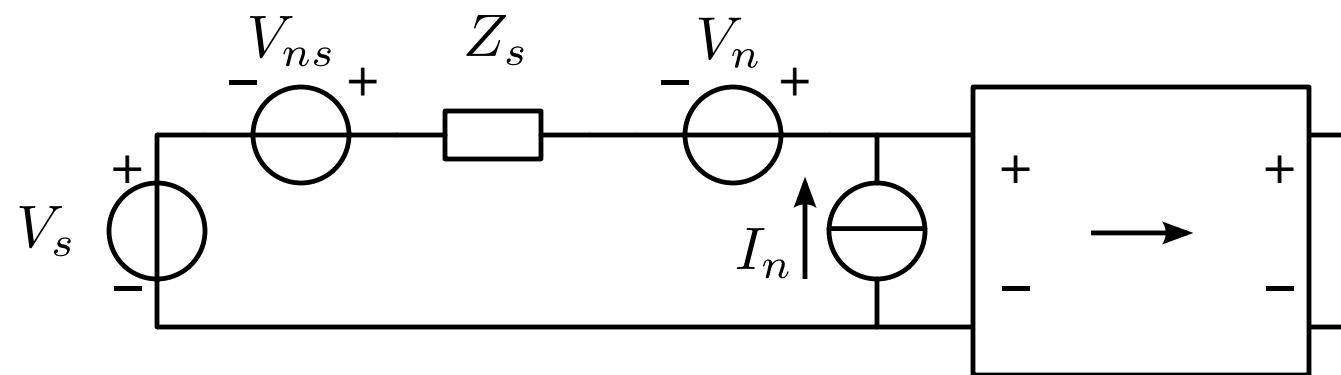


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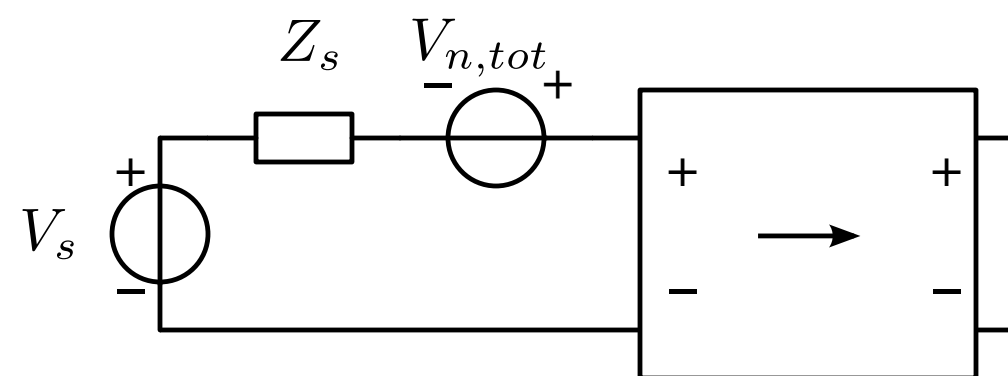
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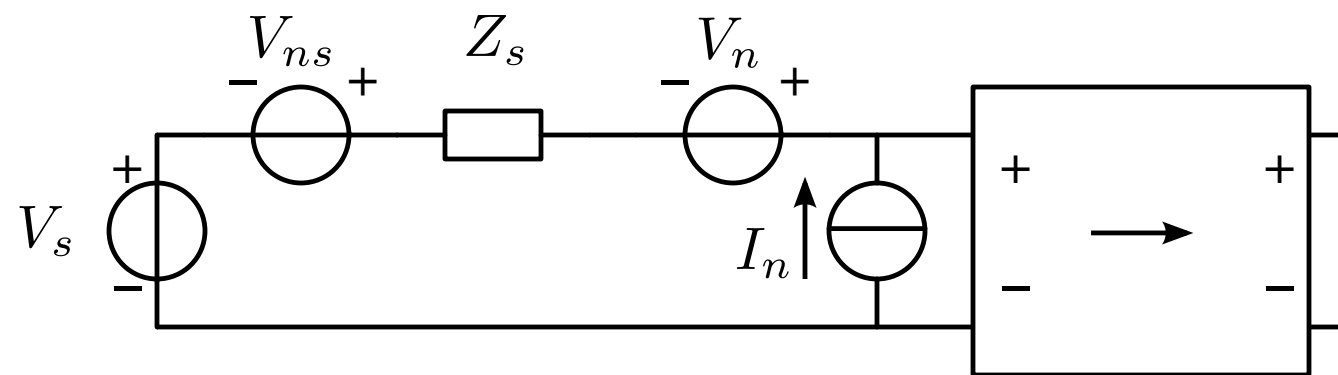
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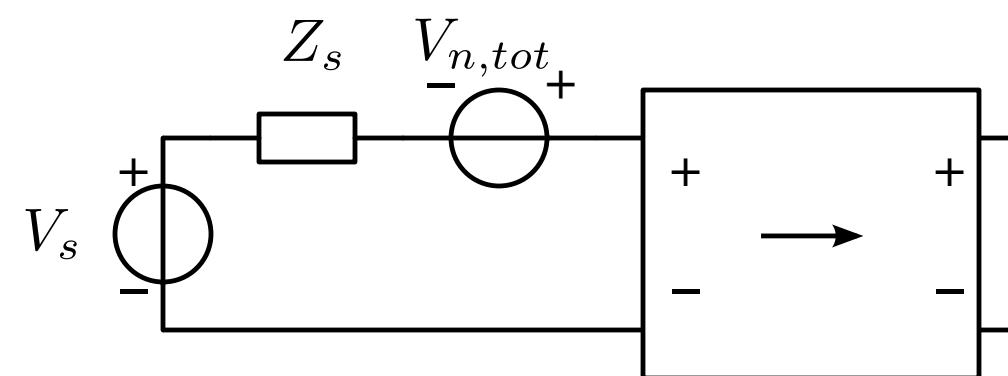
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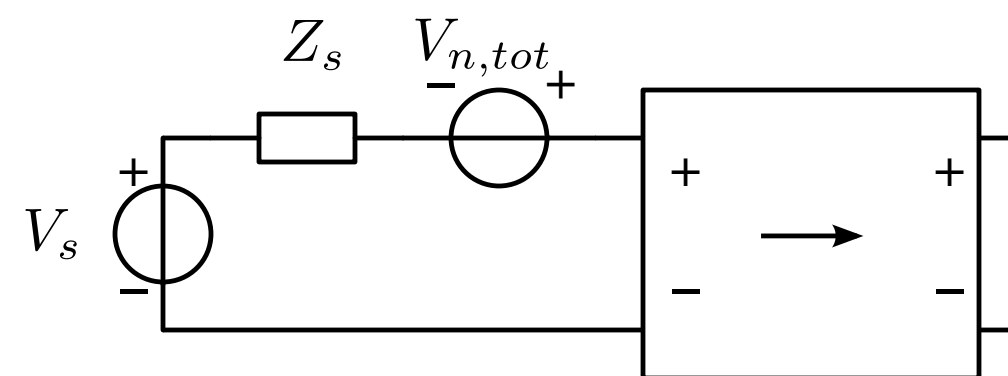
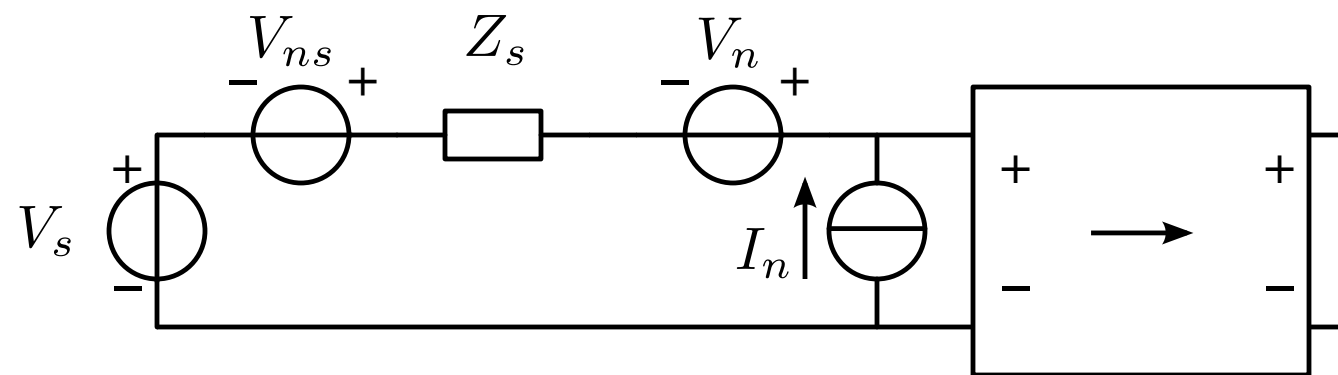
$$|W(f)|^2$$

Squared magnitude of  
weighting function that  
models the sensitivity of  
the observer as a function  
of frequency

# Amplifier noise design

Equivalent-input noise description is convenient at early stages of the design.

Budgets for equivalent input noise sources can be determined without knowledge of amplifier circuit.



$$S_{V_{n,tot}} = S_{V_{ns}} + S_{V_n} + S_{I_n} |Z_s|^2$$

Noise figure  
equivalent-input notation:

$$F = \frac{\int_0^\infty S_{V_{n,tot}} |W(f)|^2 df}{\int_0^\infty S_{V_{ns}} |W(f)|^2 df}$$

$|W(f)|^2$

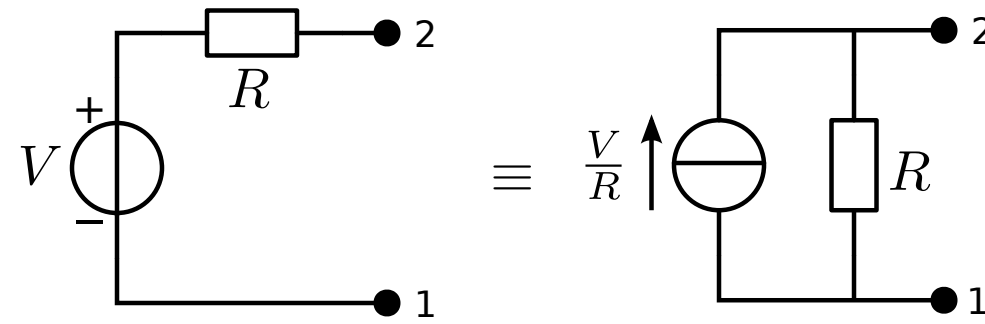
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# Source transformation techniques

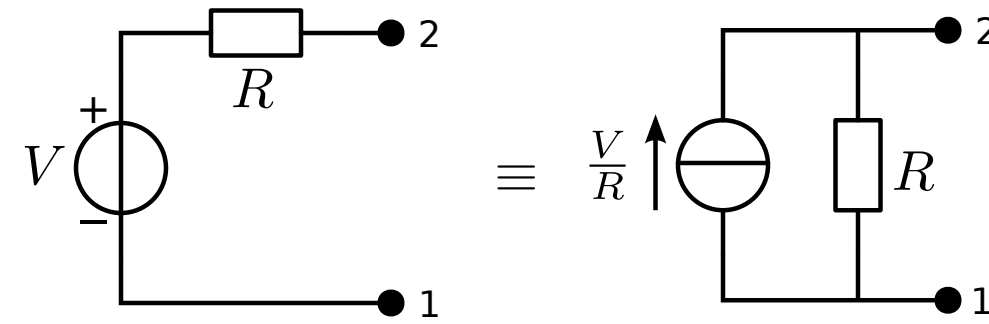
# Source transformation techniques

Thévenin / Norton  
equivalent networks

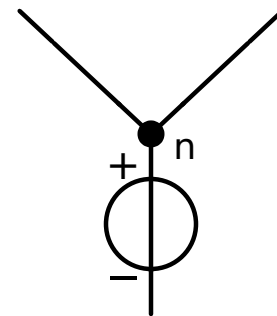


# Source transformation techniques

Thévenin / Norton  
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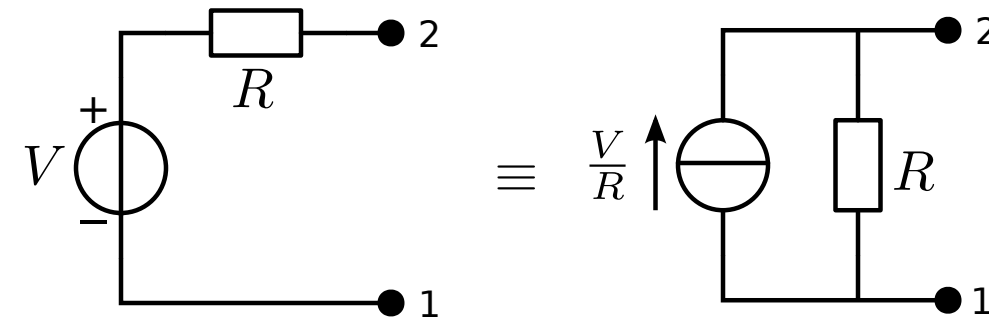


Blakesley voltage shift

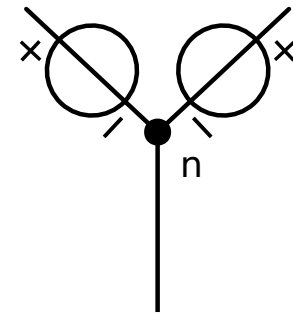


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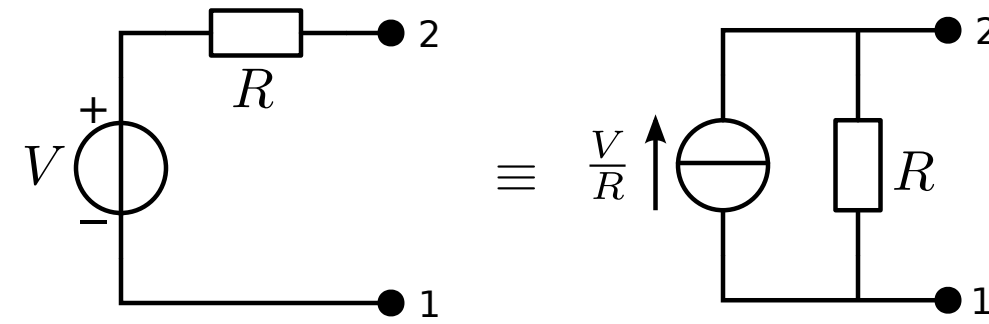


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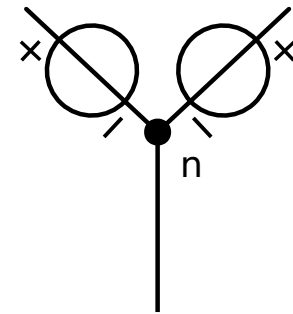


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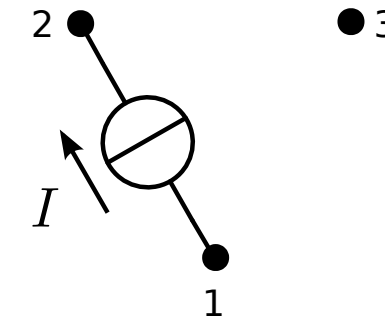
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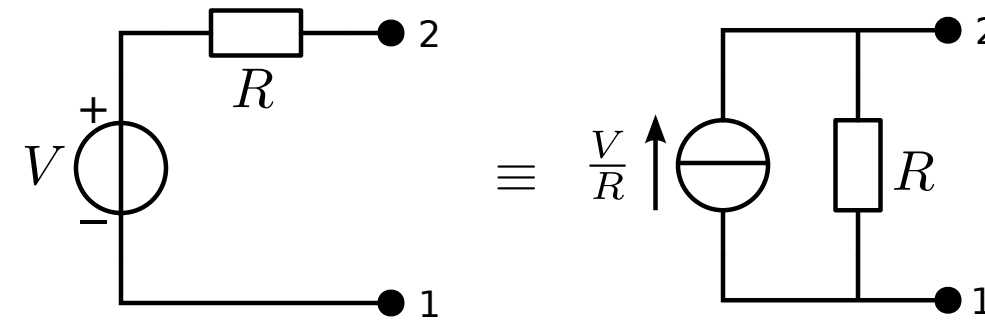


Current split / redirect

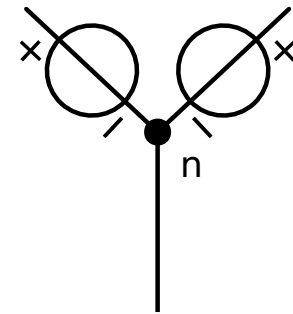


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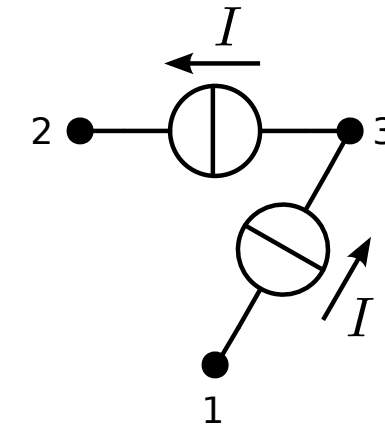
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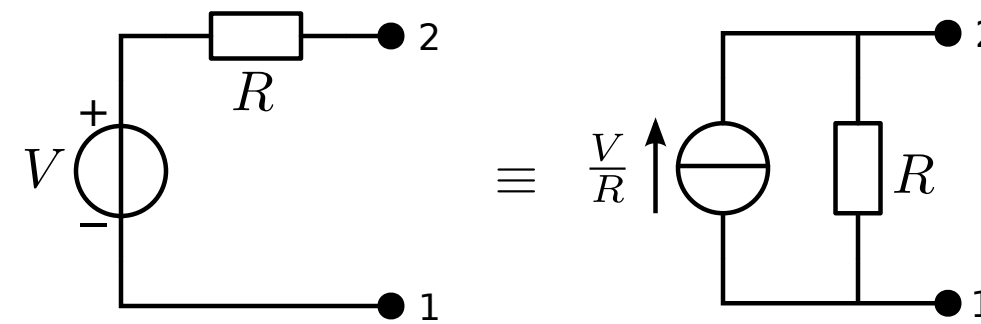


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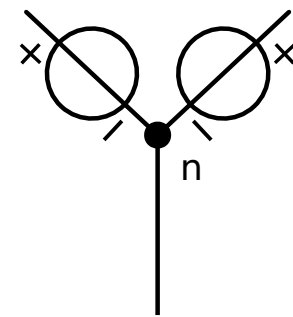


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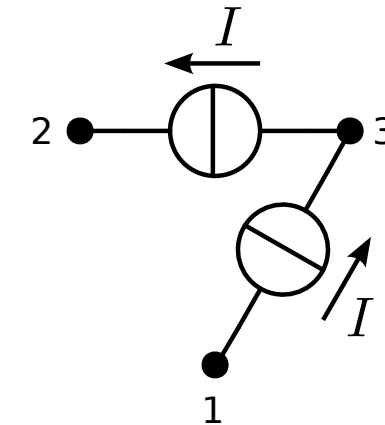
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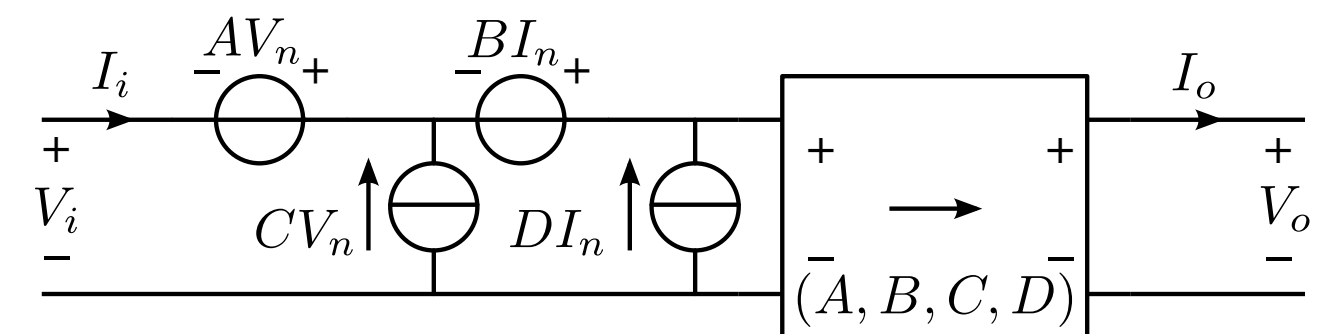
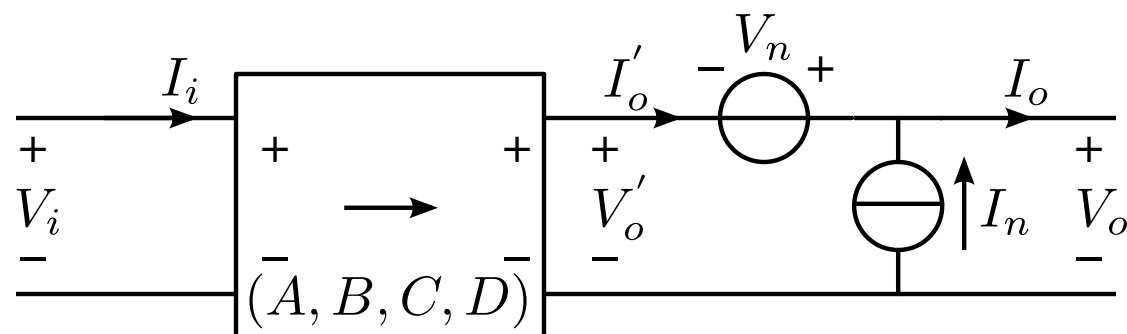
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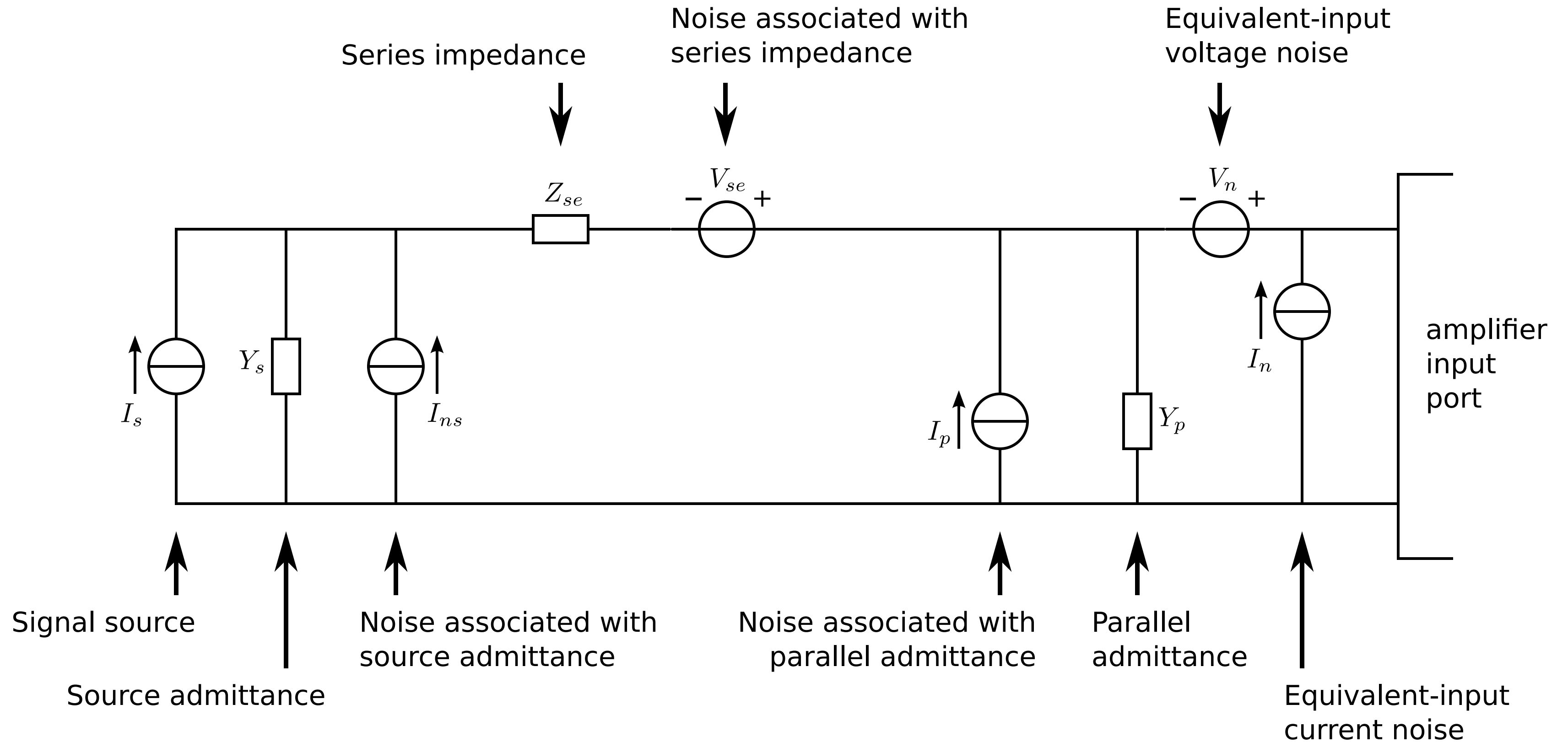
Current split / redirect



Equivalent two-port representations

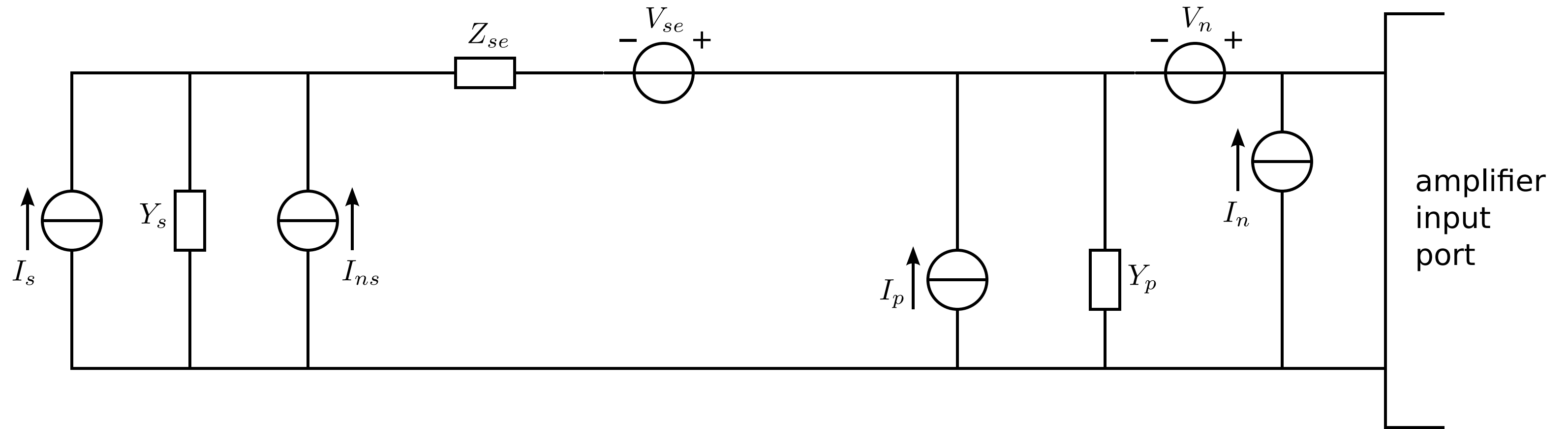


# Impedances in the signal path

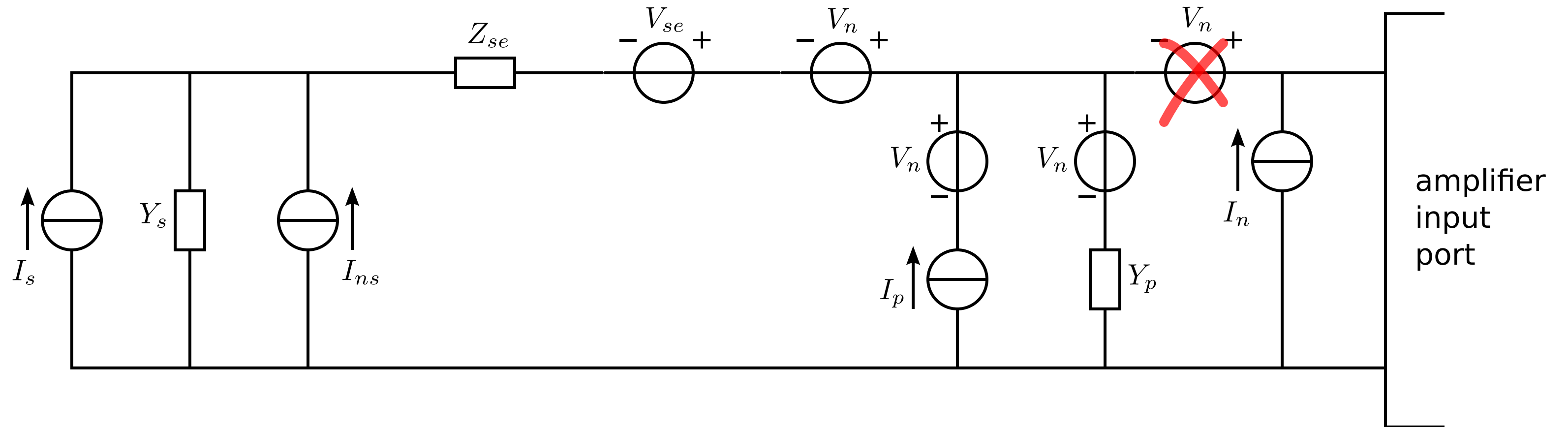




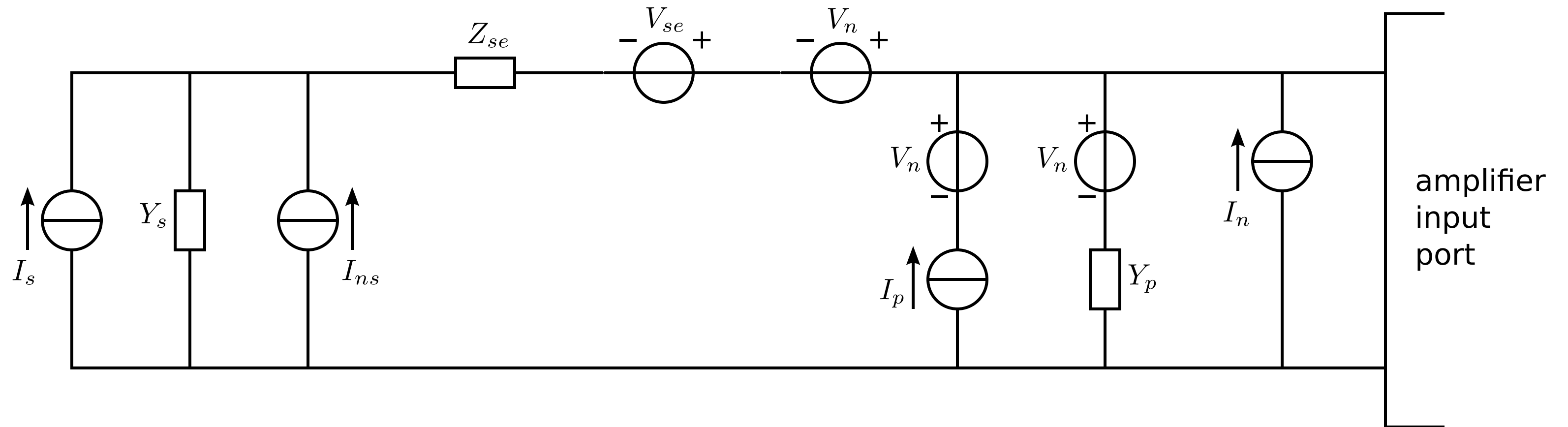
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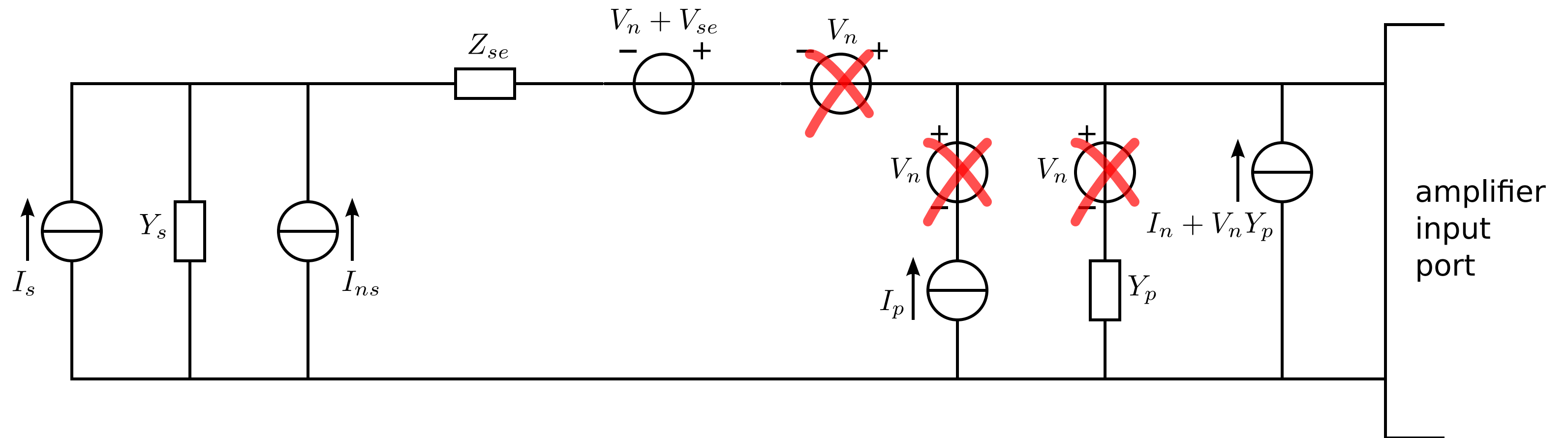
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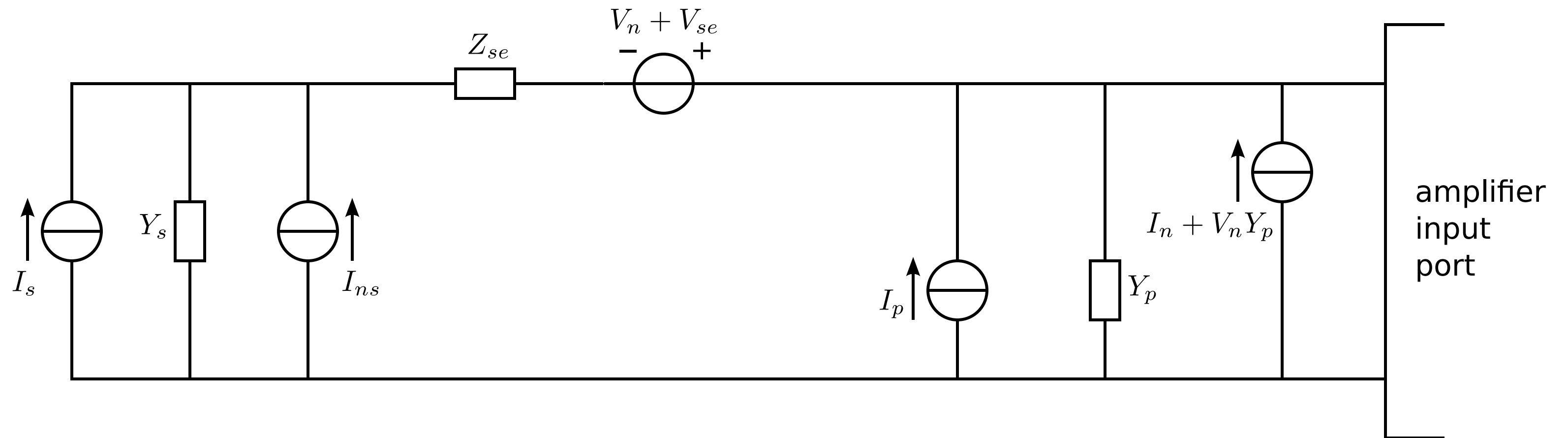
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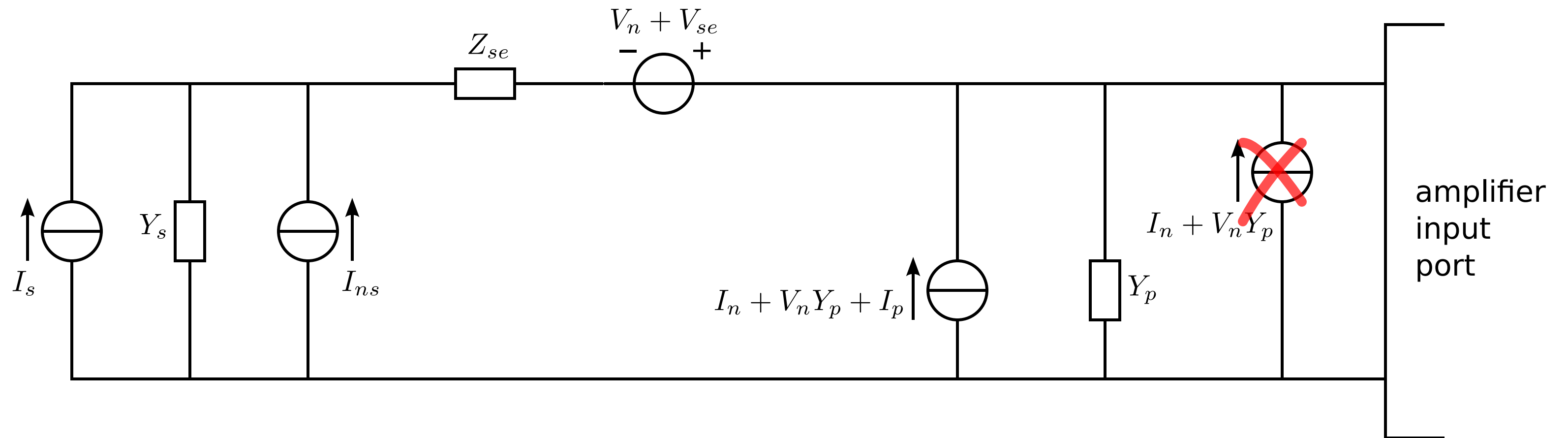
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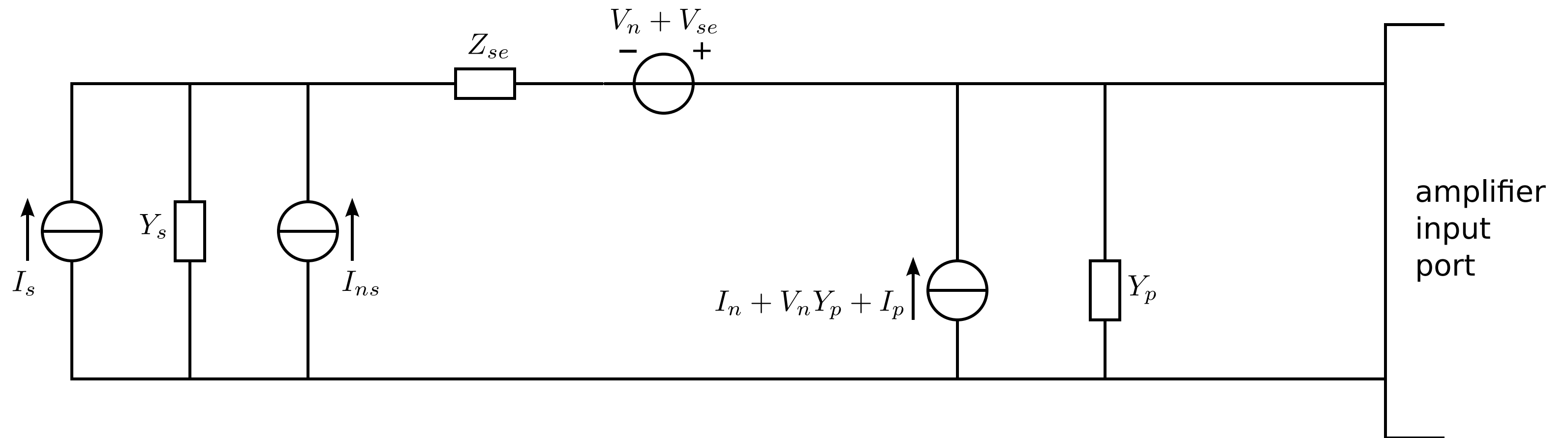
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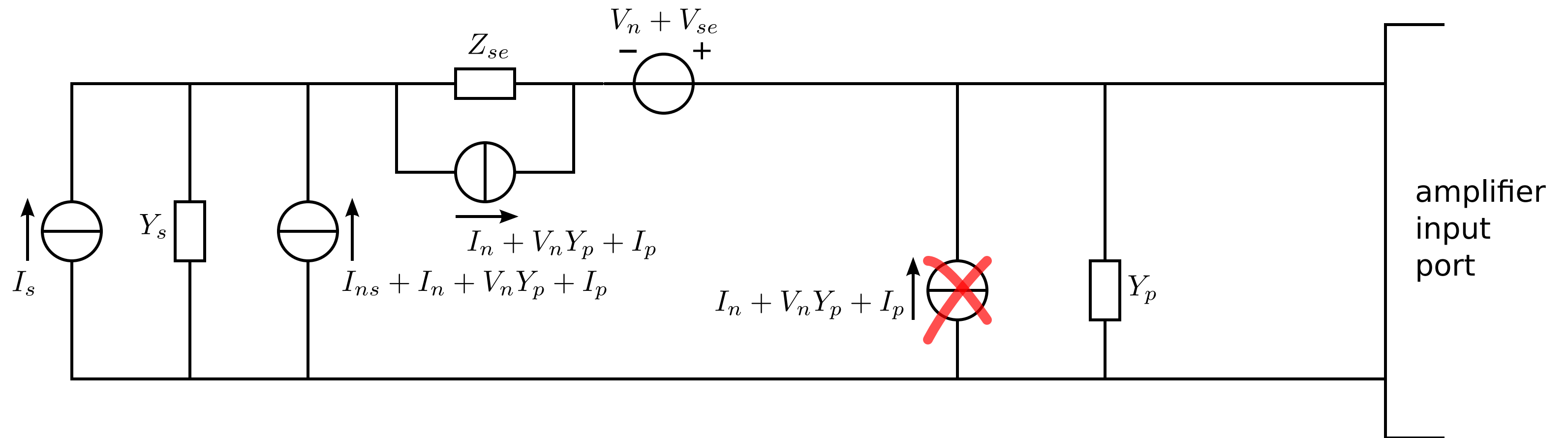
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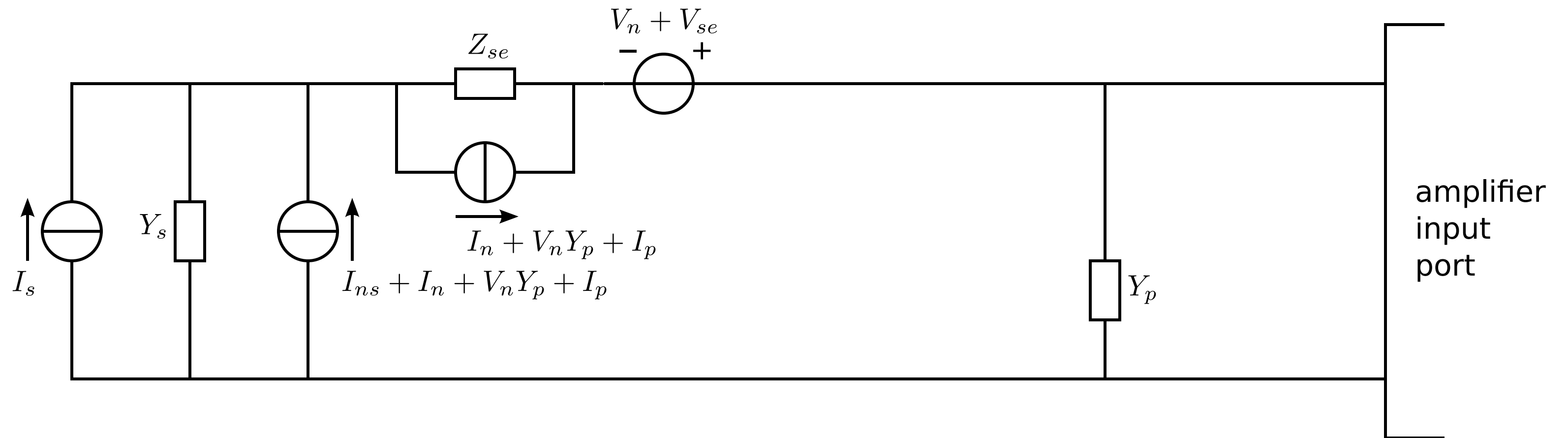


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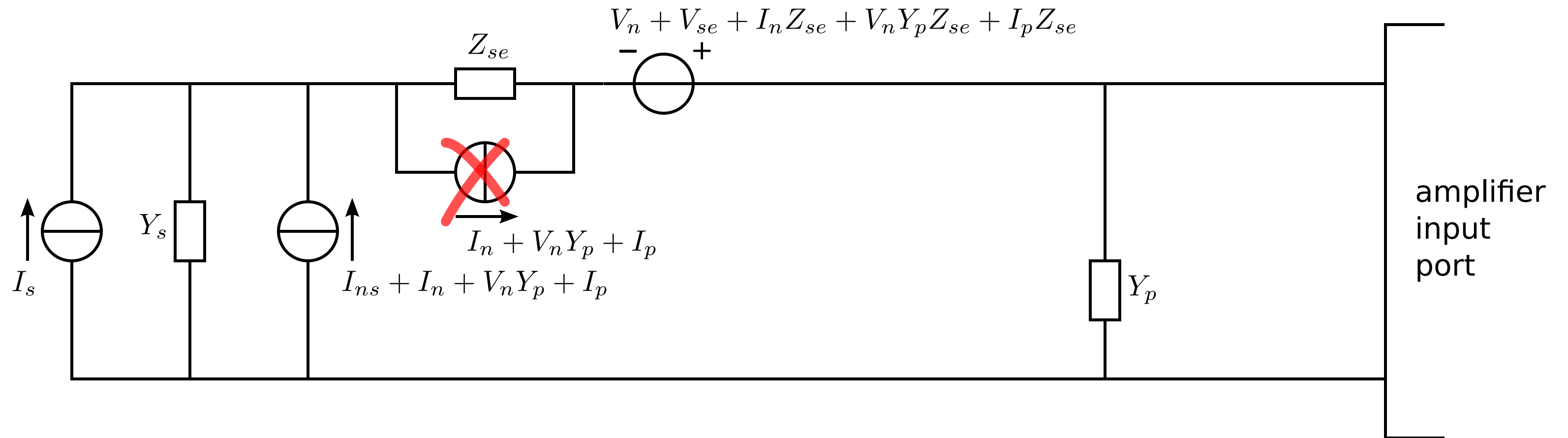




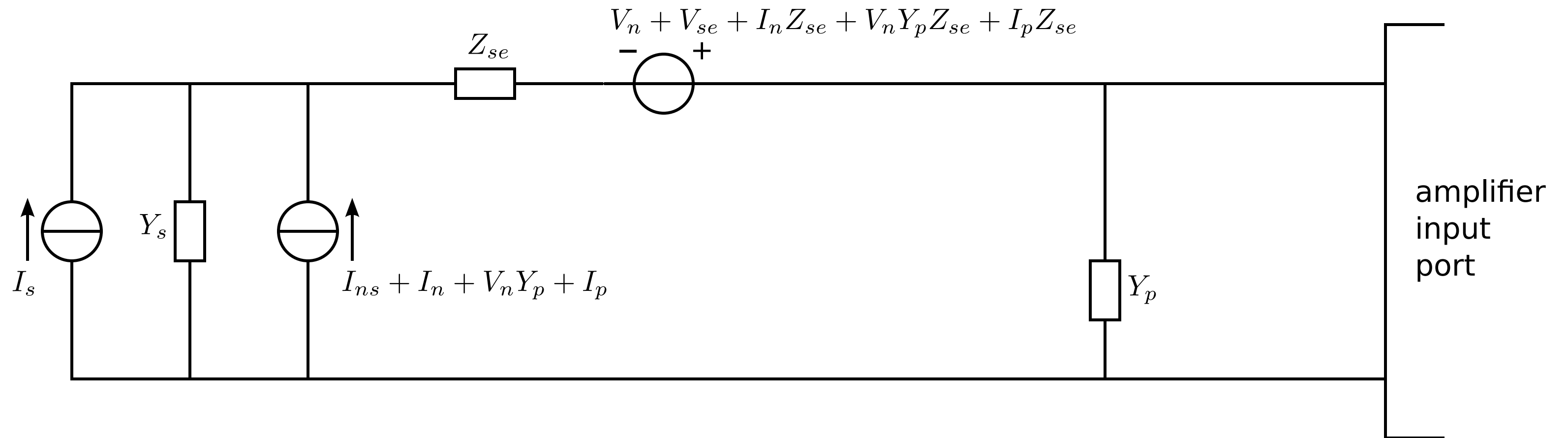
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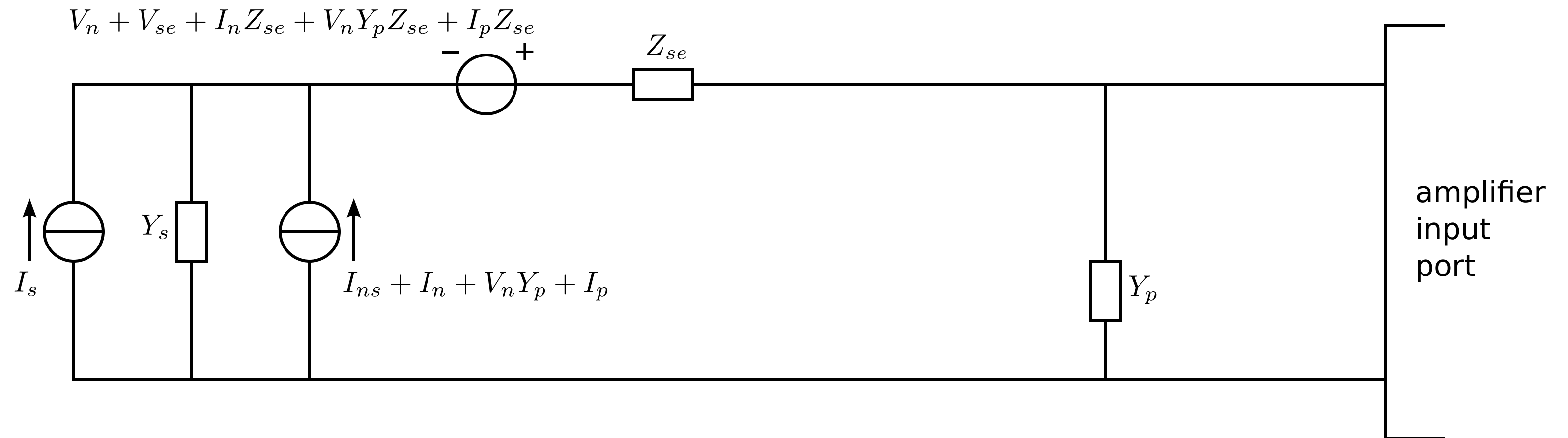
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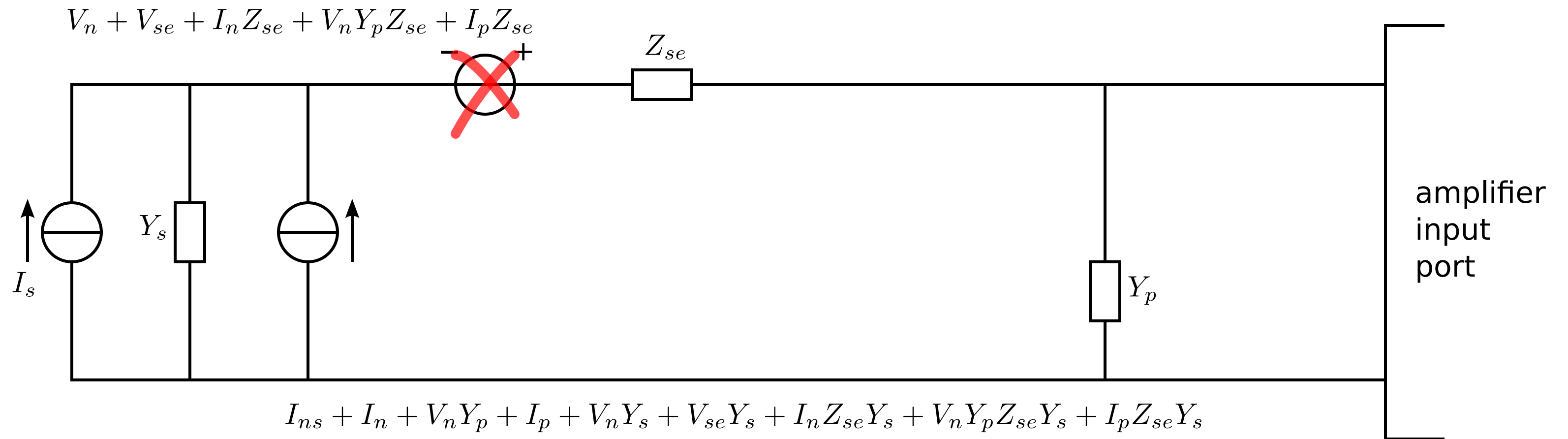
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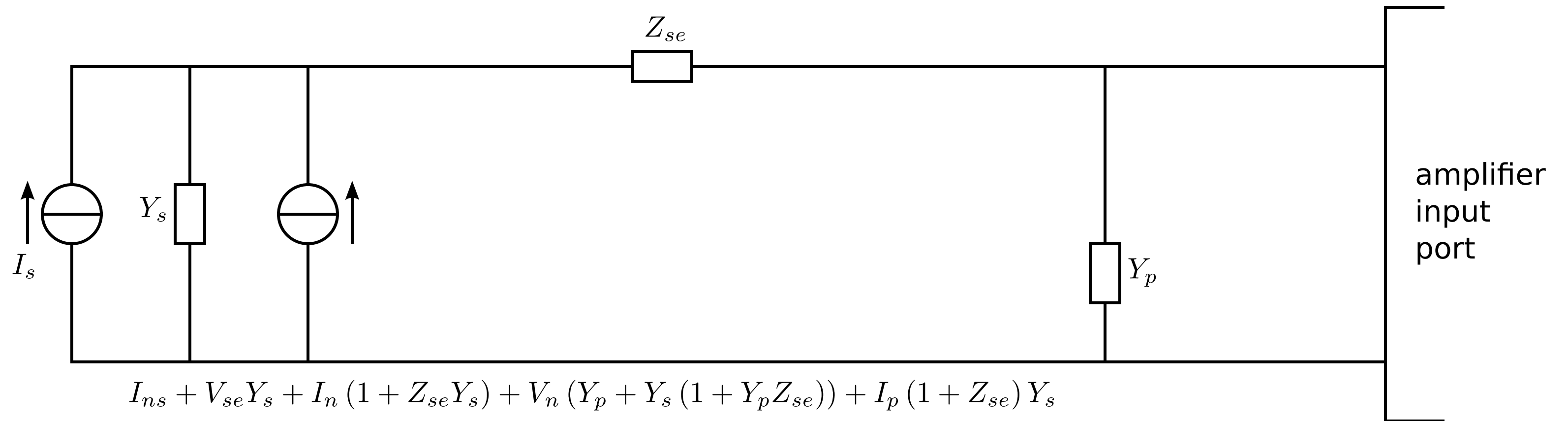
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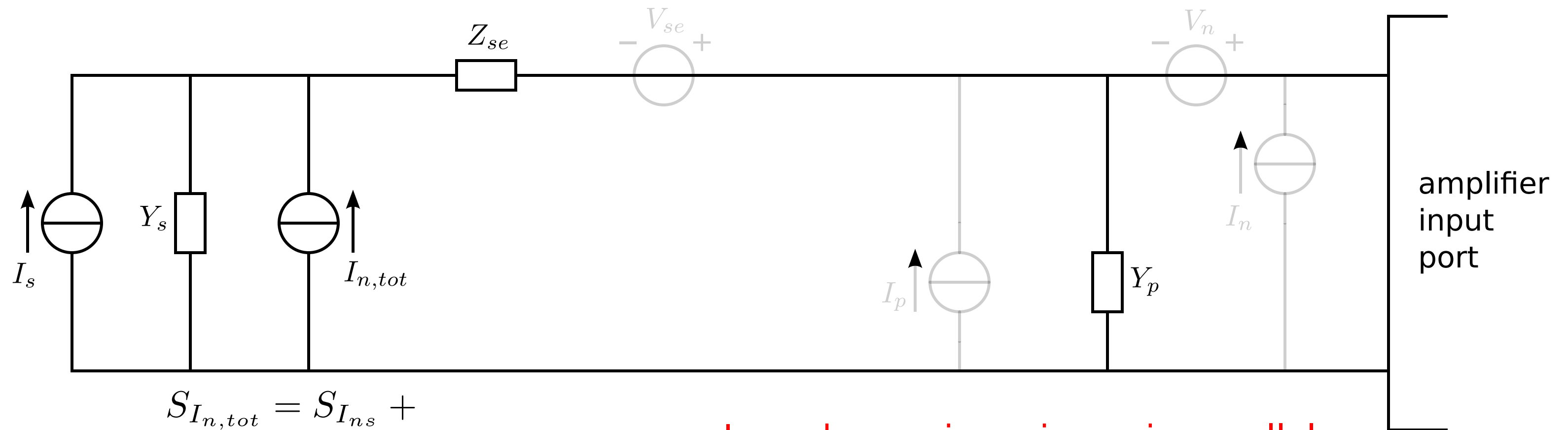
# Impedances in the signal path



# Impedances in the signal path



# Impedances in the signal path



$$\begin{aligned}
 S_{I_{n,tot}} = & S_{I_{ns}} + \\
 & + S_{V_{se}} |Y_s|^2 + \\
 & + S_{I_n} |1 + Z_{se} Y_s|^2 + \\
 & + S_{V_n} |Y_p + Y_s (1 + Y_p Z_{se})|^2 + \\
 & + S_{I_p} |1 + Z_{se} Y_s|^2
 \end{aligned}$$

Impedances in series or in parallel with the signal path increase the influence of noise sources further in the signal path

# Design conclusions



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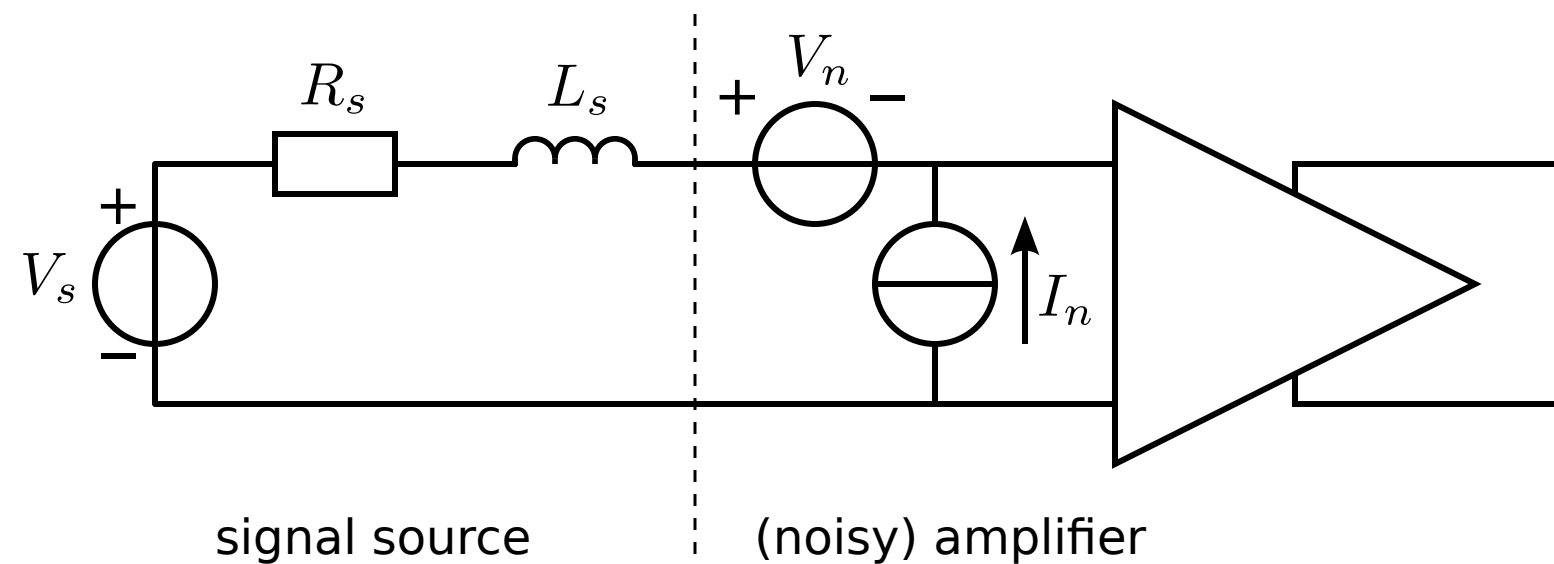
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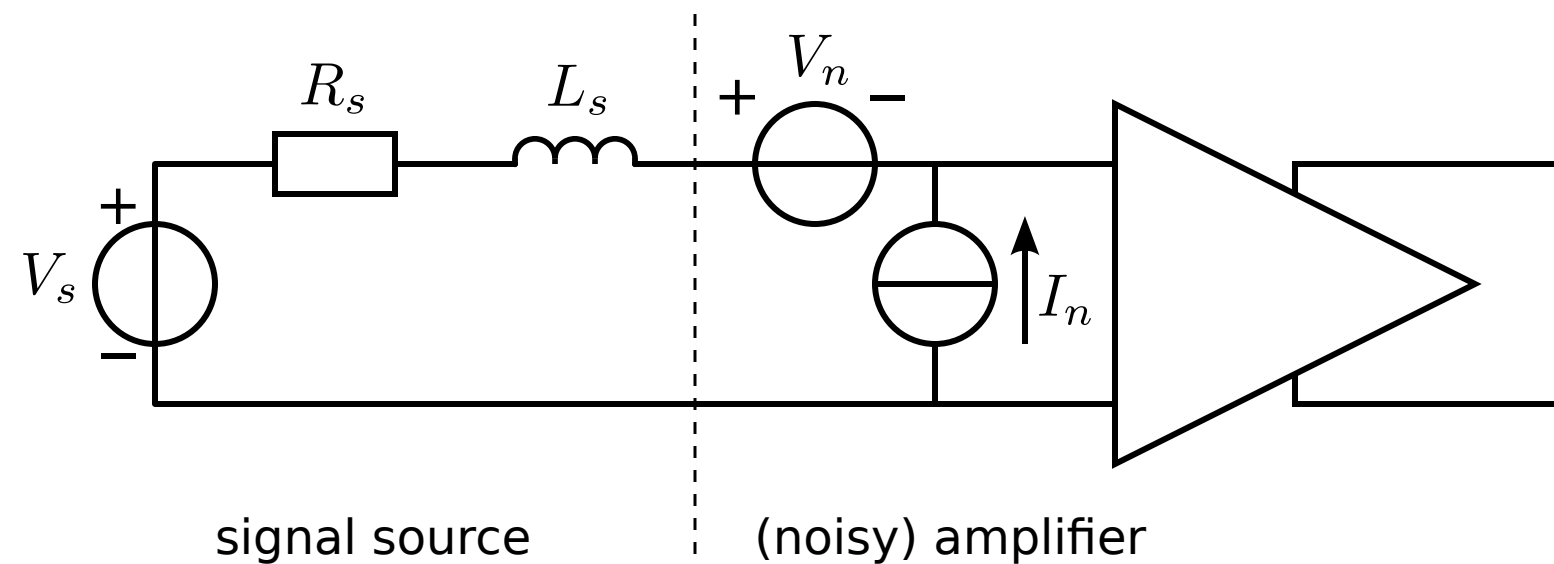
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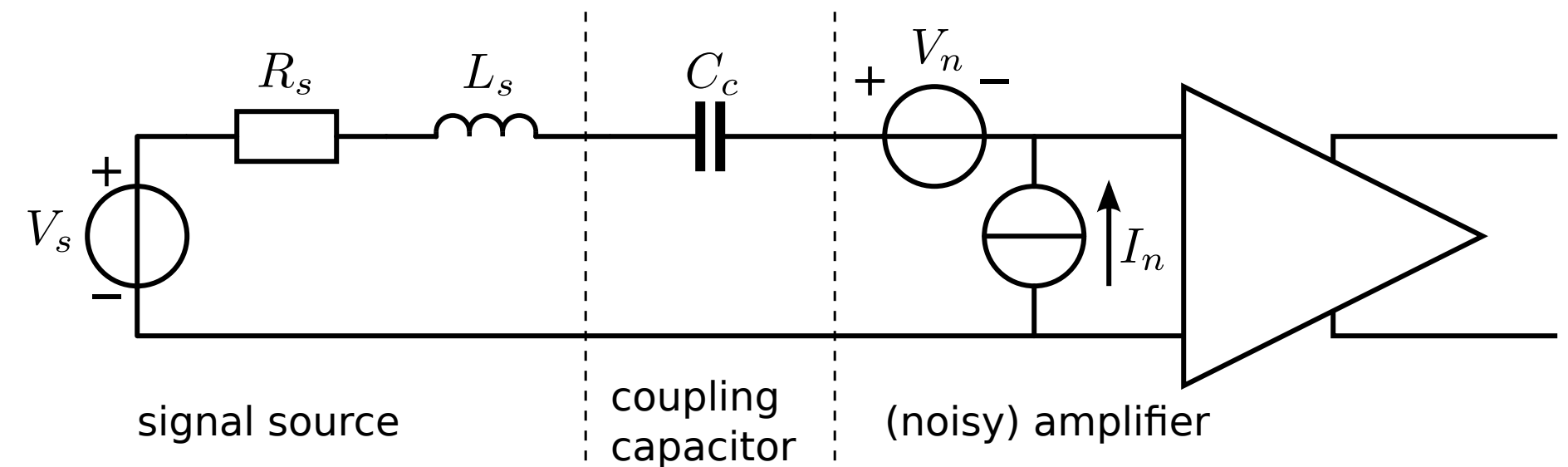
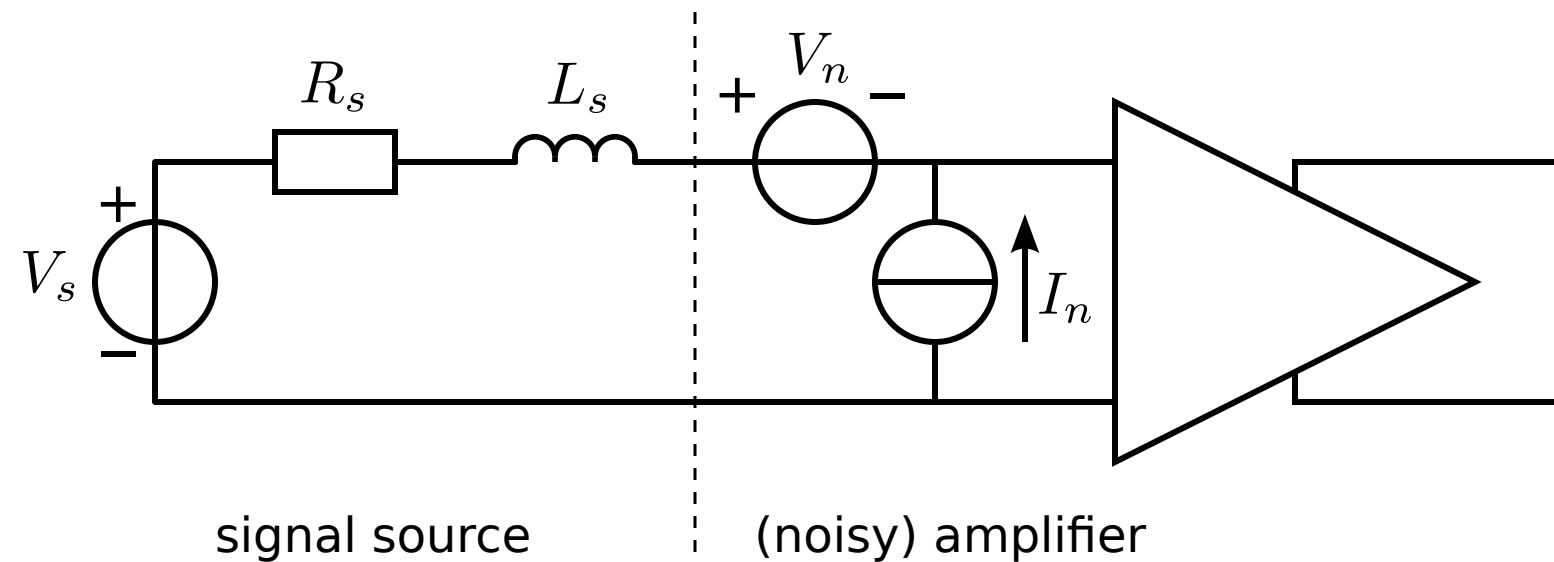
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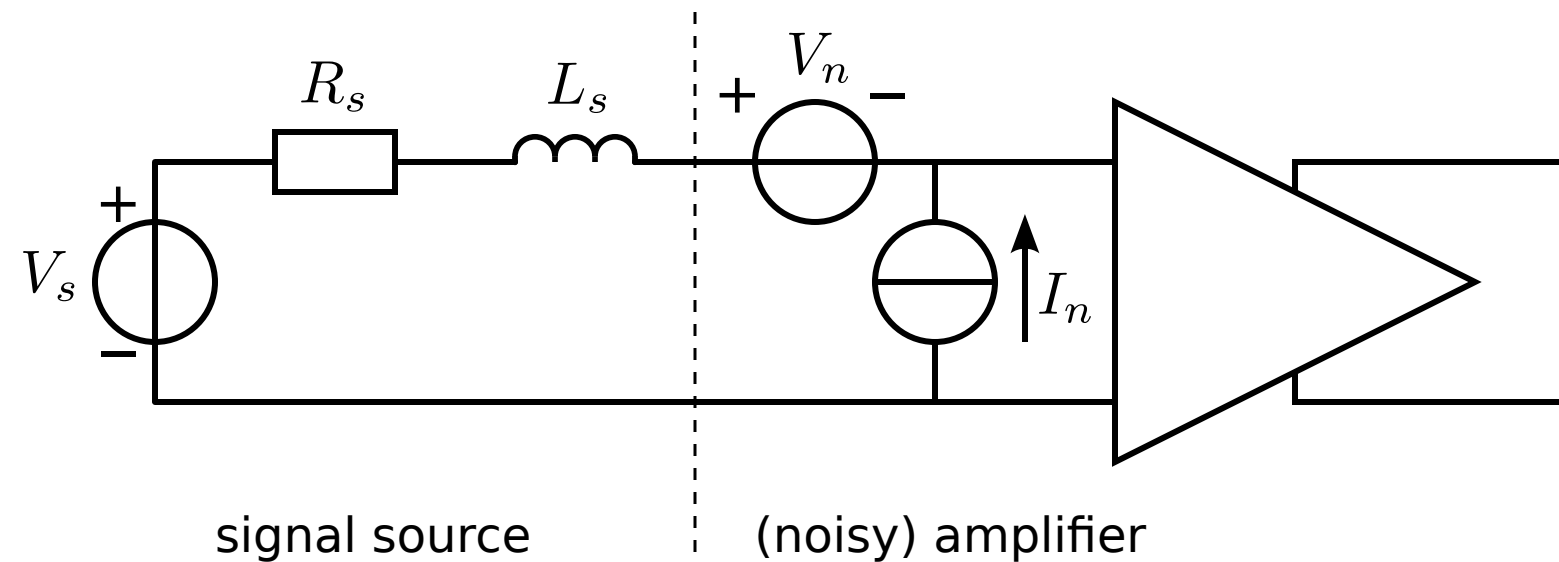
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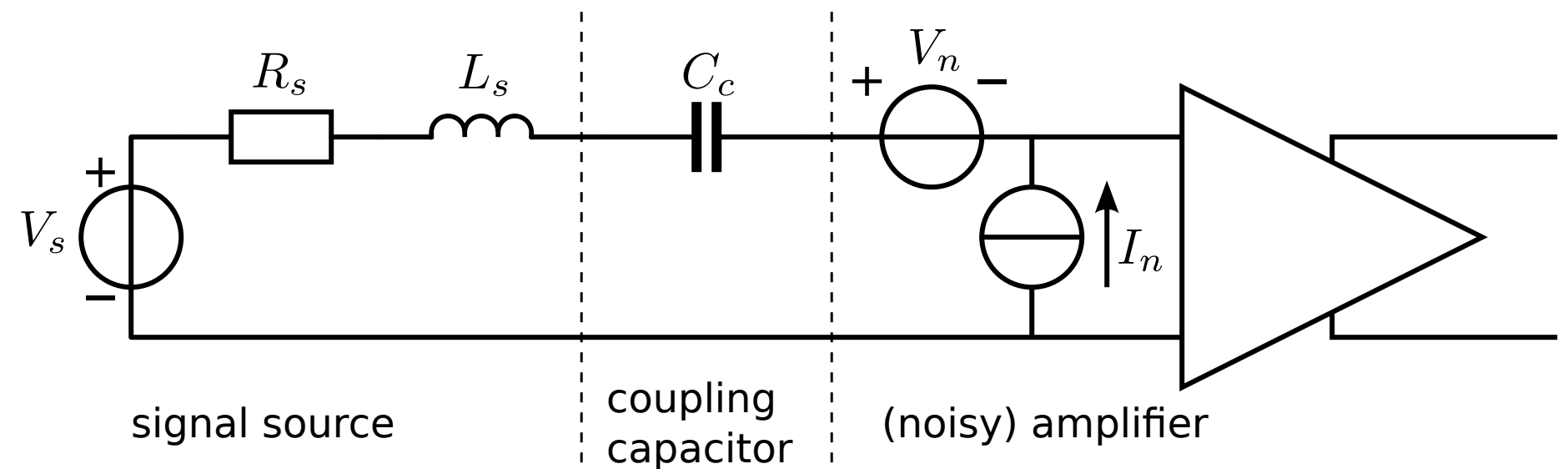
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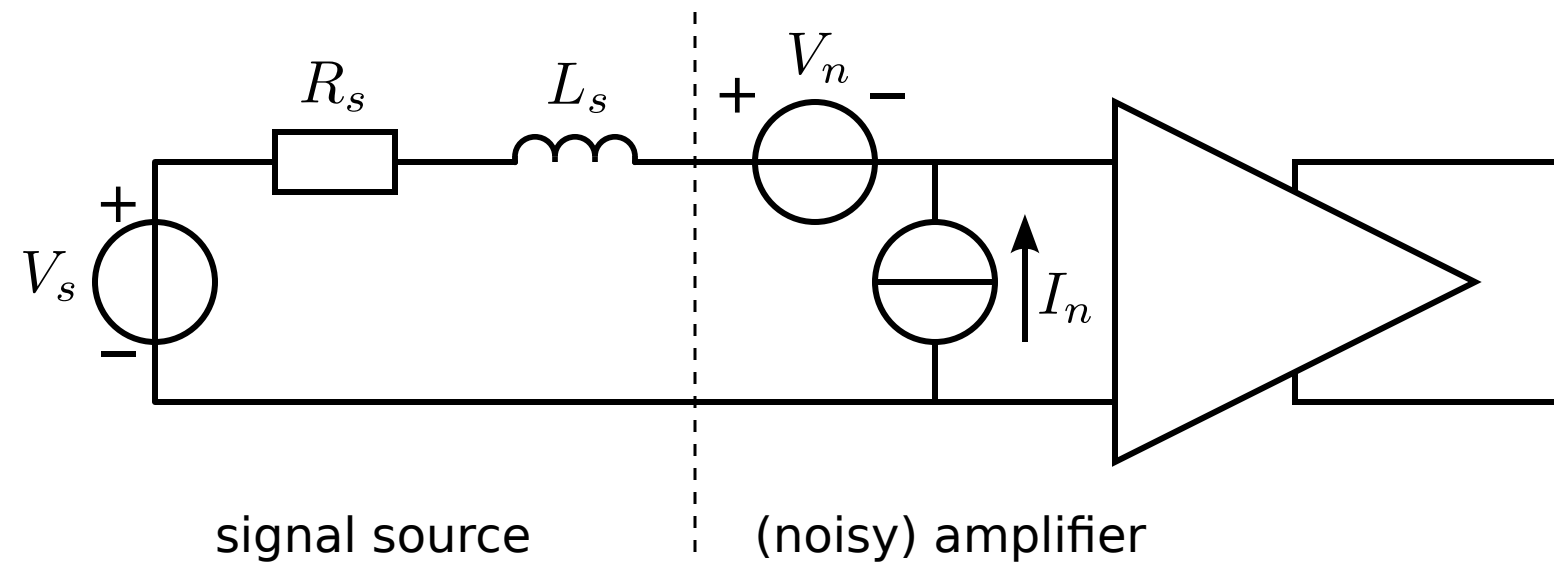
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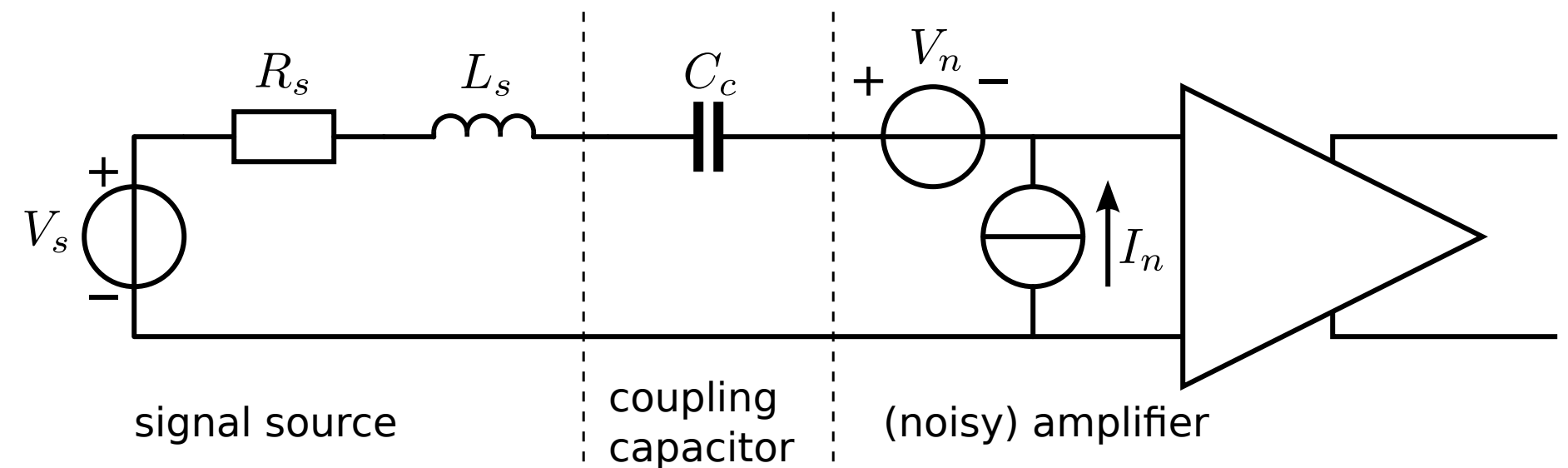
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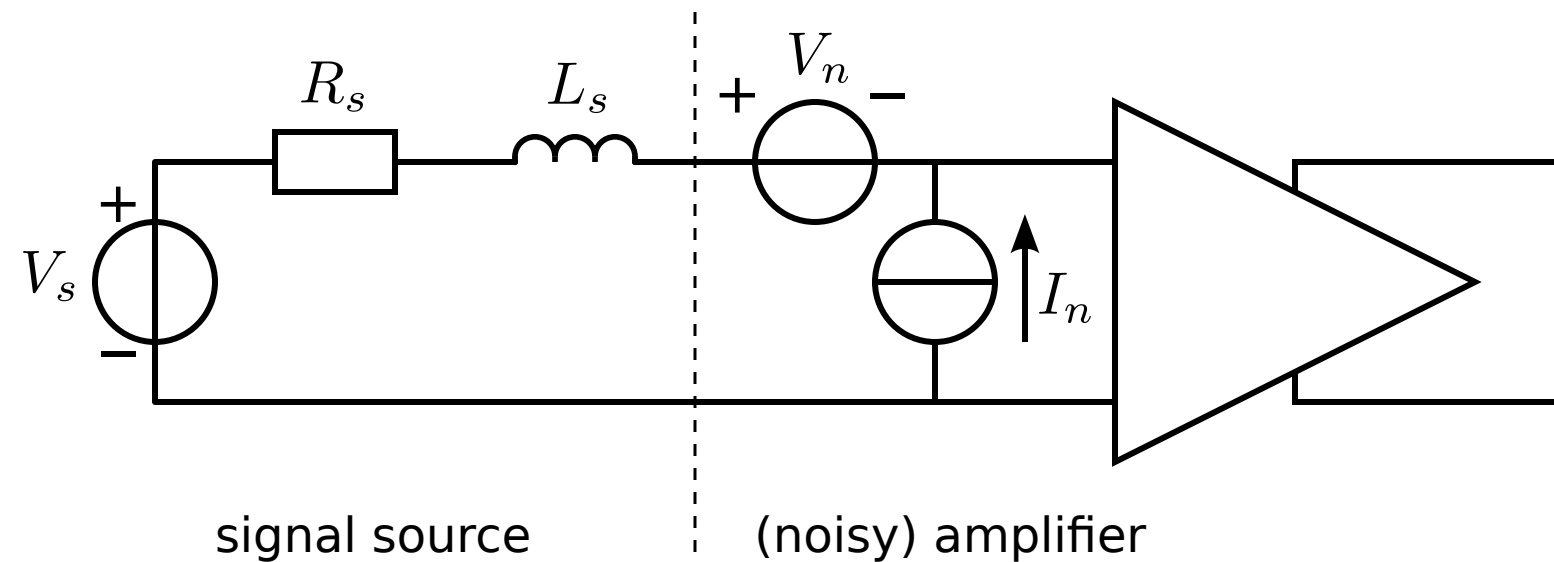
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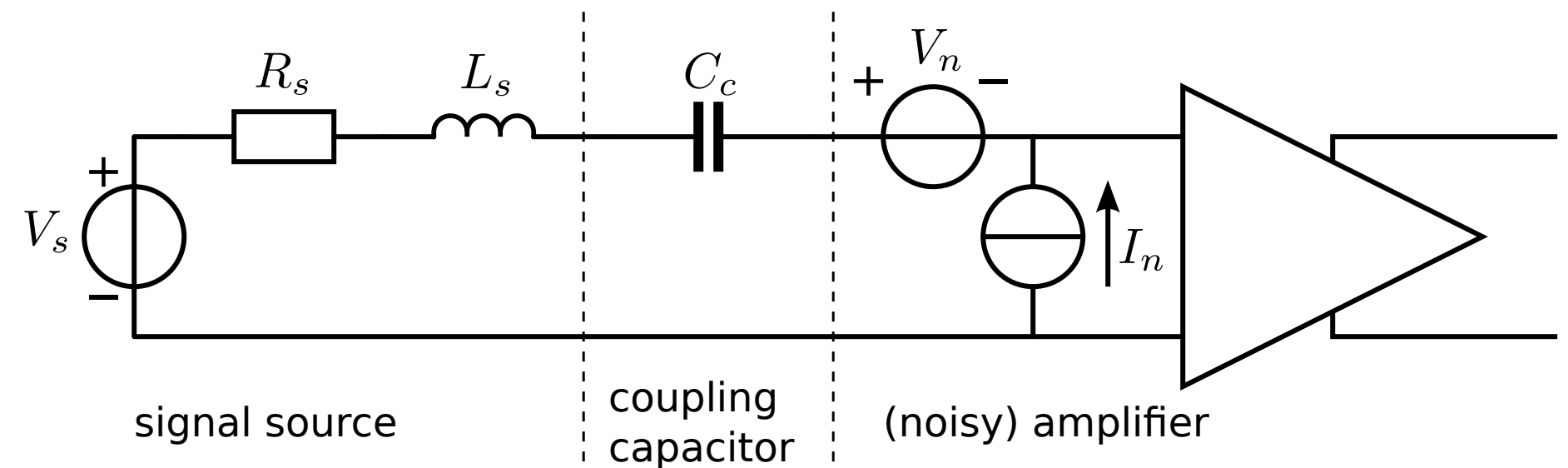
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