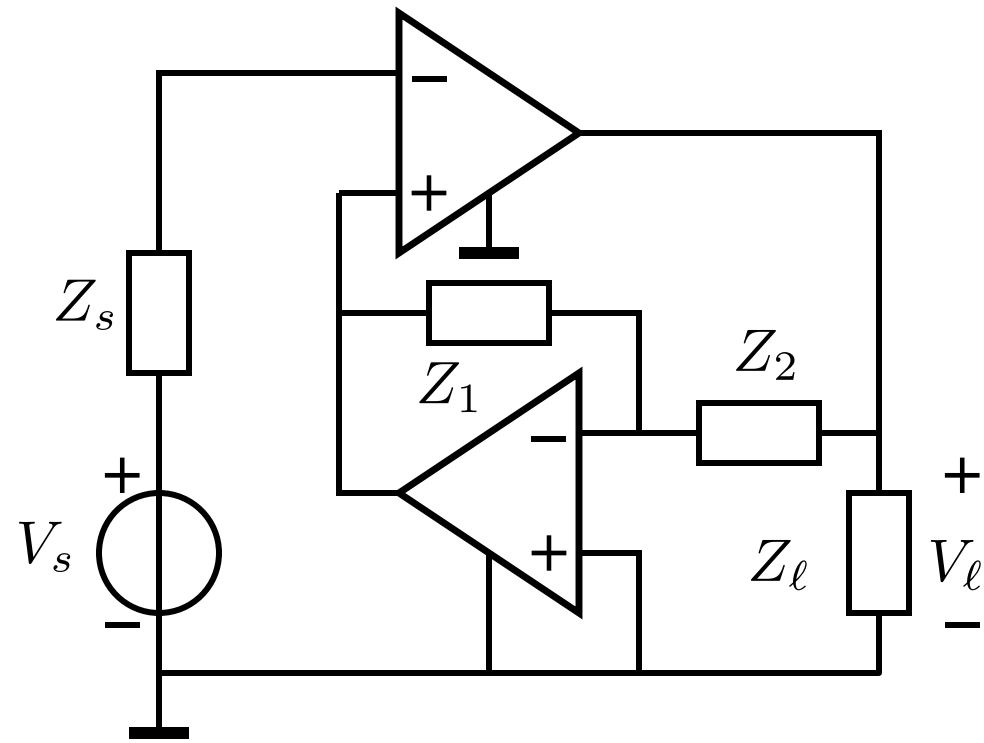


# Structured Electronic Design

## Multiple-nullor circuit synthesis

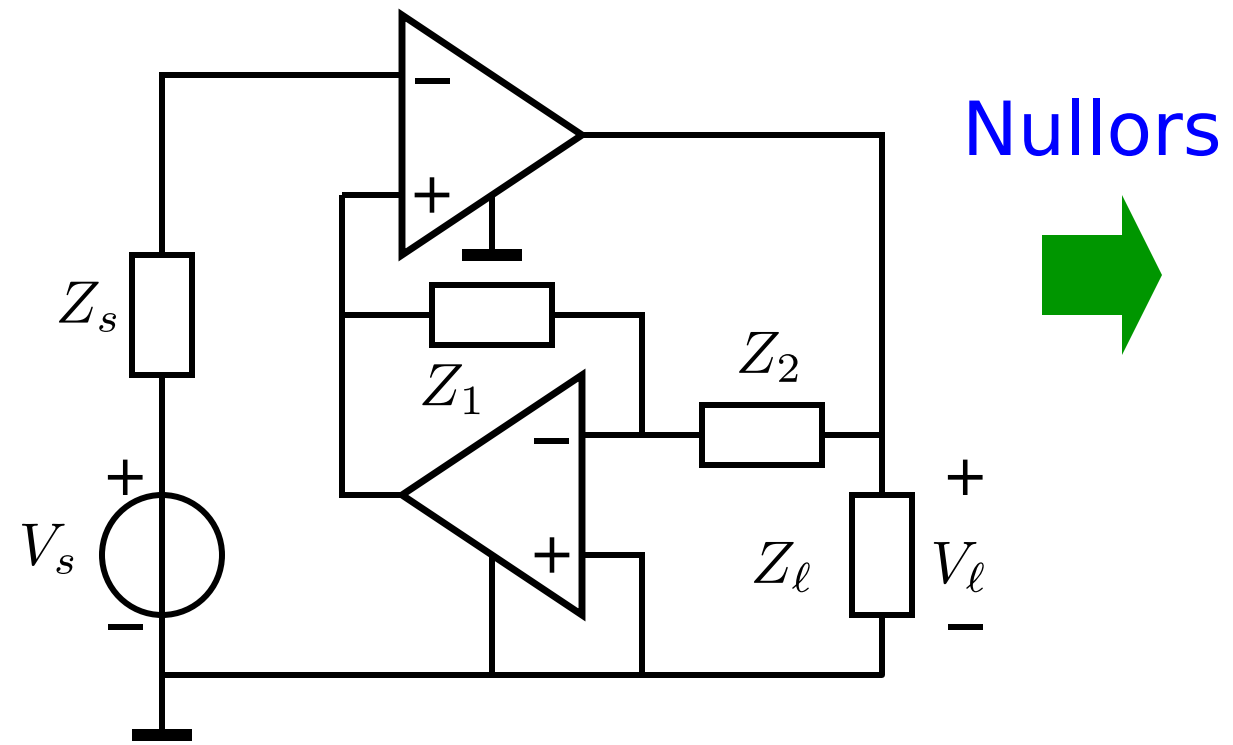
# Multiple nullor synthesis technique

# Multiple nullor synthesis technique



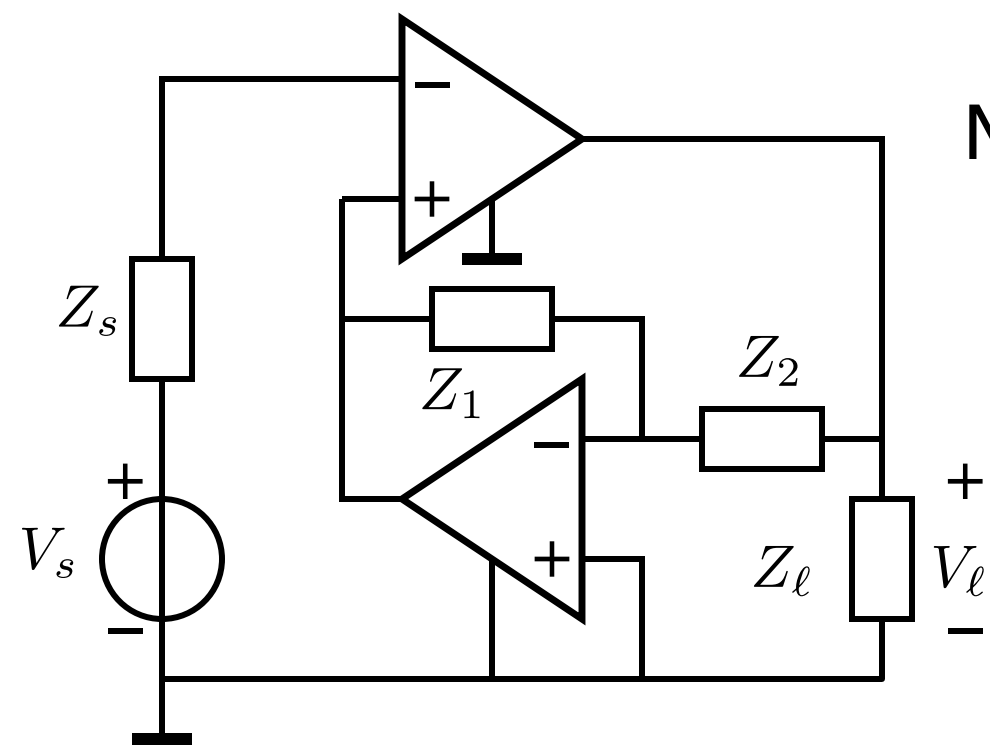
Inverting  
voltage amplifier

# Multiple nullor synthesis technique

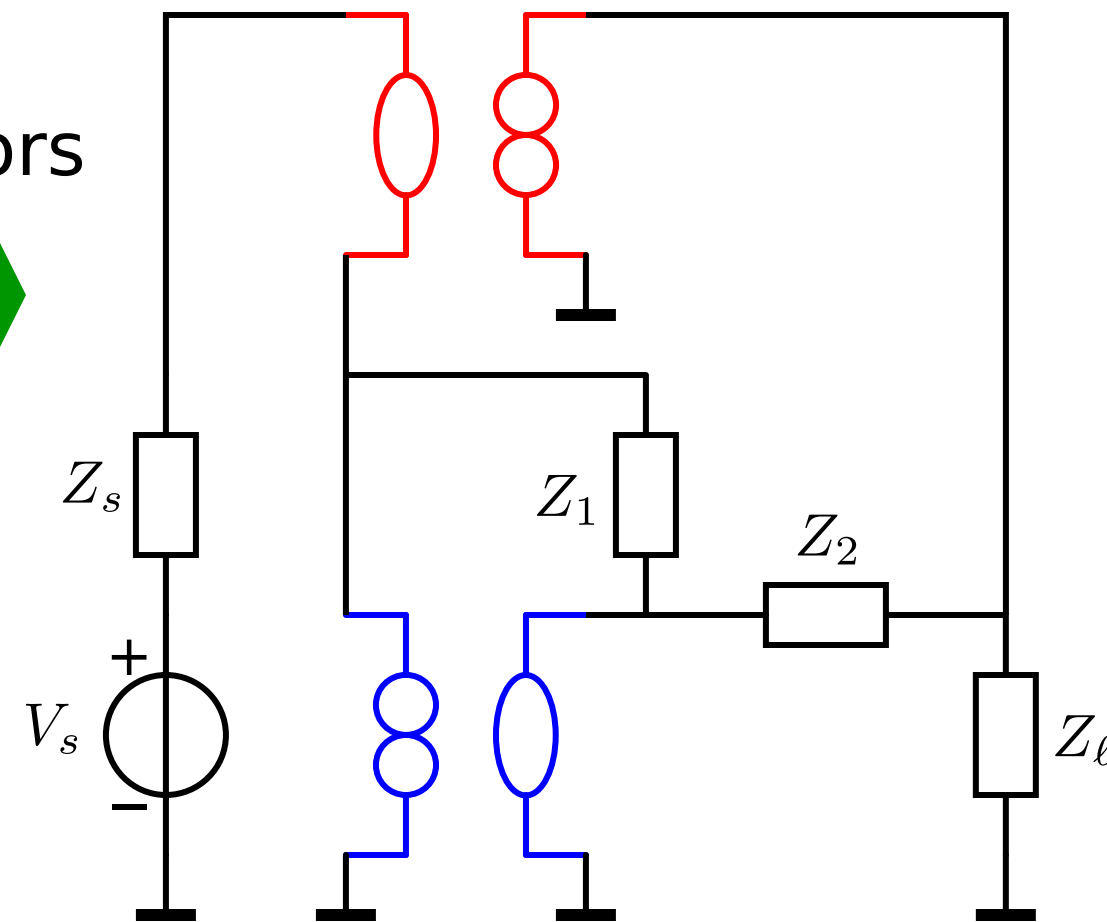
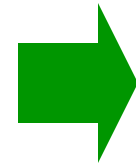


Inverting  
voltage amplifier

# Multiple nullor synthesis technique

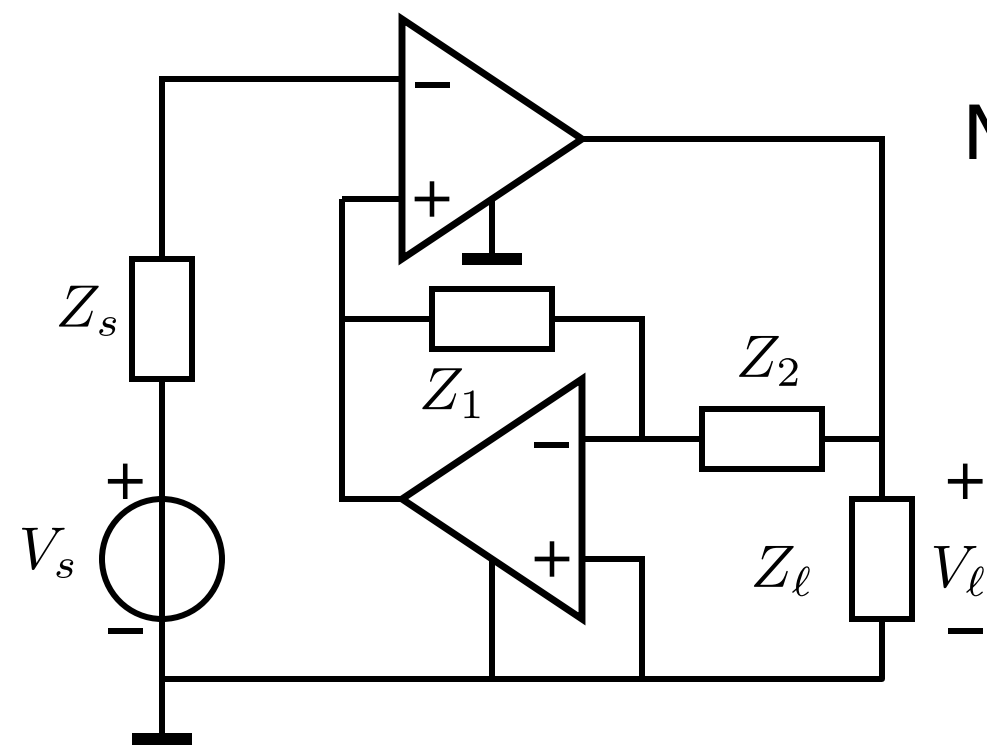


Nullors

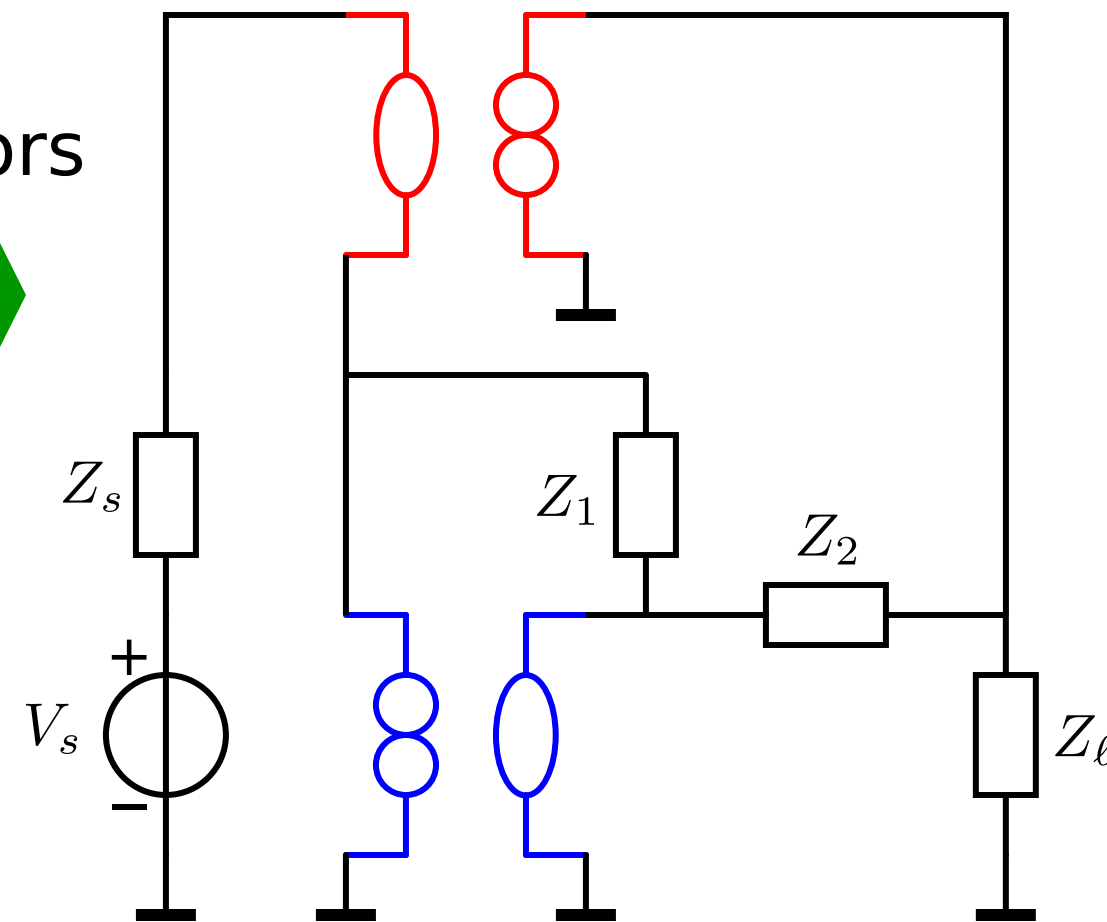
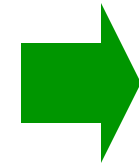


Inverting  
voltage amplifier

# Multiple nullor synthesis technique

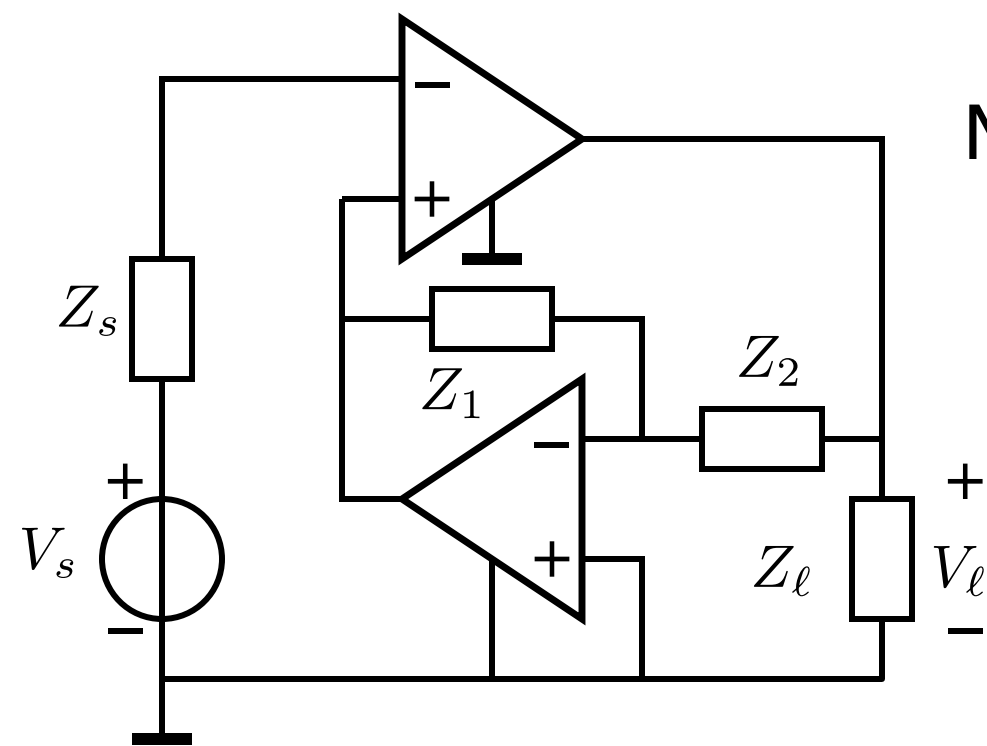


Nullors



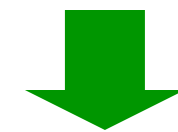
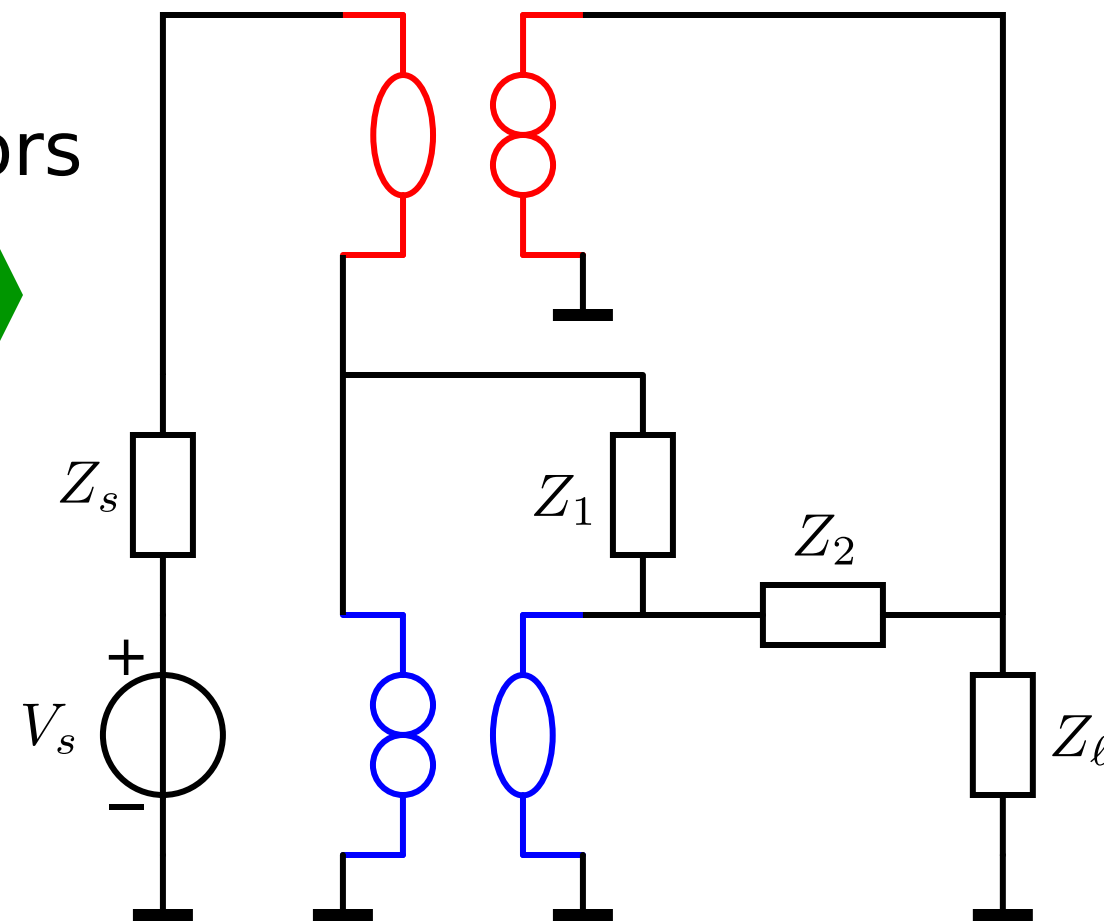
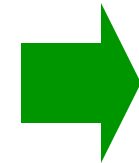
Nulator and norator  
are independent  
network elements

# Multiple nullor synthesis technique



Inverting  
voltage amplifier

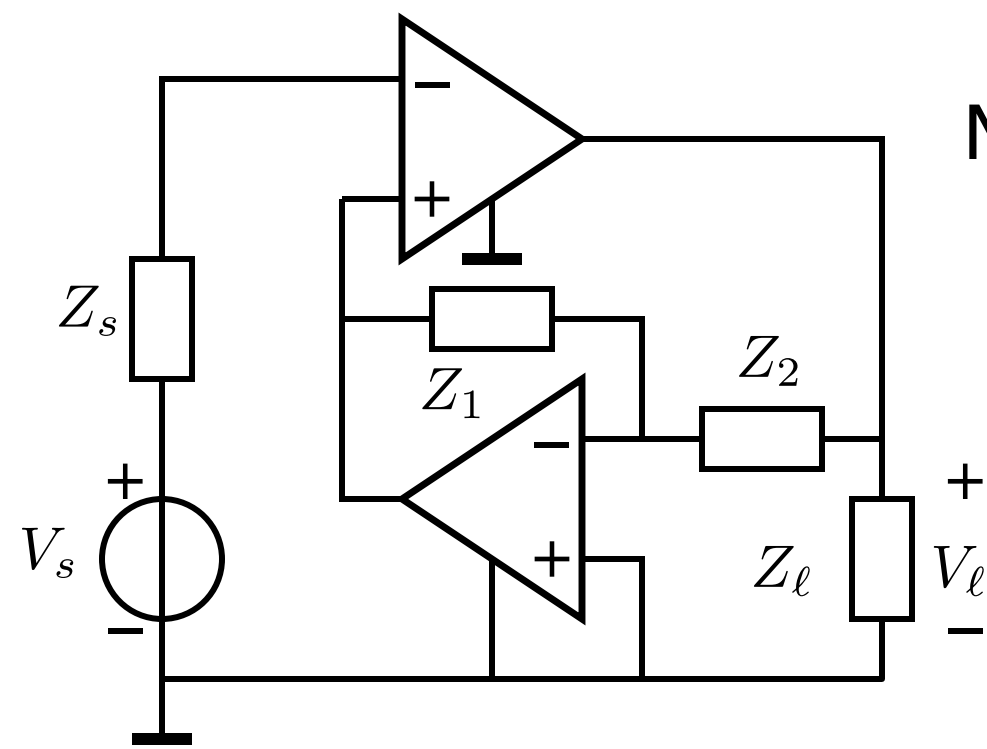
Nullors



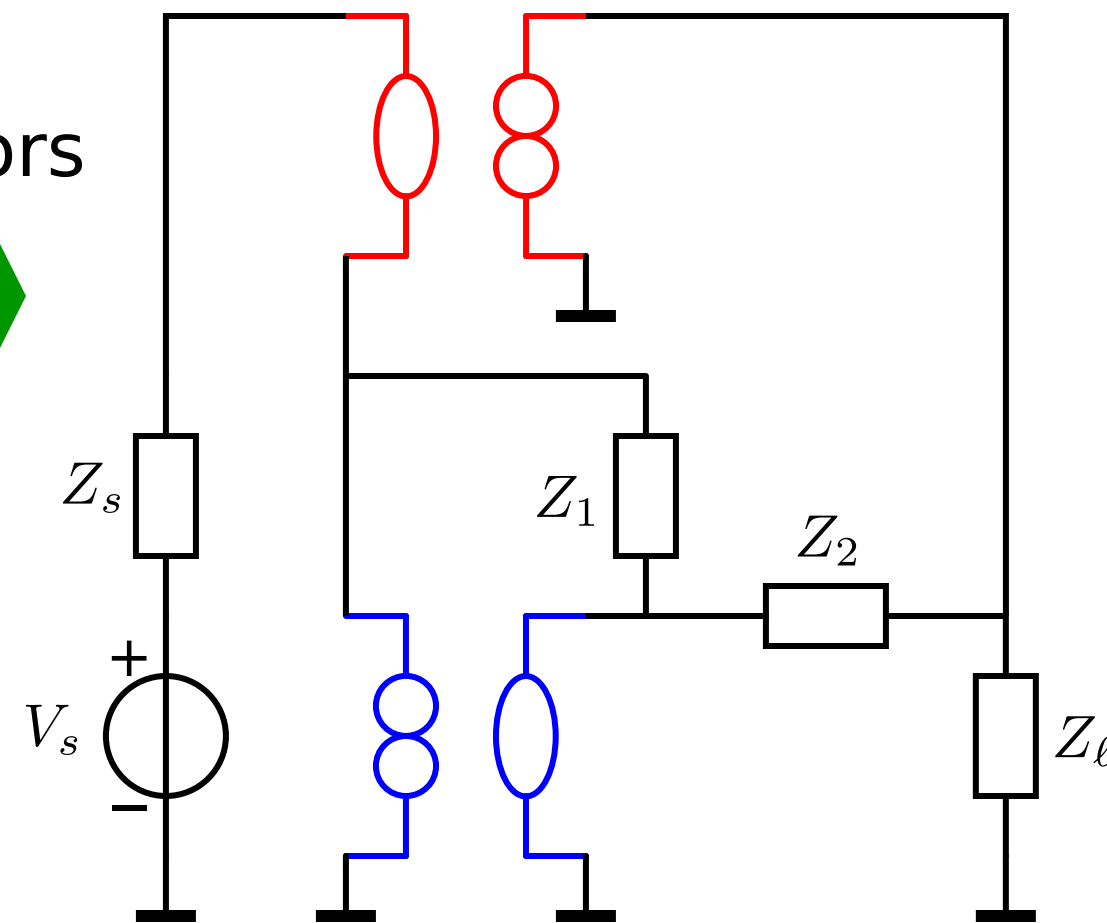
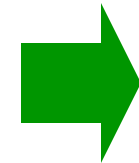
Pair differently

Nulator and norator  
are independent  
network elements

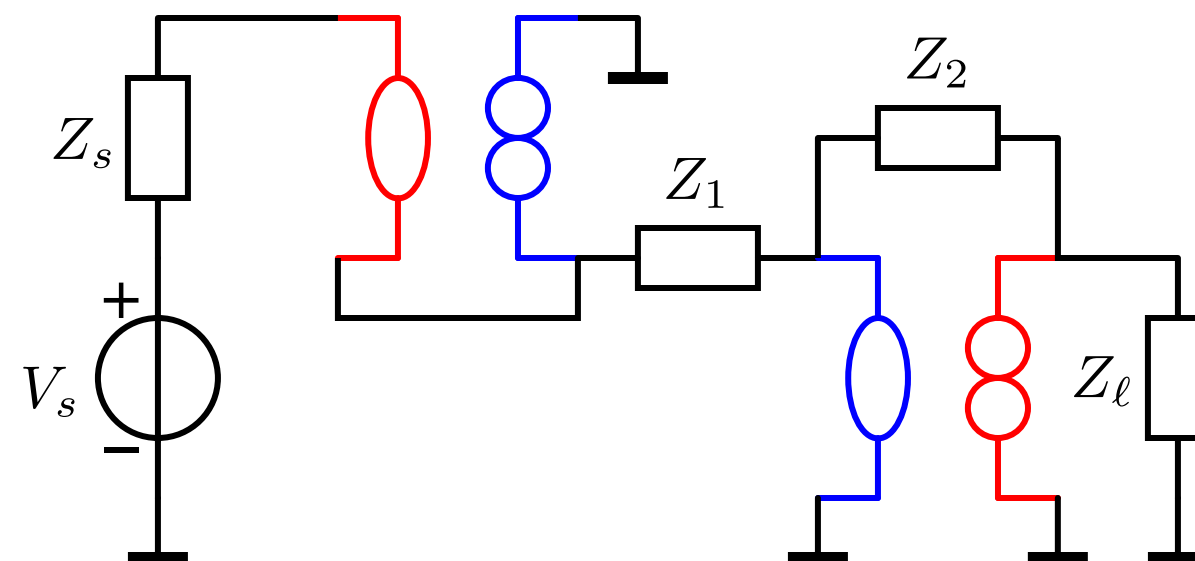
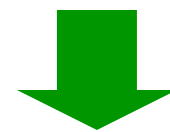
# Multiple nullor synthesis technique



Nullors



Pair differently

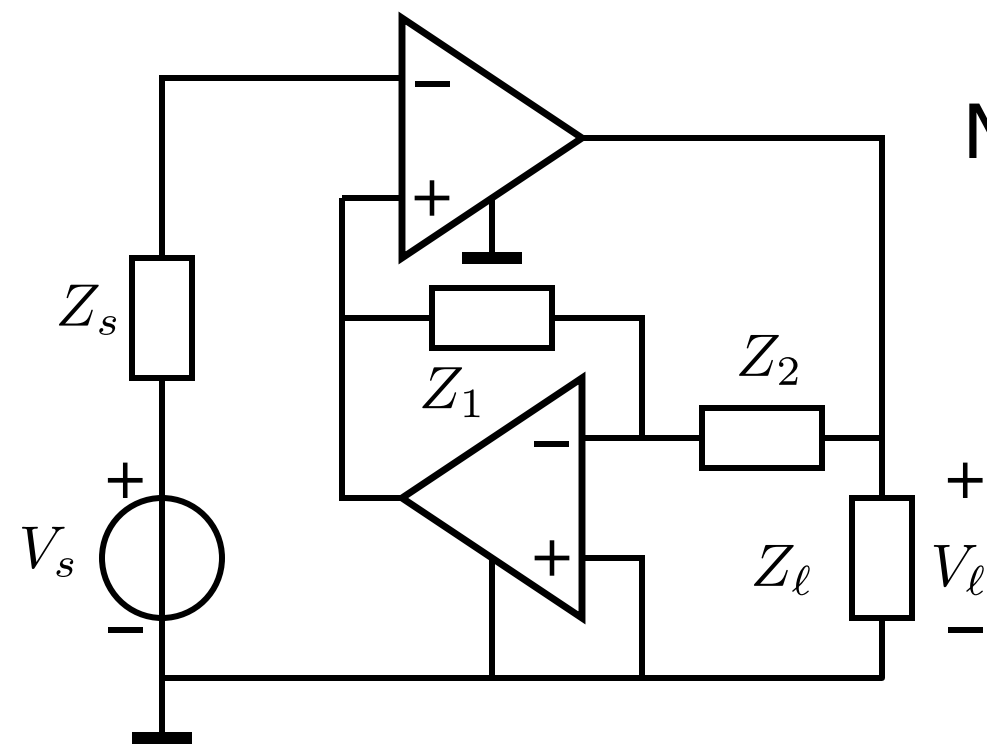


Inverting  
voltage amplifier

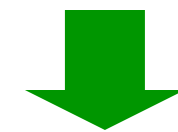
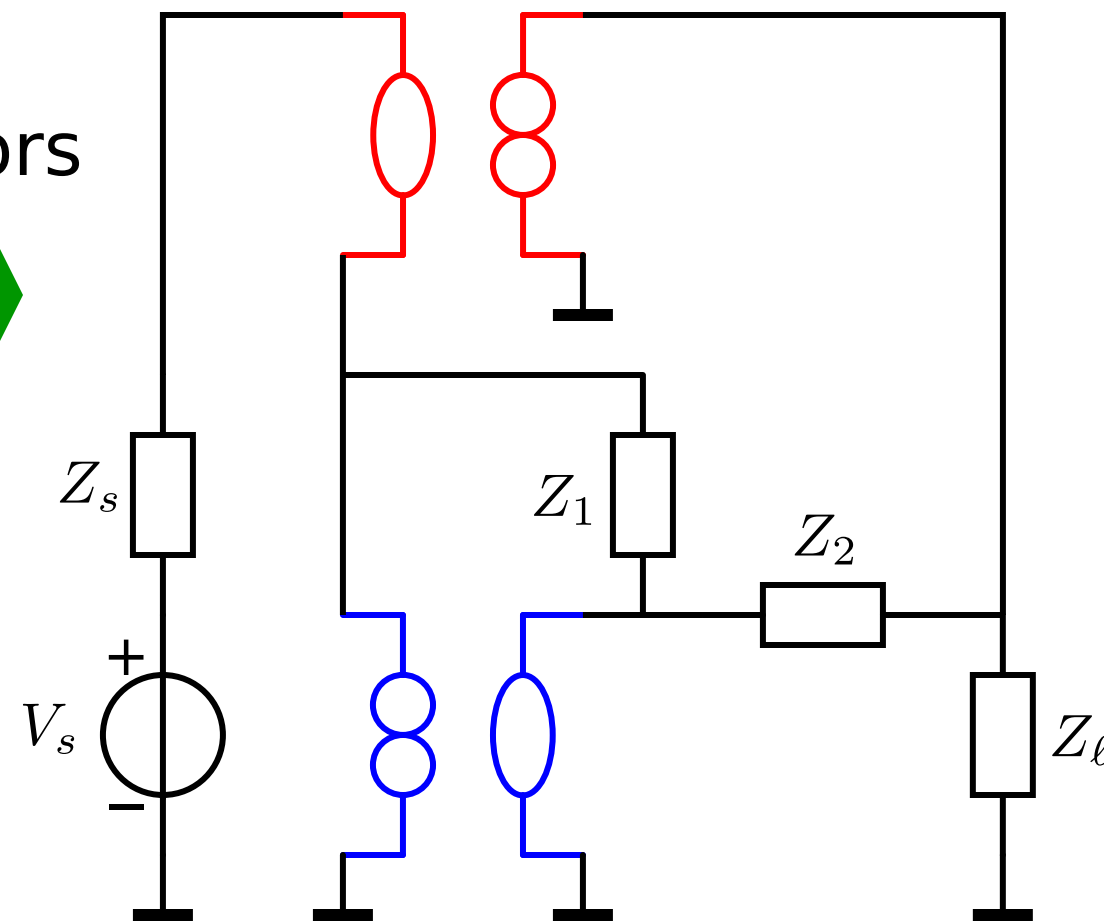
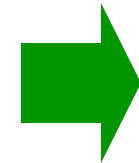
Nulator and norator  
are independent  
network elements



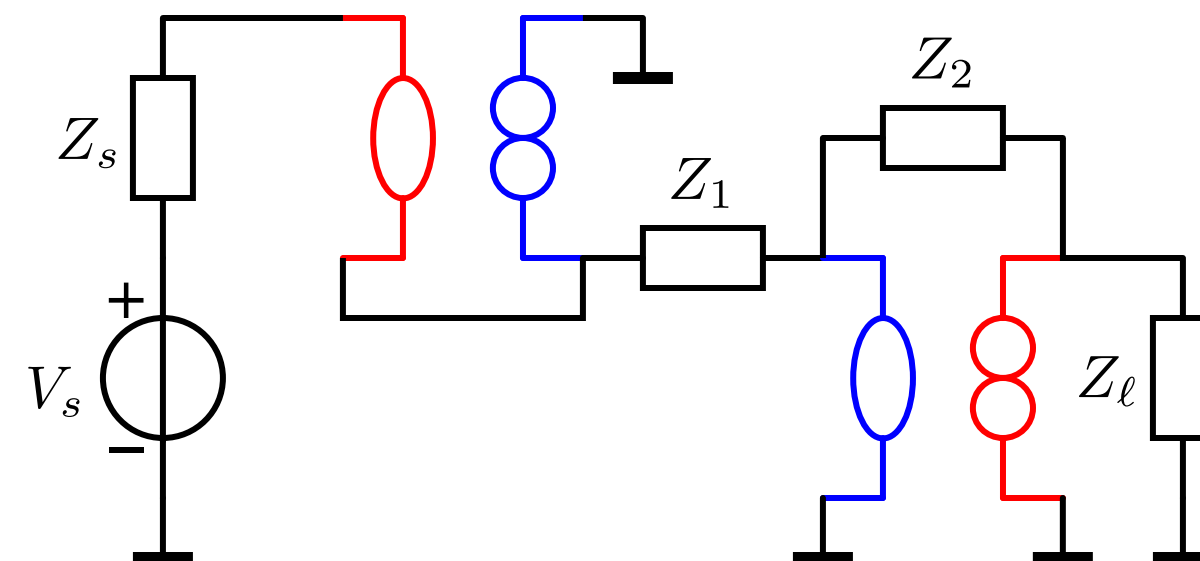
# Multiple nullor synthesis technique



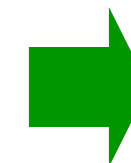
Nullors



Pair differently

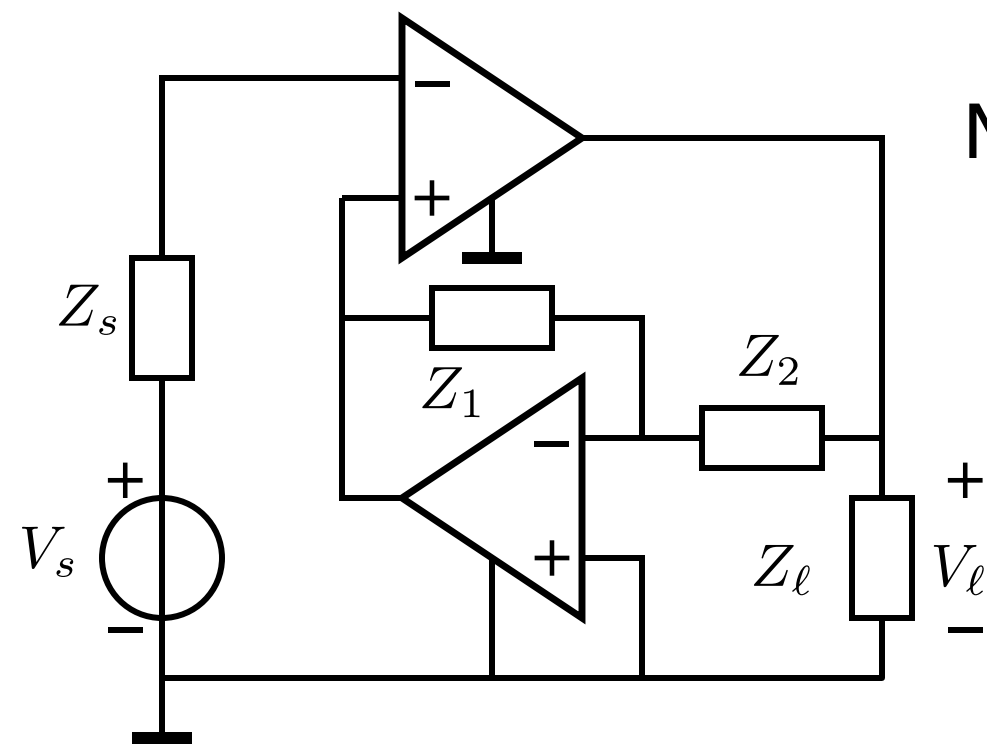


Another  
inverting  
voltage  
amplifier



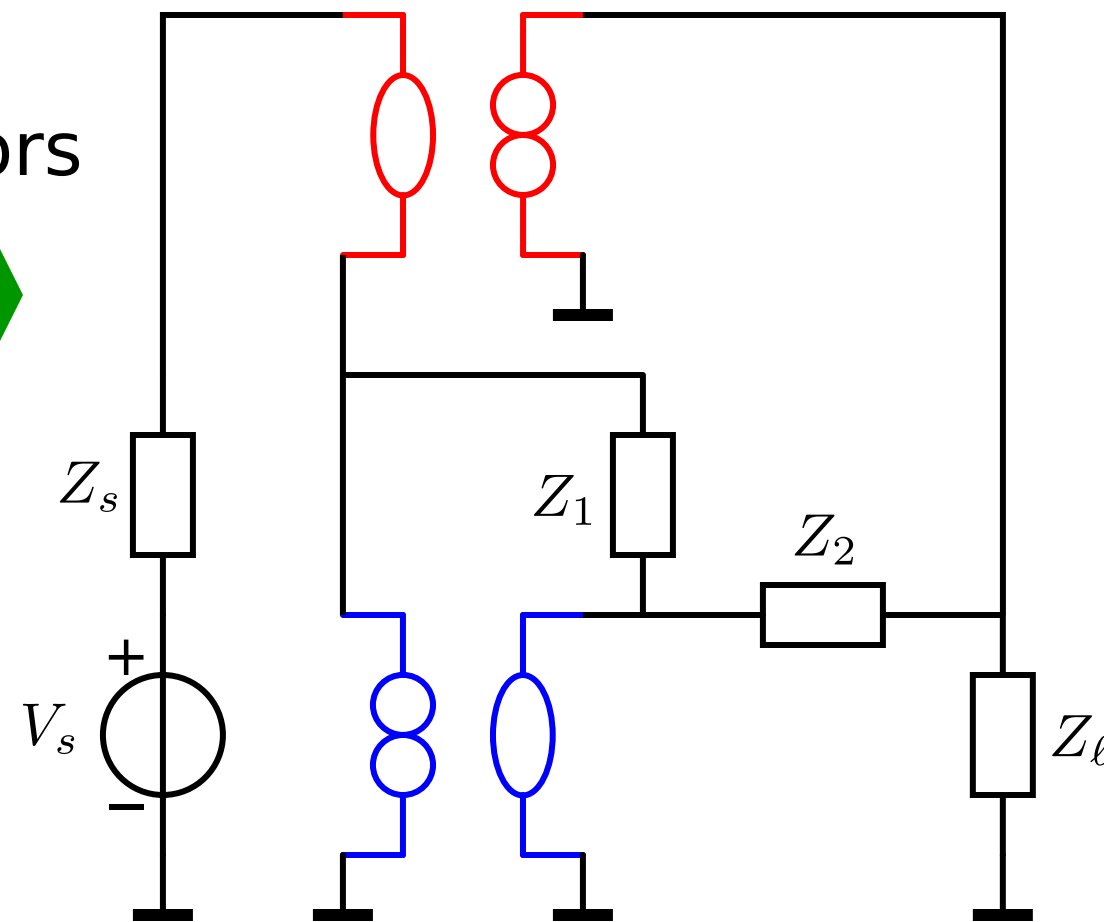
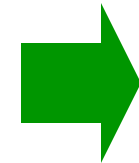
Nulator and norator  
are independent  
network elements

# Multiple nullor synthesis technique



Inverting voltage amplifier

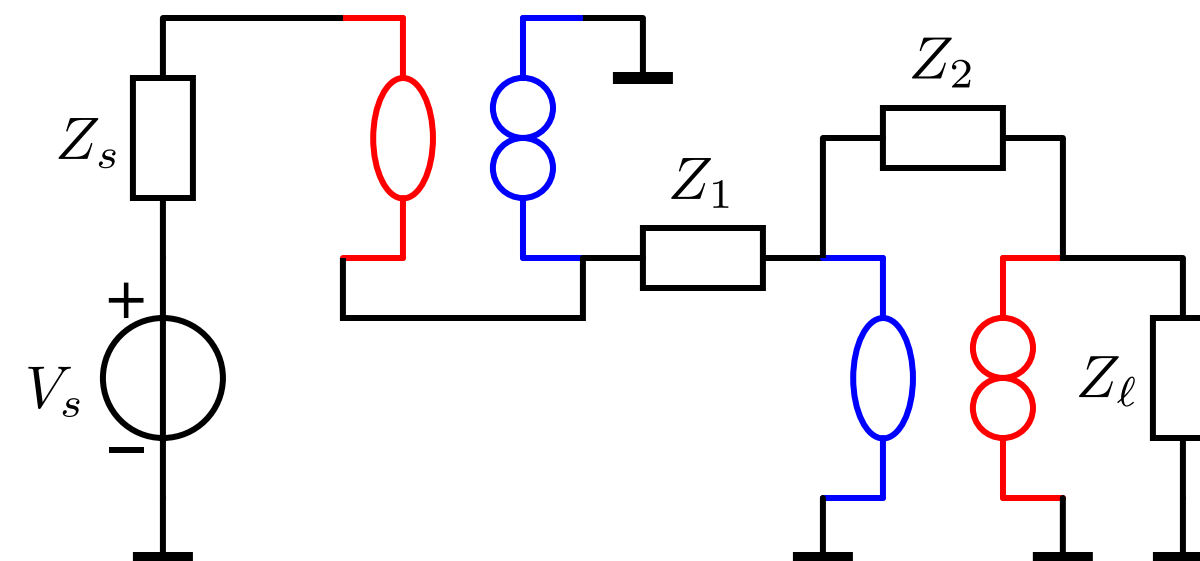
Nullors



Pair differently



Nulator and norator are independent network elements



Another inverting voltage amplifier

