

# Structured Electronic Design

## Analysis and budgeting of biasing errors

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**Simplifications:**  $\frac{1}{1+\delta} \approx 1 - \delta; \delta \ll 1$

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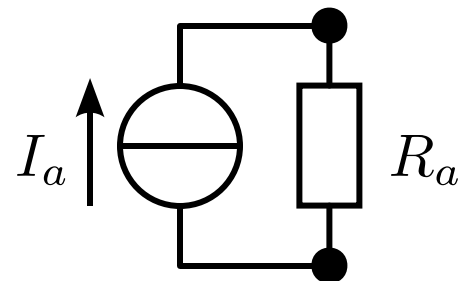
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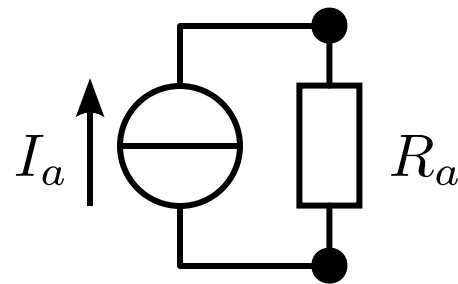
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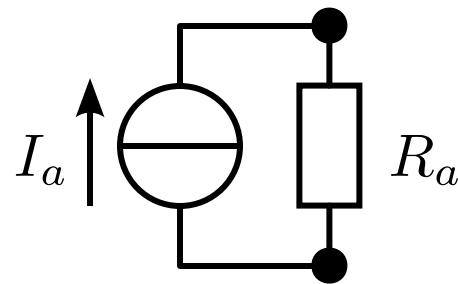
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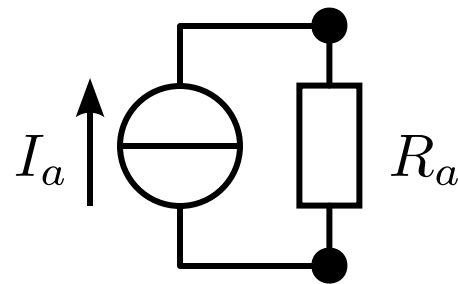
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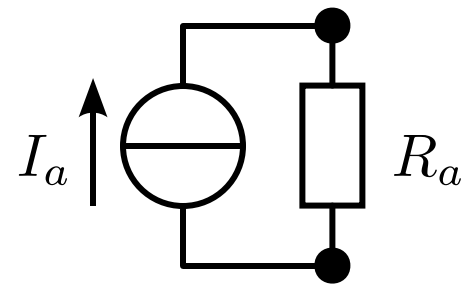
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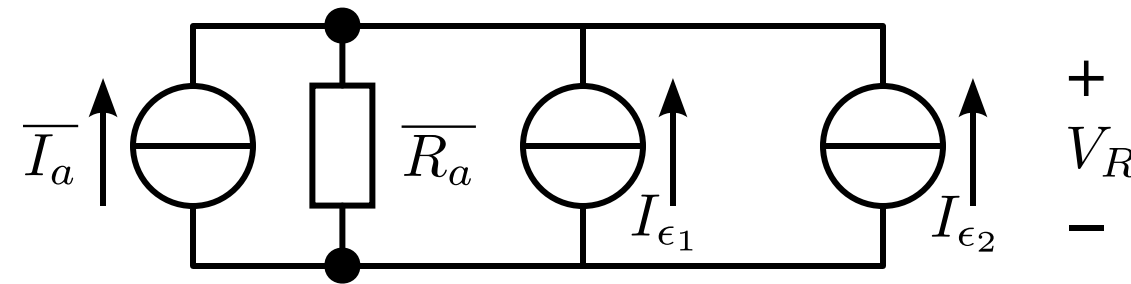


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Equivalent circuit using  
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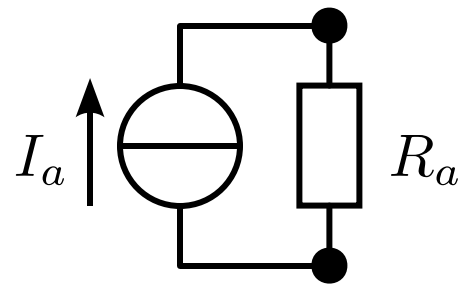
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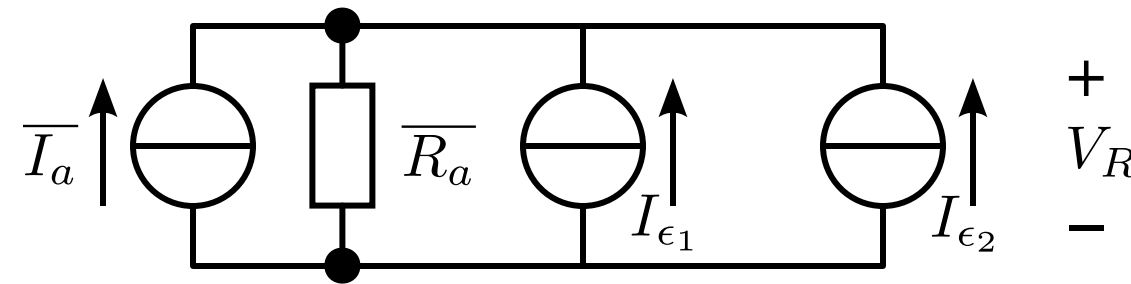


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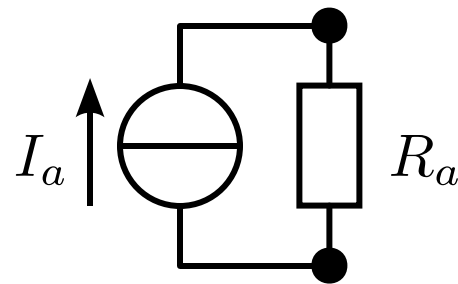
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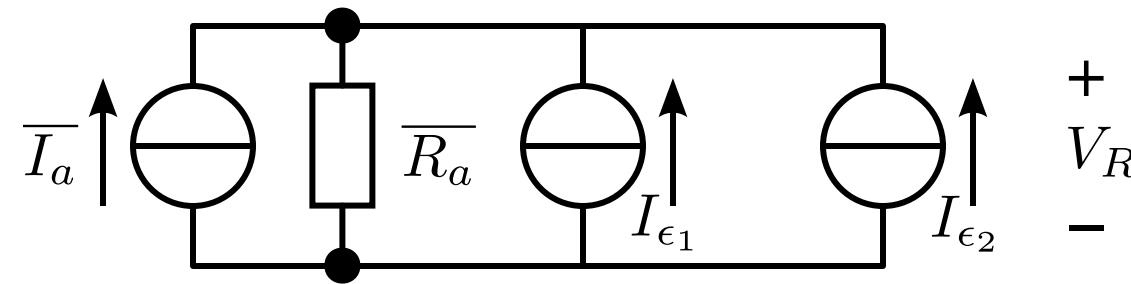


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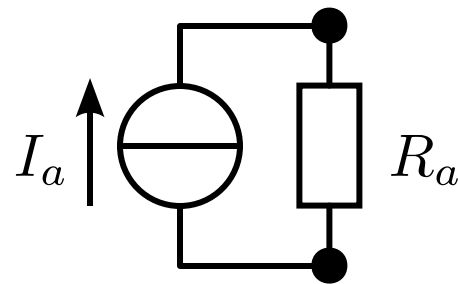
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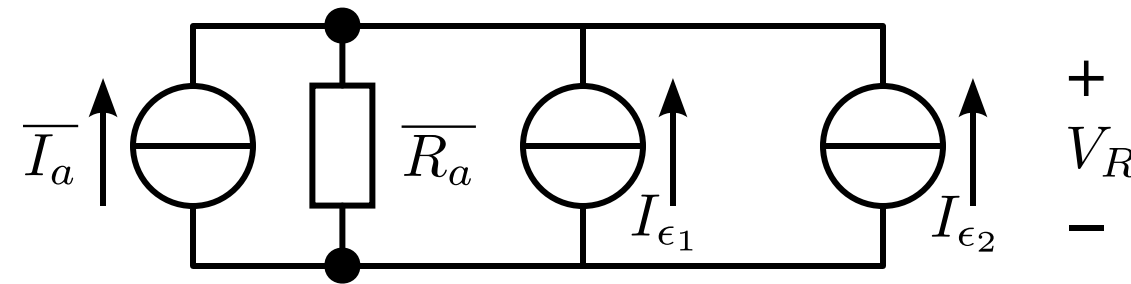


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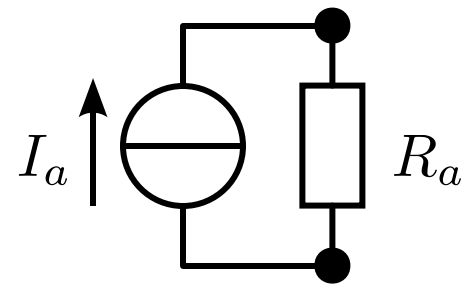
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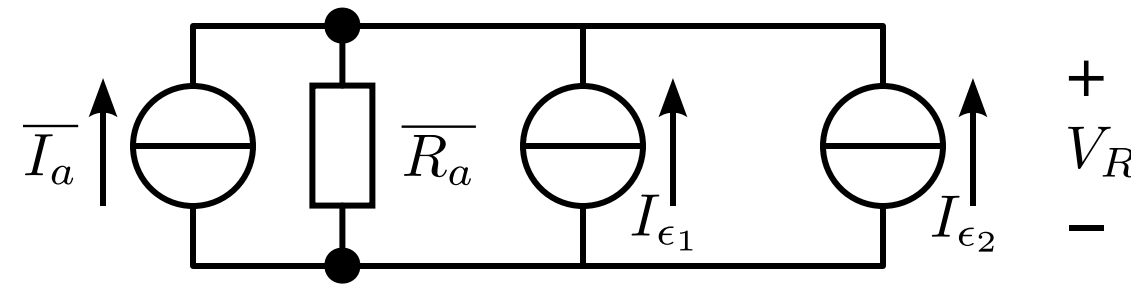


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Calculation of the variance of  
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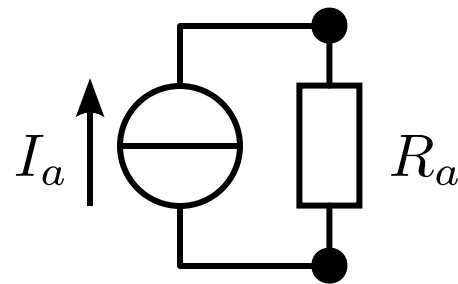
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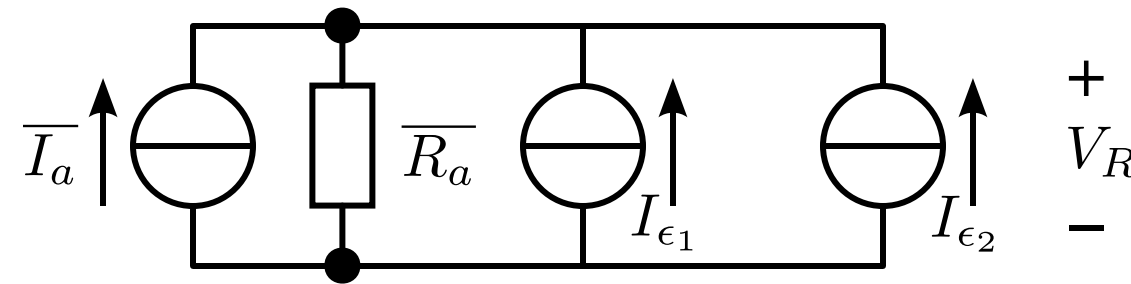


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Add their uncorrelated contributions:

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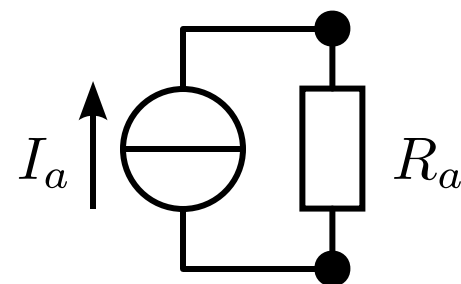
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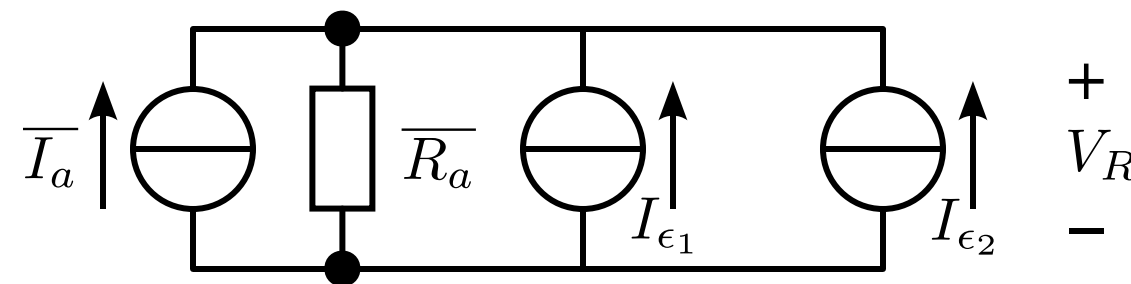


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Calculation of the variance of  
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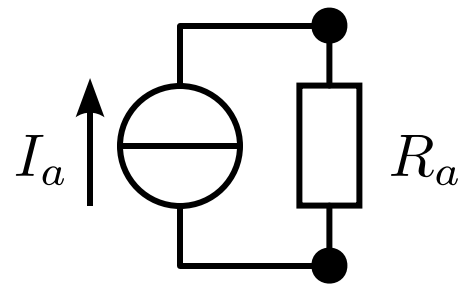
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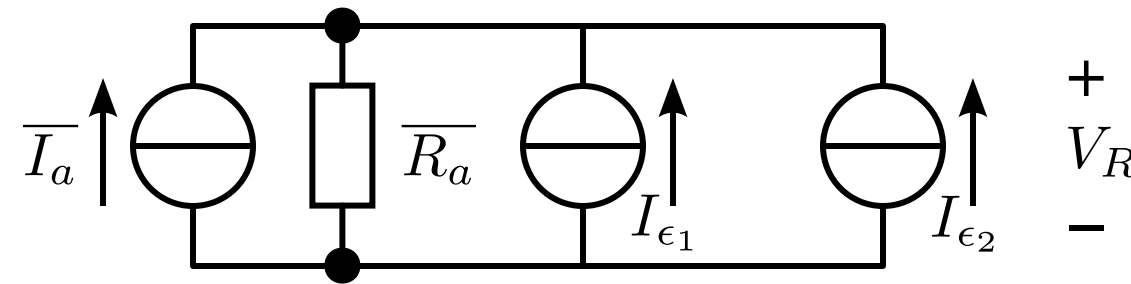


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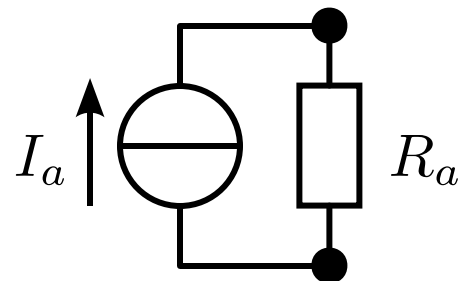
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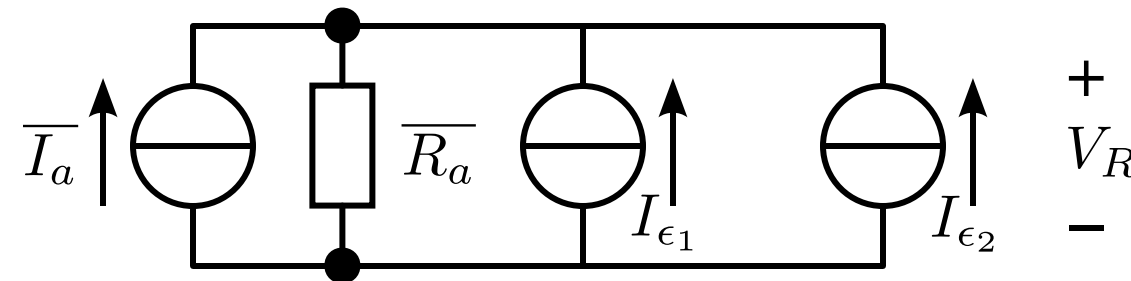


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$$\sigma_{V_R} = \sqrt{\overline{R_a}^2 \sigma_{I_a}^2 + \overline{I_a}^2 \sigma_{R_a}^2}$$

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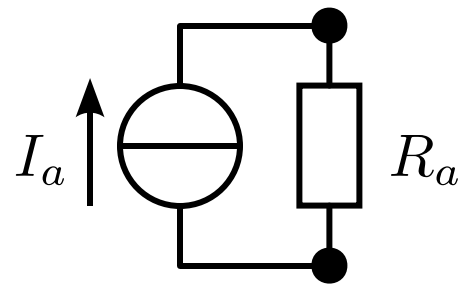
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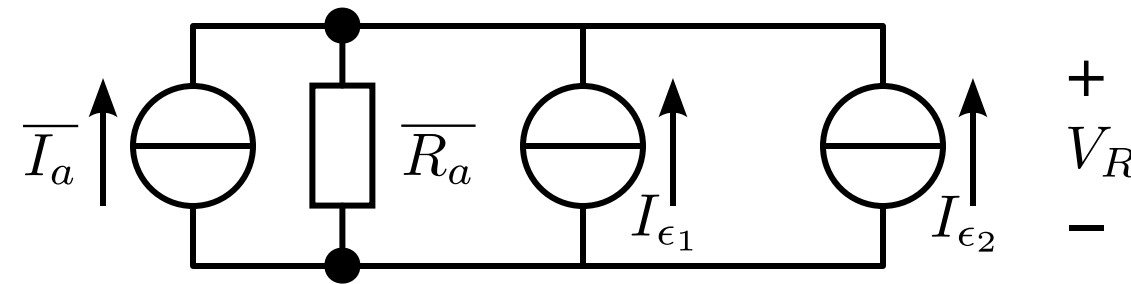


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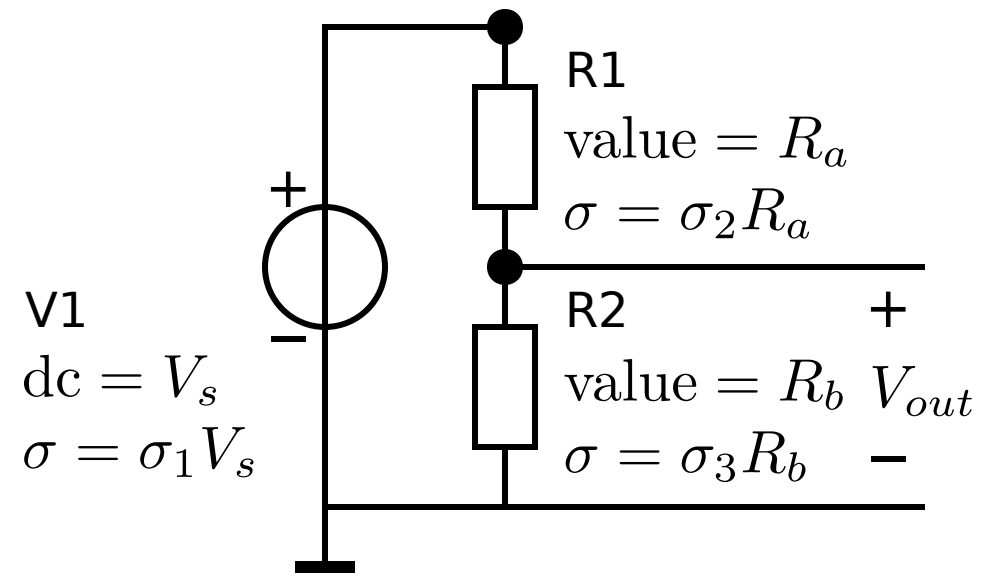
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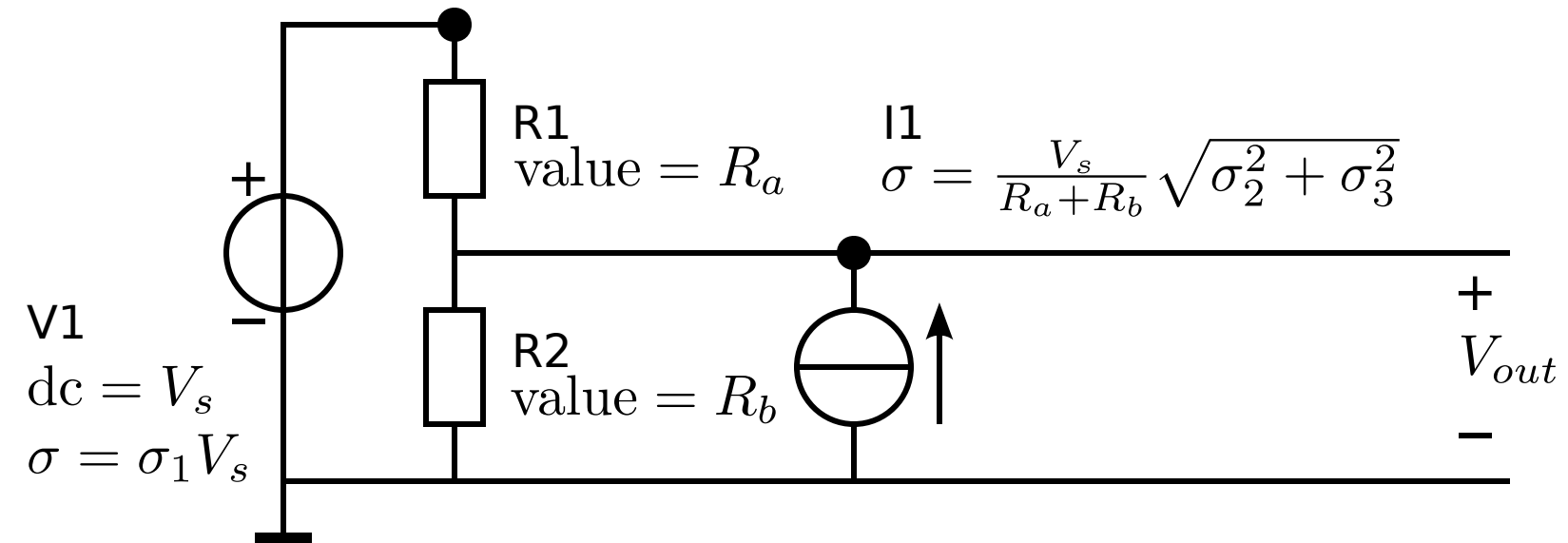
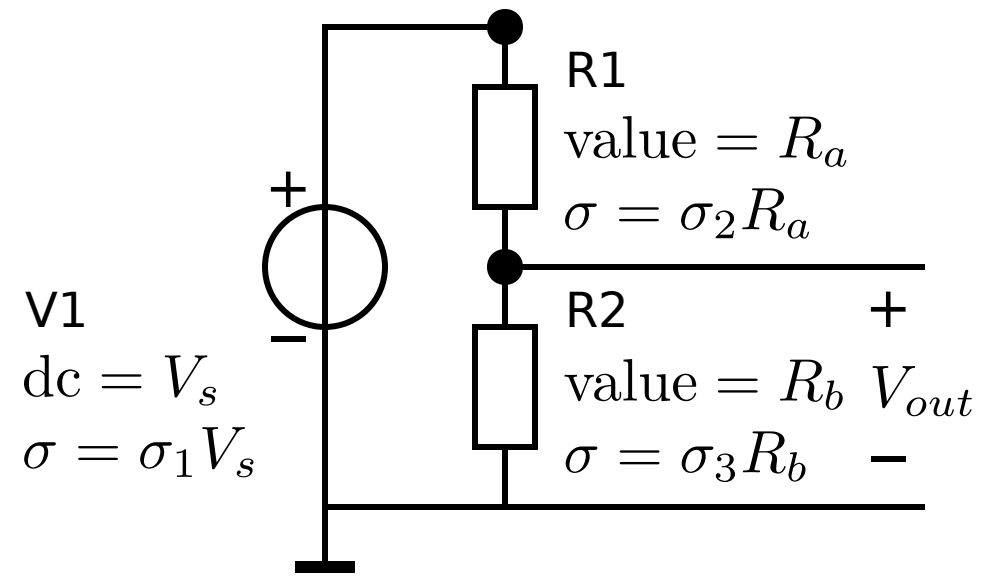
$$\sigma_{V_R} = \sqrt{\overline{R_a}^2 \sigma_{I_a}^2 + \overline{I_a}^2 \sigma_{R_a}^2}$$

# Influence of supply and resistor tolerances

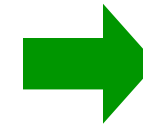
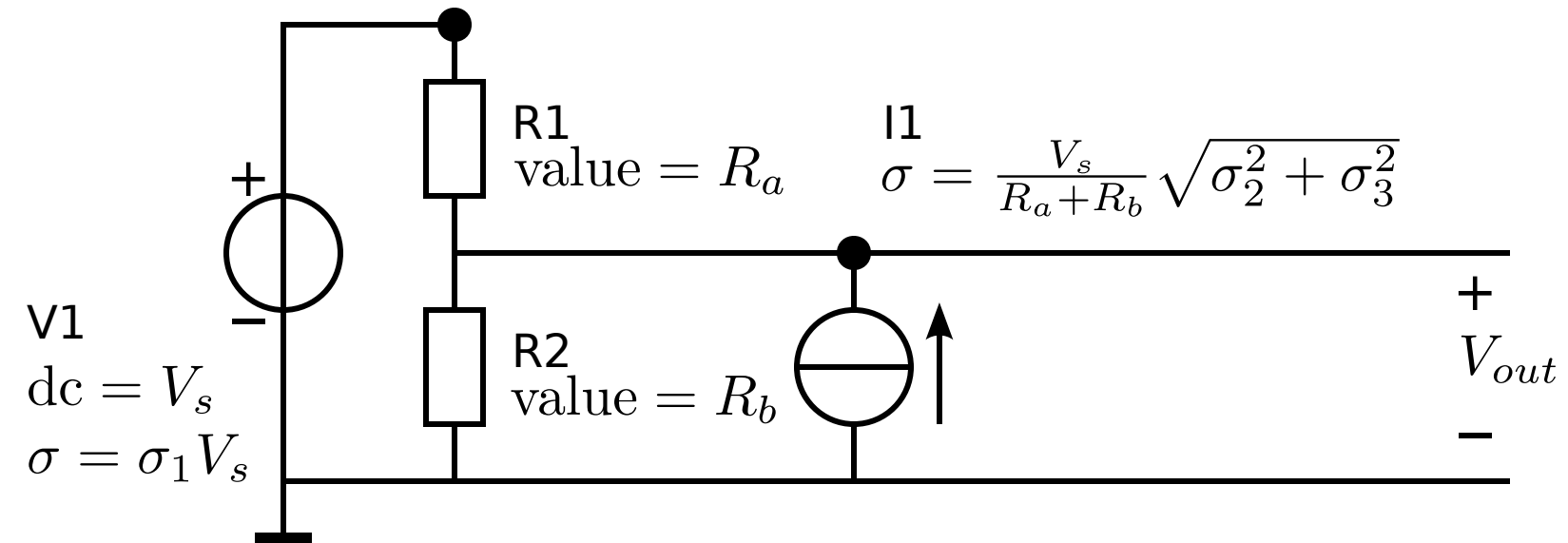
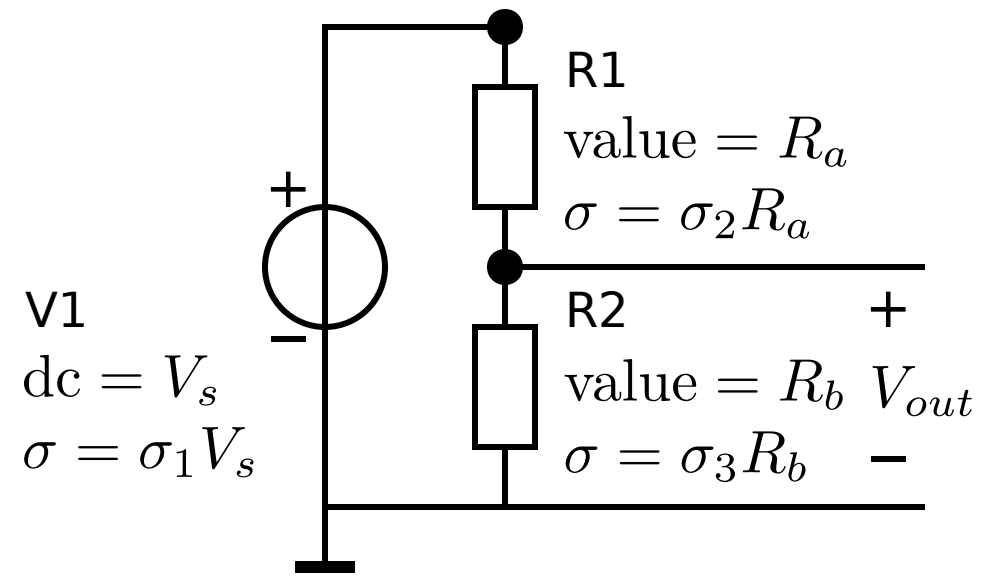
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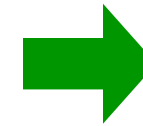
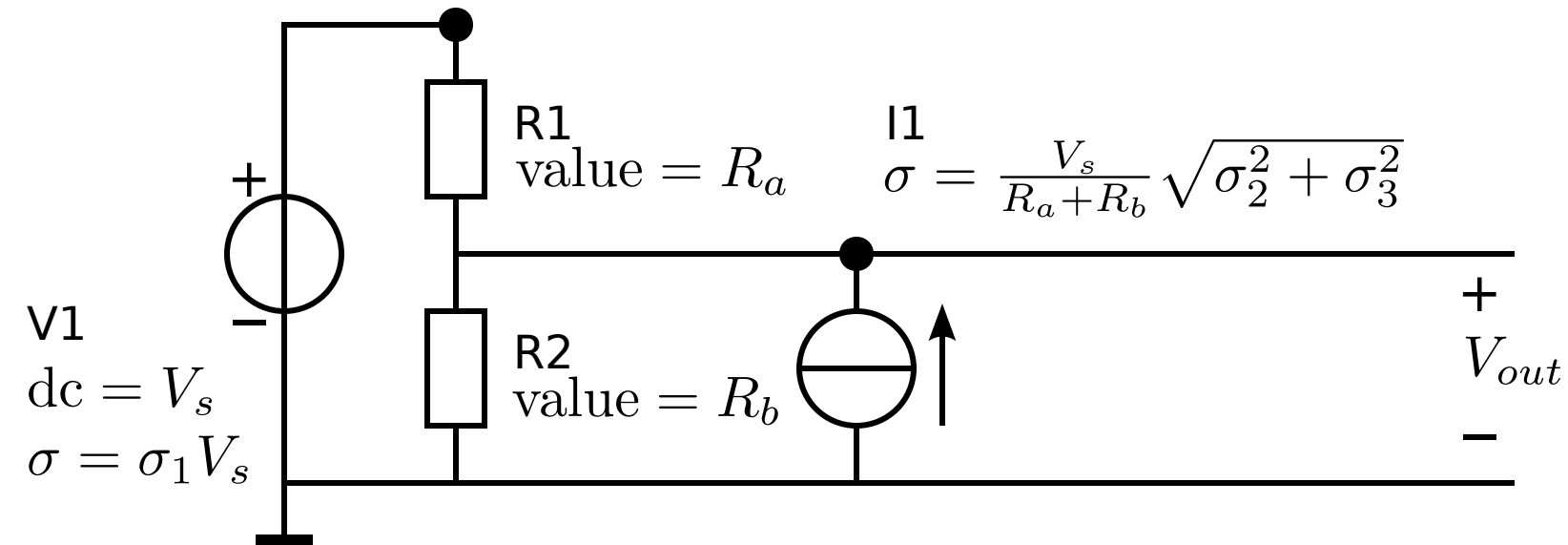
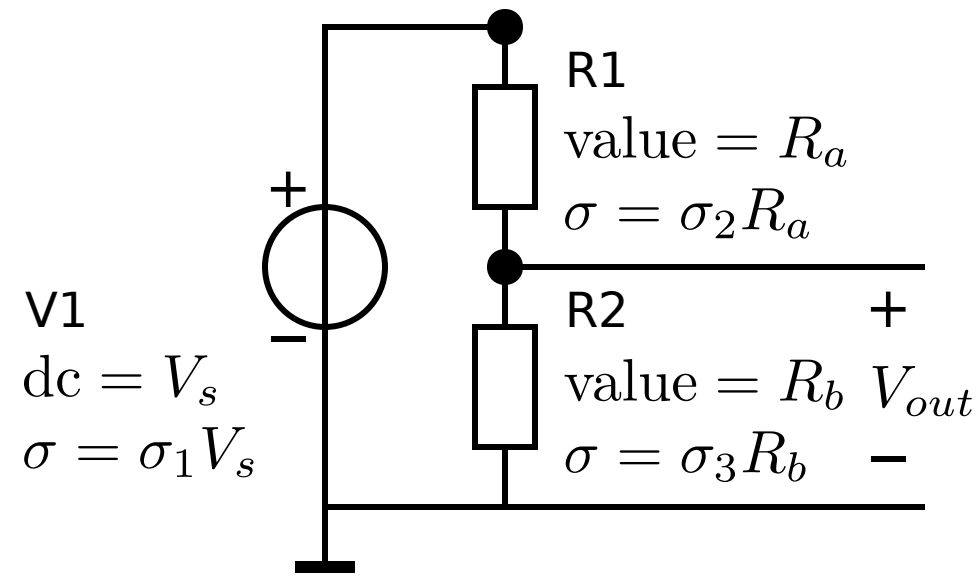


# Influence of supply and resistor tolerances



$$\overline{V_{out}} = V_s \frac{R_b}{R_a + R_b}$$

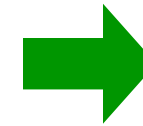
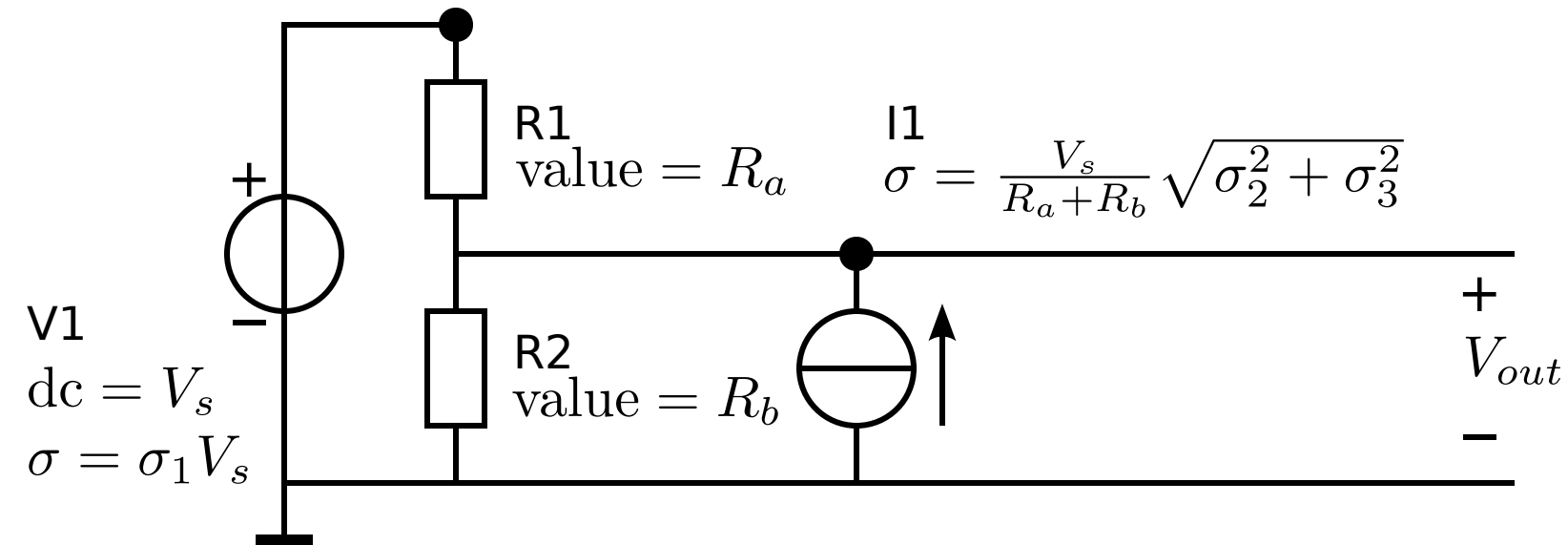
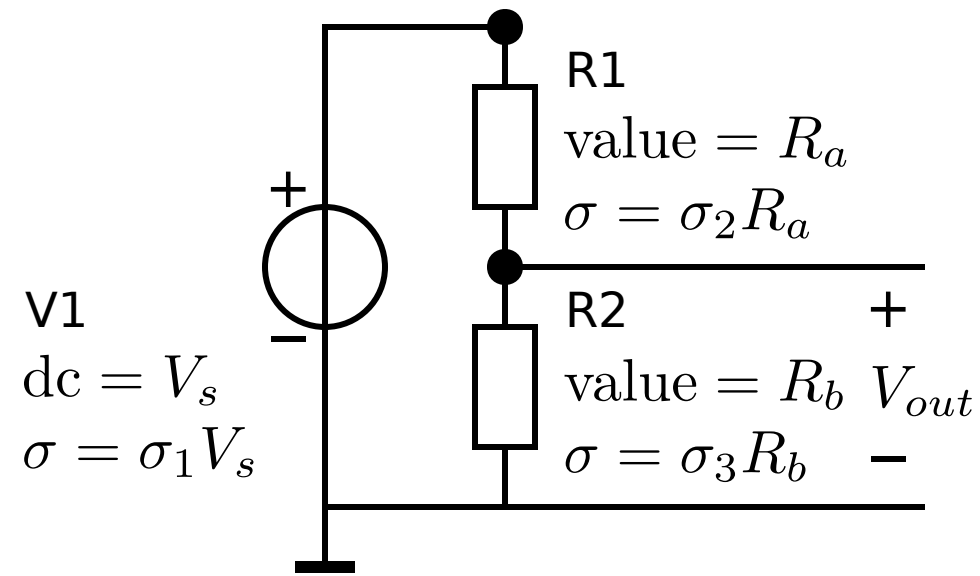
# Influence of supply and resistor tolerances



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$$\frac{\sigma_{V_{out}}}{V_{out}} = \sqrt{\left(\frac{R_a}{R_a + R_b}\right)^2 (\sigma_2^2 + \sigma_3^2) + \sigma_1^2}$$

# Influence of supply and resistor tolerances



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# Influence of controller biasing errors

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SLiCAP model of ideal  
OpAmp with bias errors

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SLiCAP model of ideal  
OpAmp with bias errors

- Correlated bias currents

# Influence of controller biasing errors

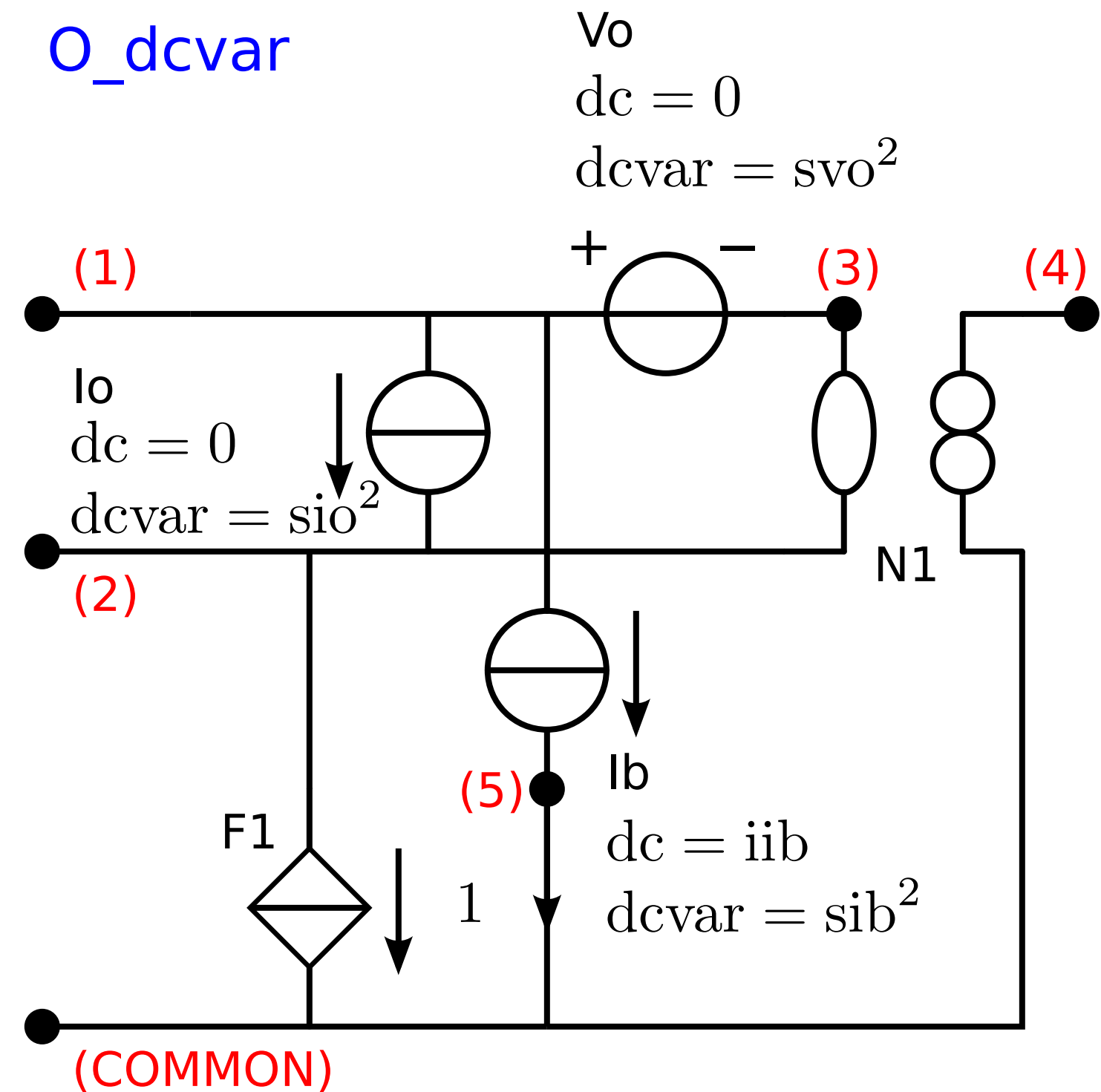
SLiCAP model of ideal  
OpAmp with bias errors

- Correlated bias currents
- Uncorrelated offsets

# Influence of controller biasing errors

## SLiCAP model of ideal OpAmp with bias errors

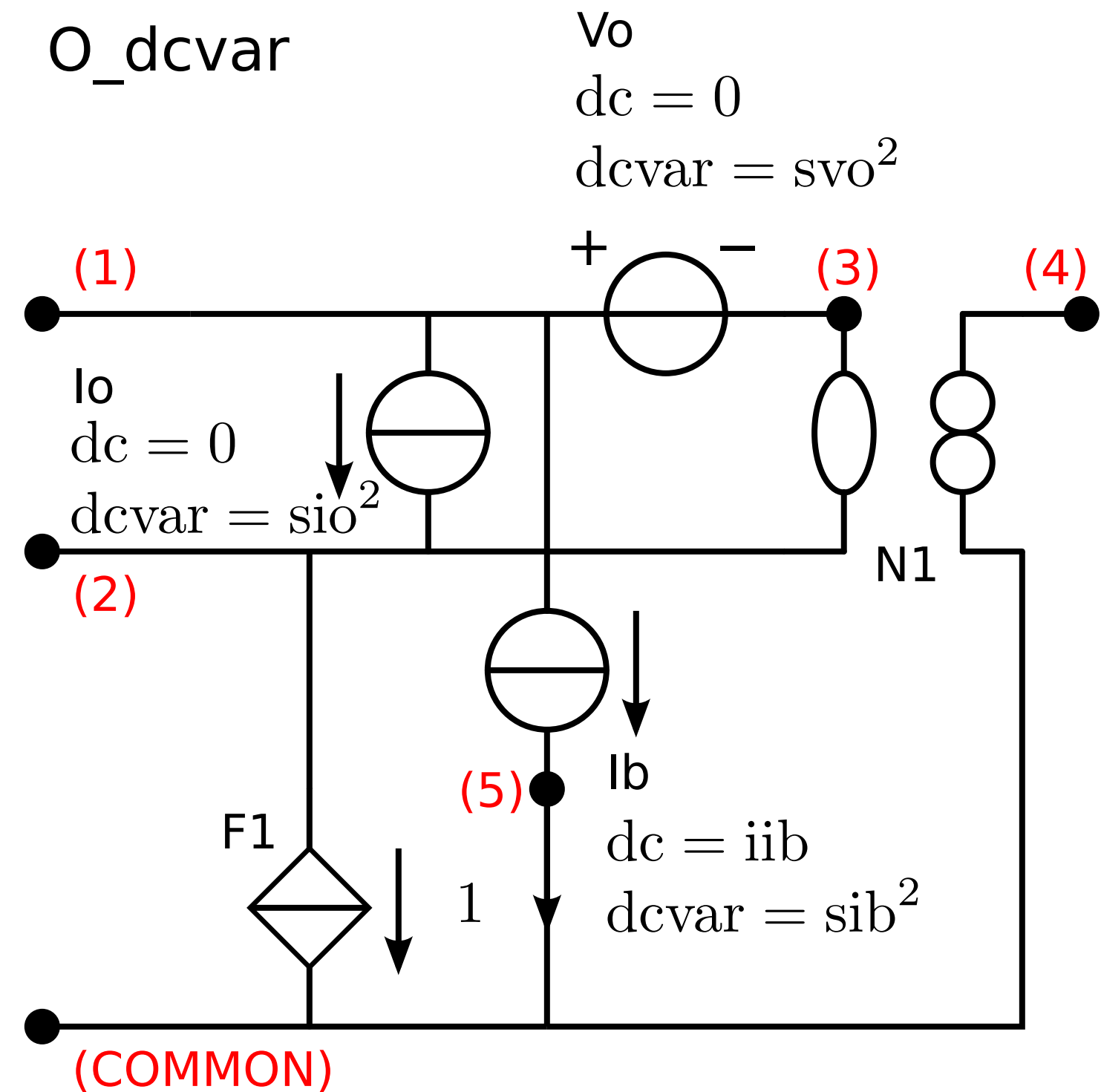
- Correlated bias currents
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# Influence of controller biasing errors

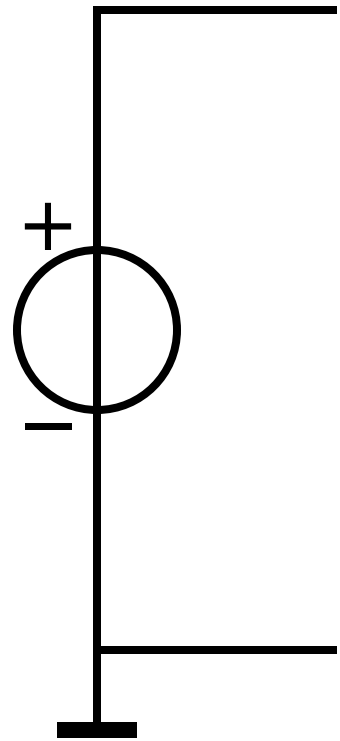
## SLiCAP model of ideal OpAmp with bias errors

- Correlated bias currents
- Uncorrelated offsets



# Total bias errors

# Total bias errors



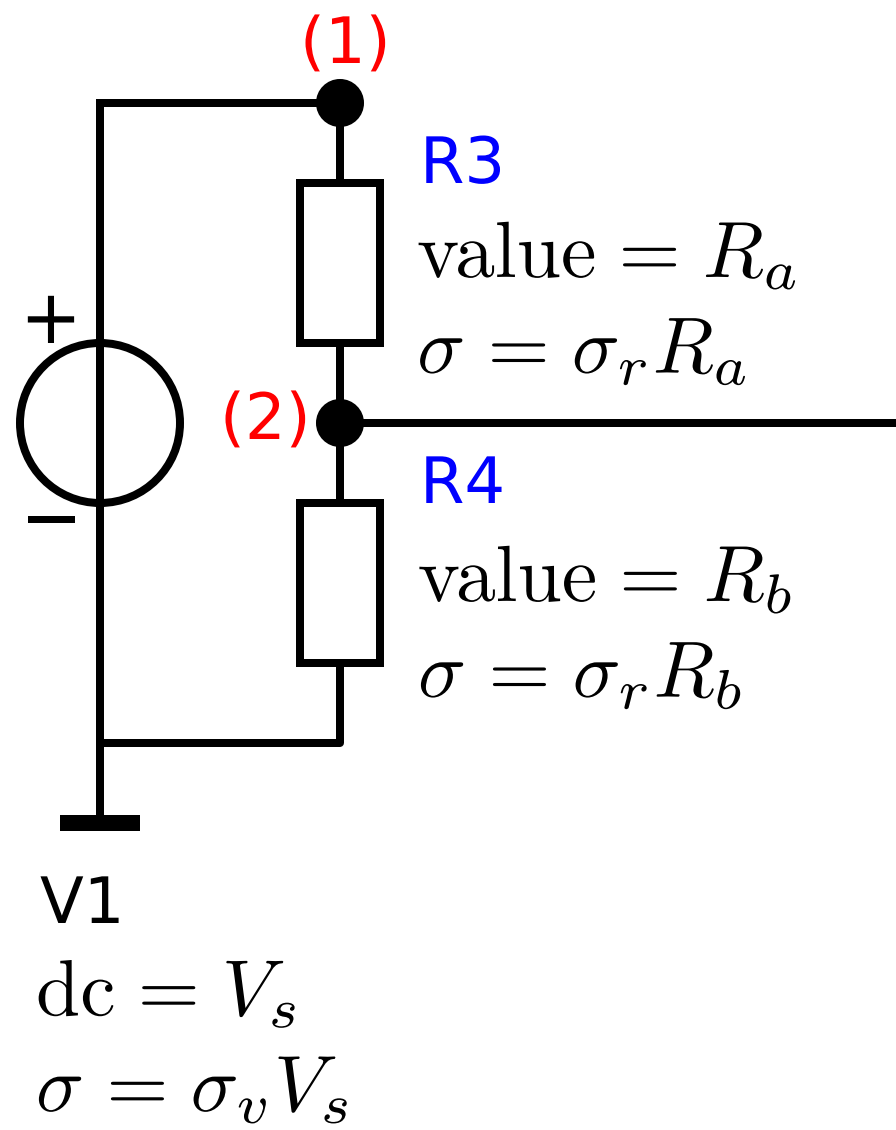
V1

$$\text{dc} = V_s$$

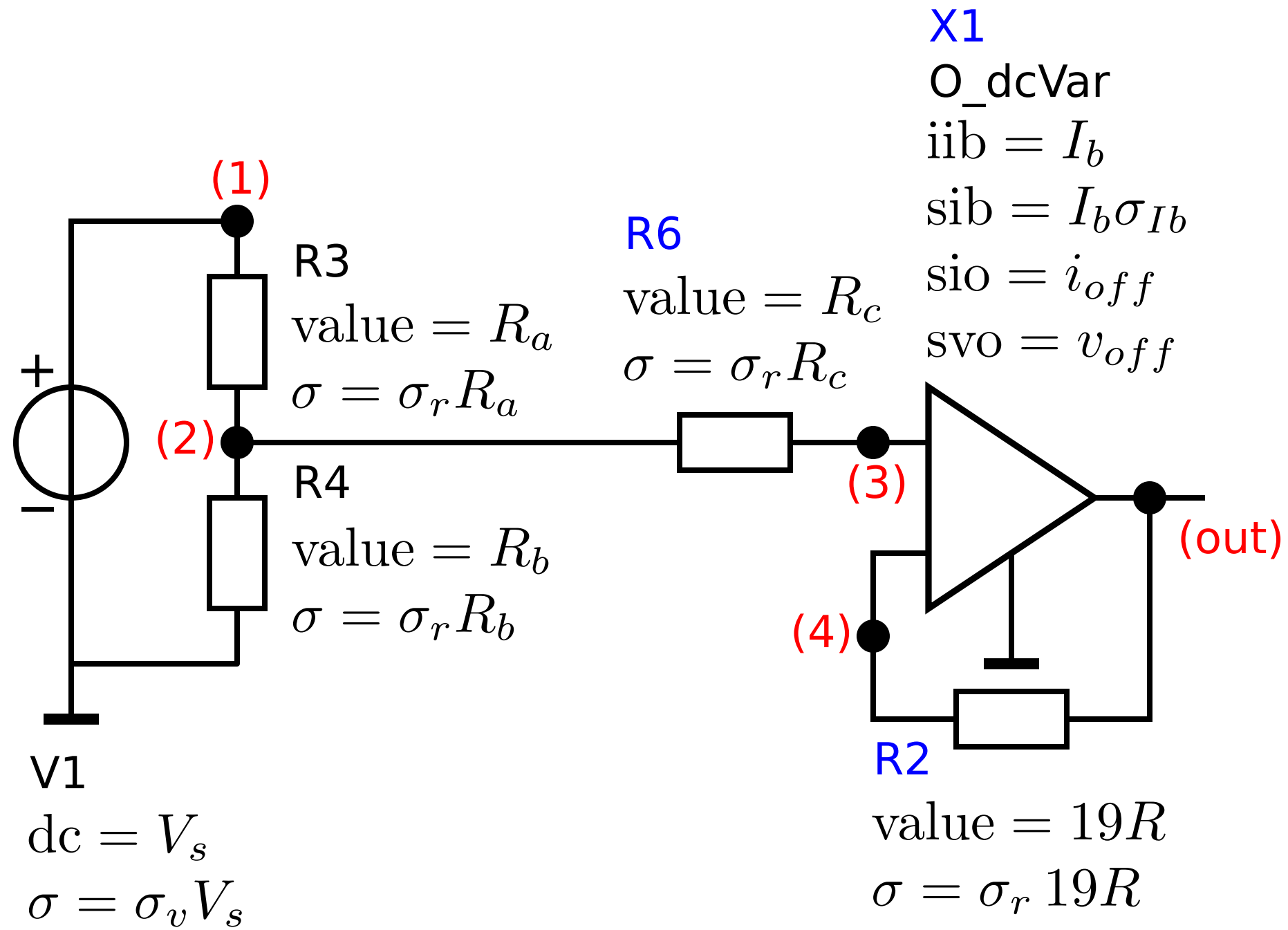
$$\sigma = \sigma_v V_s$$



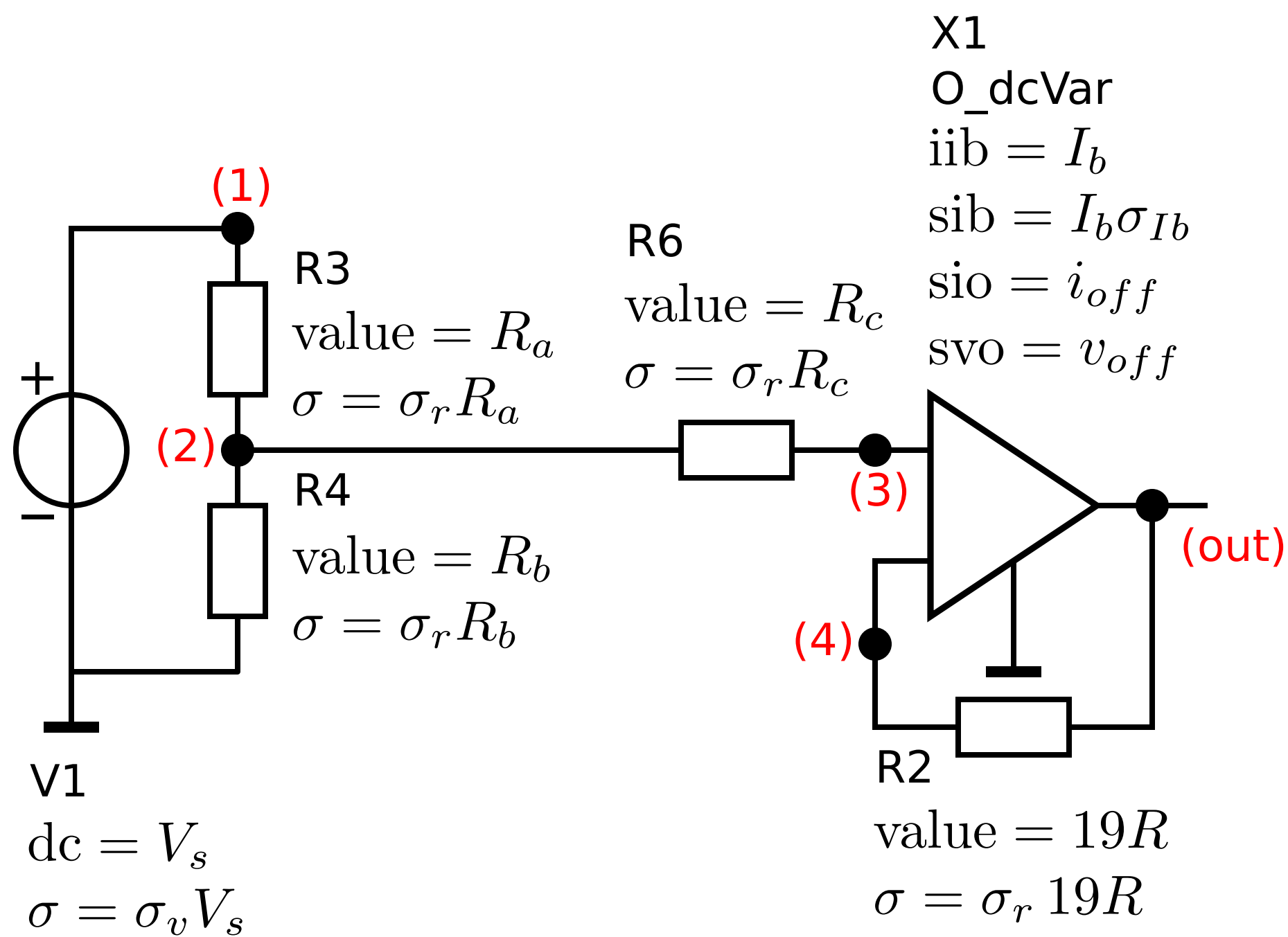
# Total bias errors



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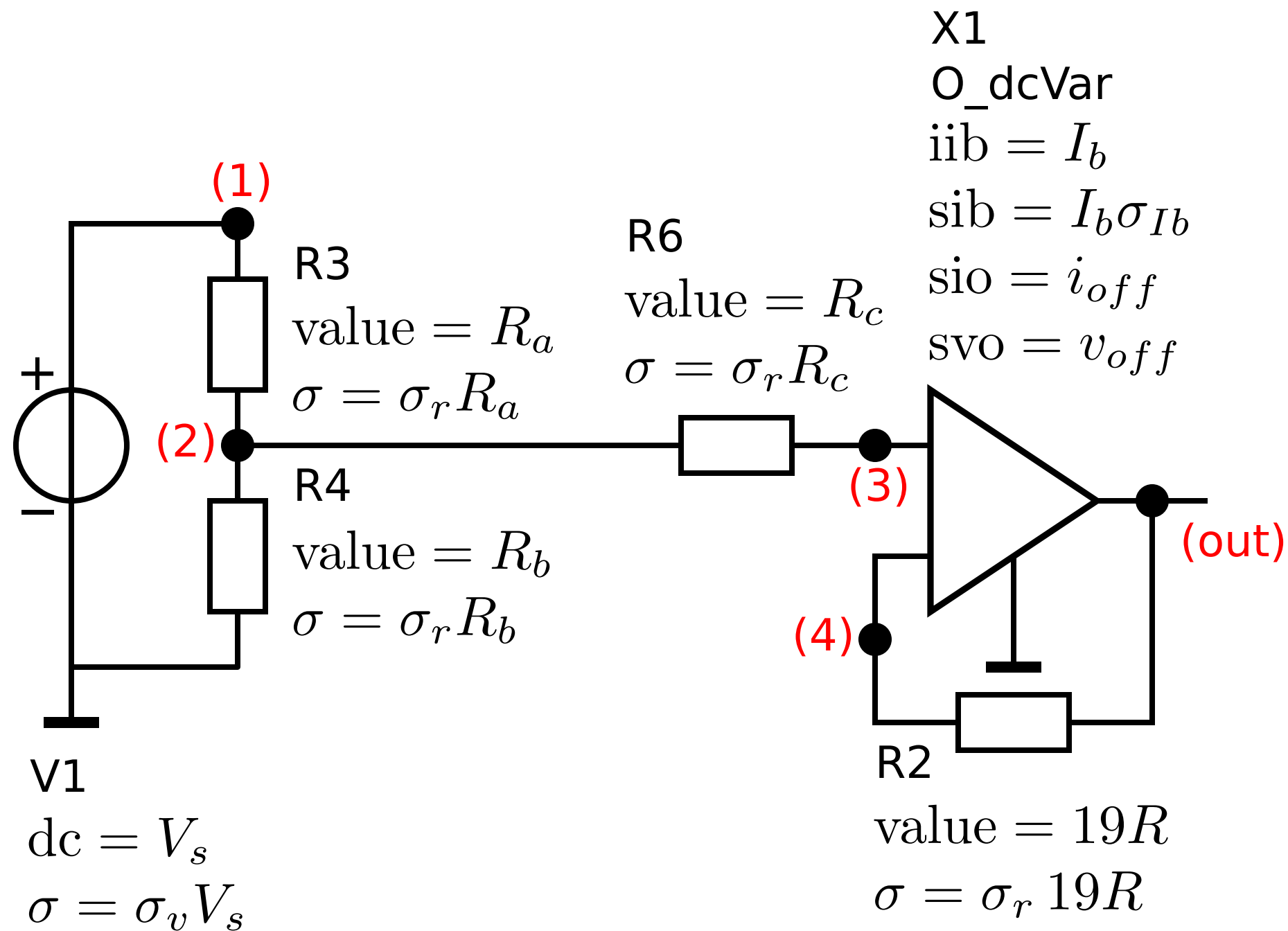


# Total bias errors



Simplified result:  $R_c \gg \frac{R_a R_b}{R_a + R_b}$

# Total bias errors



Simplified result:  $R_c \gg \frac{R_a R_b}{R_a + R_b}$

$$\begin{aligned}
 \sigma_{V_{out}}^2 = & 2\sigma_r^2 \left( \frac{V_s}{R_a + R_b} \right)^2 \left( \frac{R_a R_b}{R_a + R_b} \right)^2 \\
 & + \sigma_v^2 V_s^2 \left( \frac{R_b}{R_a + R_b} \right)^2 \\
 & + v_{off}^2 \\
 & + i_{off}^2 (R_c + 19R)^2 \\
 & + \sigma_{Ib}^2 I_b^2 (R_c - 19R)^2 \\
 & + \sigma_r^2 I_b^2 \left( R_c^2 + (19R)^2 \right)
 \end{aligned}$$