


# EE4109: Agenda (lecture 7)

1. Discussion homework
2. CMOS operation and modelling
  - Recap
3. How to find first guess for transistor OP and WL 
  - Theory
  - SliCAP demonstration
4. Homework: SLiCAP noise again, see BrightSpace

order of designing

independent performance aspects

	1	2	3	4	5	6	7	8	9
type	X								
gain		X							
noise			X						
power efficiency				X					
clipping					X				
small-signal bandwidth						X			
frequency response							X		
weak nonlinearity								X	
DC (temperature) stability									X

Orthogonal design

feedback configuration

feedback network

controller input stage

controller output stage type

controller output stage biasing

loop gain poles product

frequency compensation

differential error to gain ratio

over-all biasing