

EE4109: Agenda (lecture 10)

1. Instructions:

1. Complete the design of your own output stage
2. Test the circuit in LT-Spice
3. **During instructions:** Upload the .asc file of your LT-Spice Simulation to Bright Space
4. **During instructions:** Upload a .txt file with your design considerations and interpretation of the simulation

Break

1. Our design of the output stage

- Design considerations
- Design in SLiCAP (including use of different models)

2. Homework:

1. Update your design (if necessary 😊)
2. For different operating points, create Bode plots of $1/C$, $1/A$ and Z_{in}
3. Update SLiCAP with output stage design (include text with design consideration and interpretation of results)