

# Structured Electronic Design Implementation of Phantom Zeros

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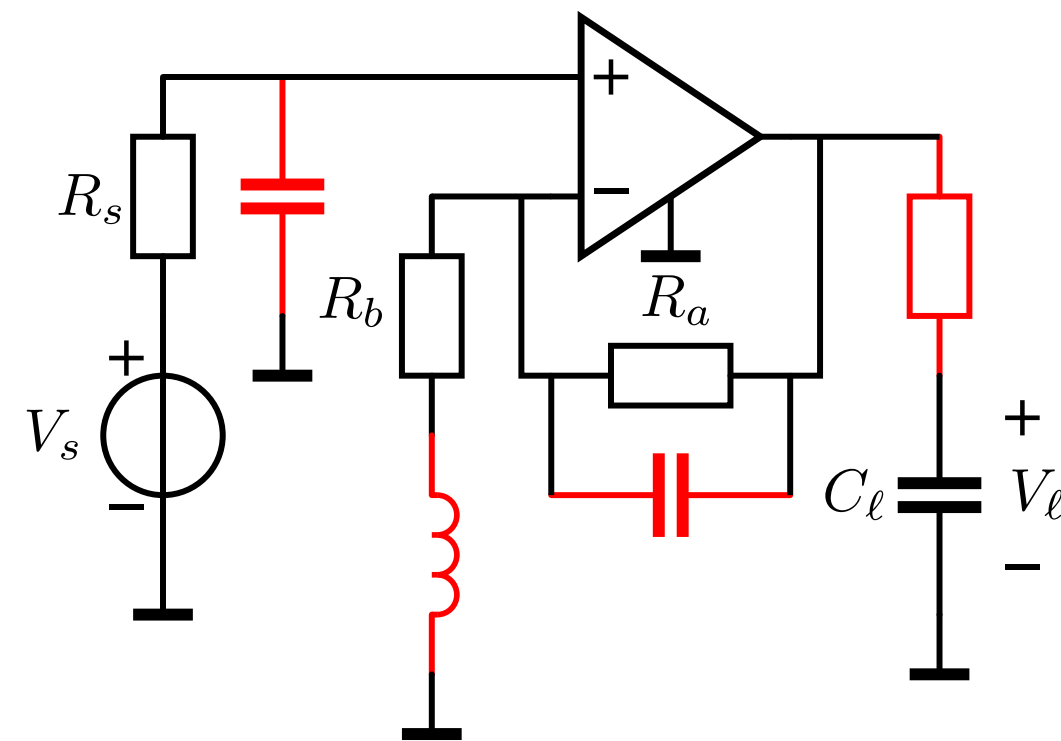
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Pole in the asymptotic gain:

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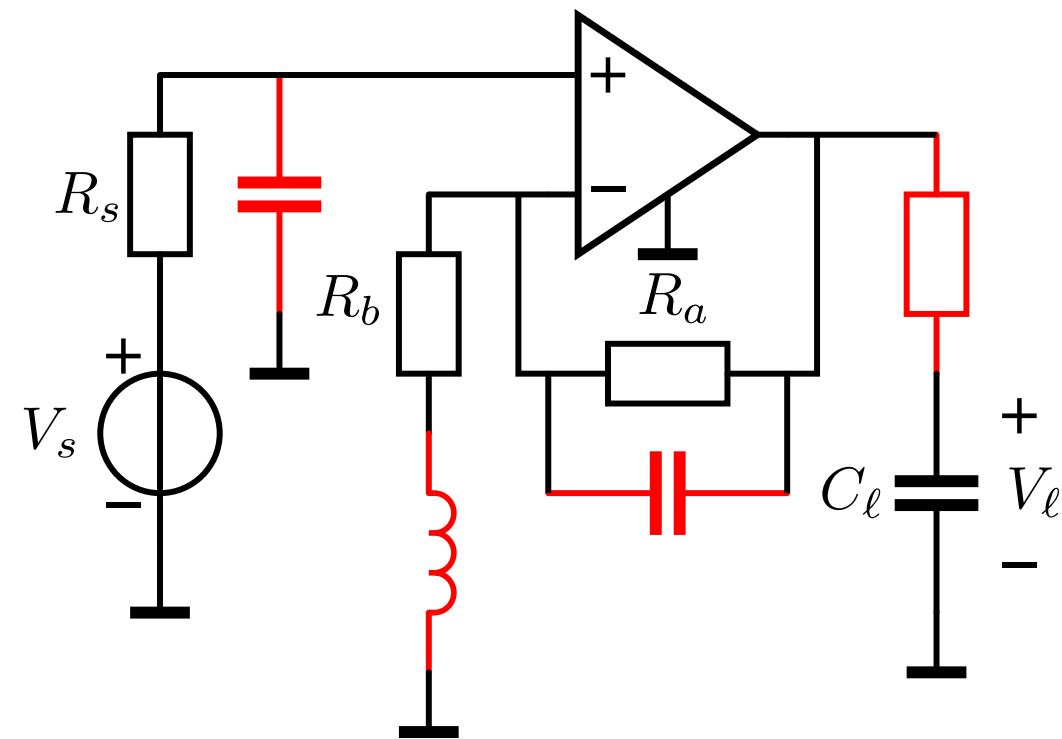


Possible phantom  
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in a voltage amplifier  
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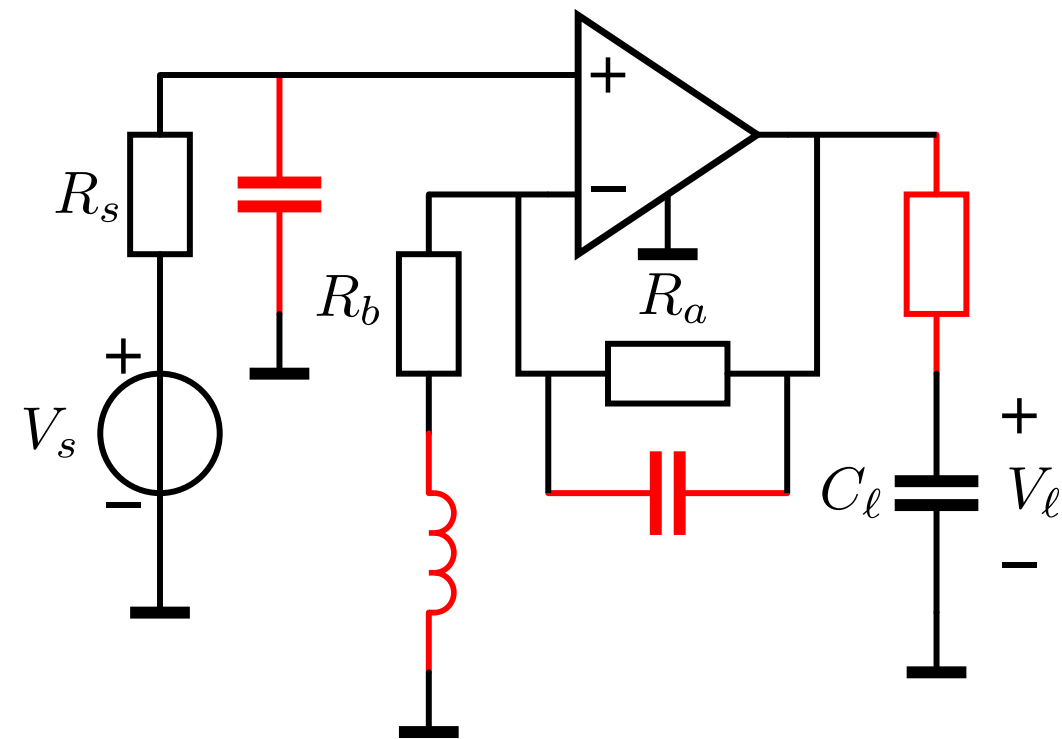
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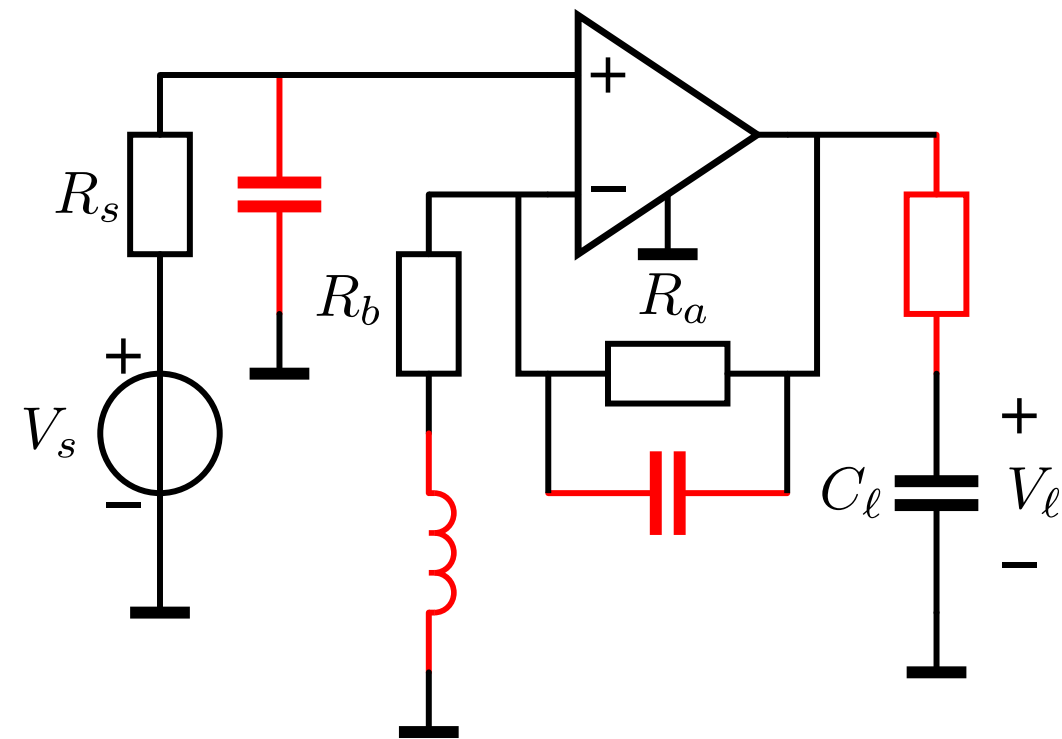
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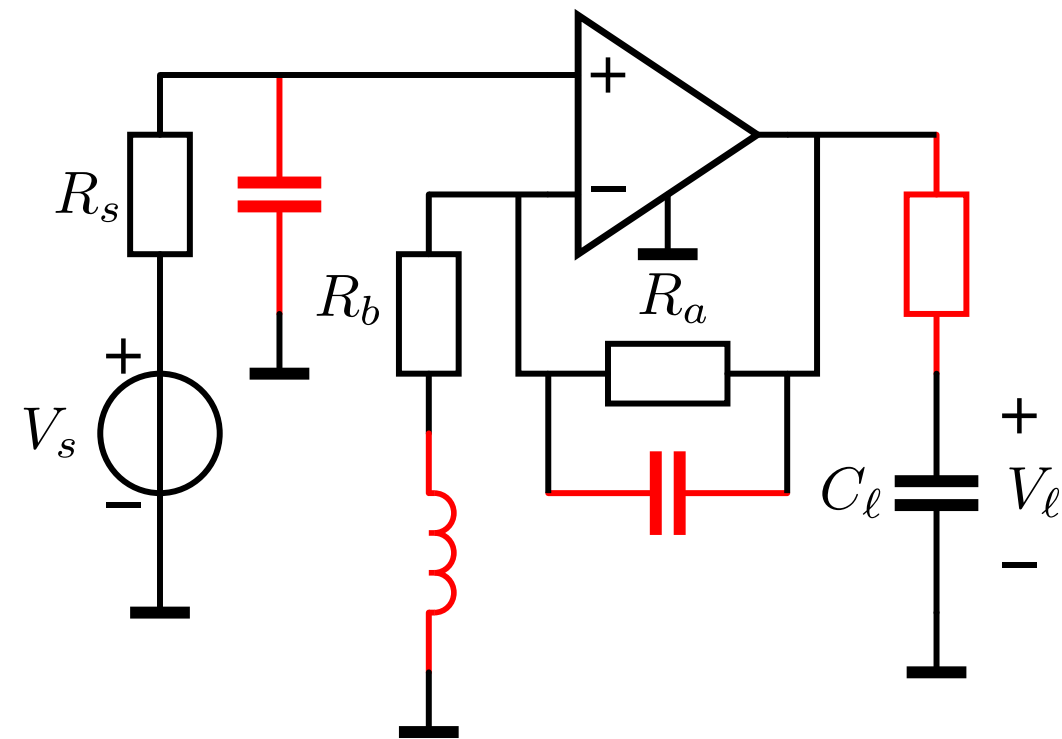
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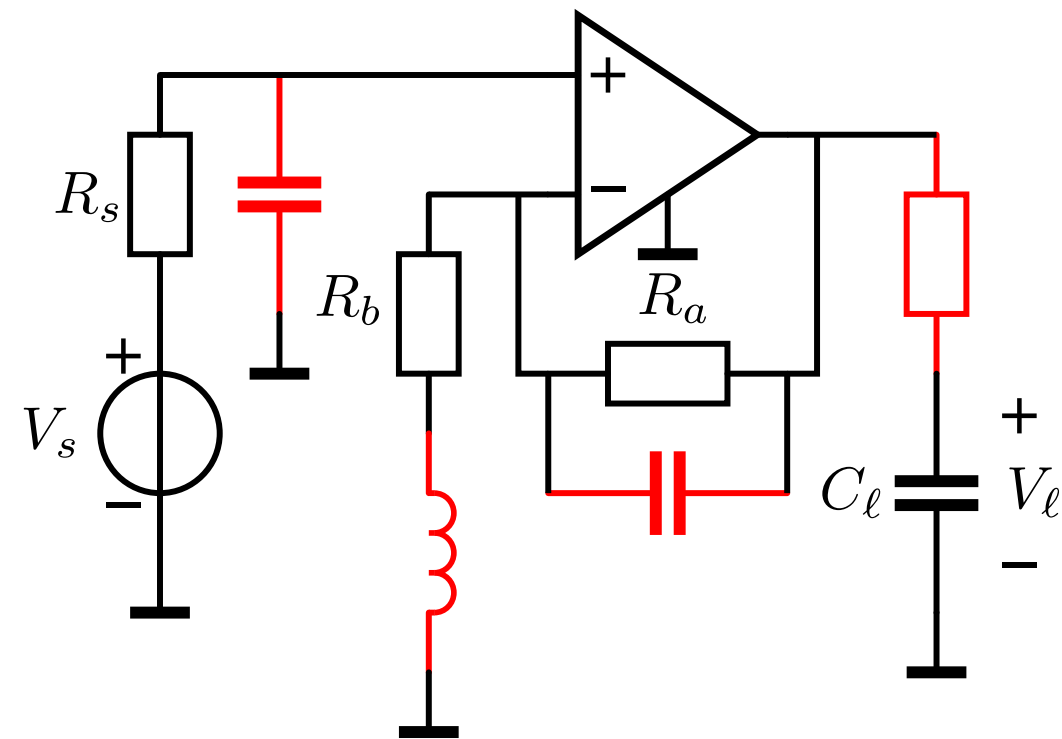
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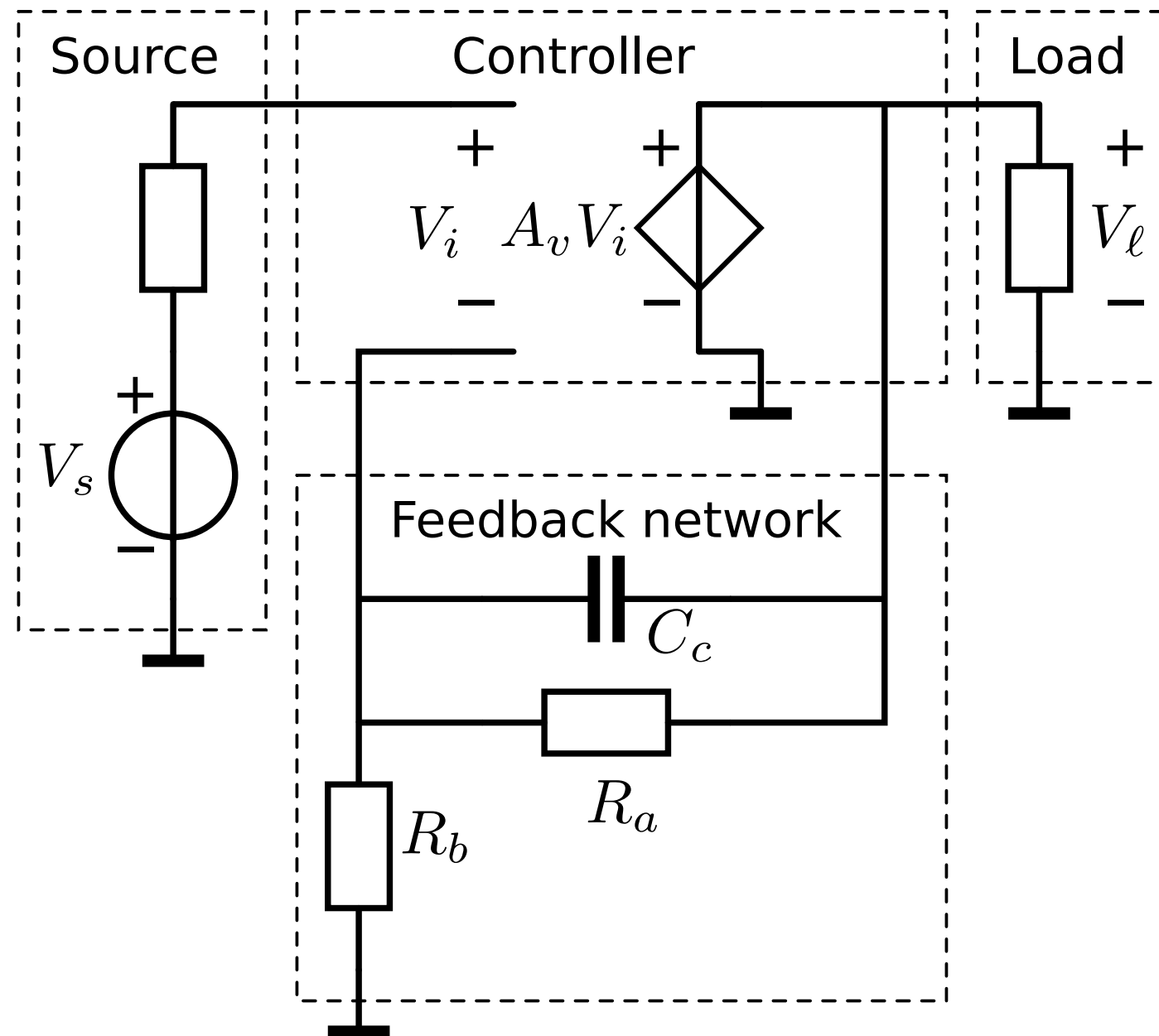
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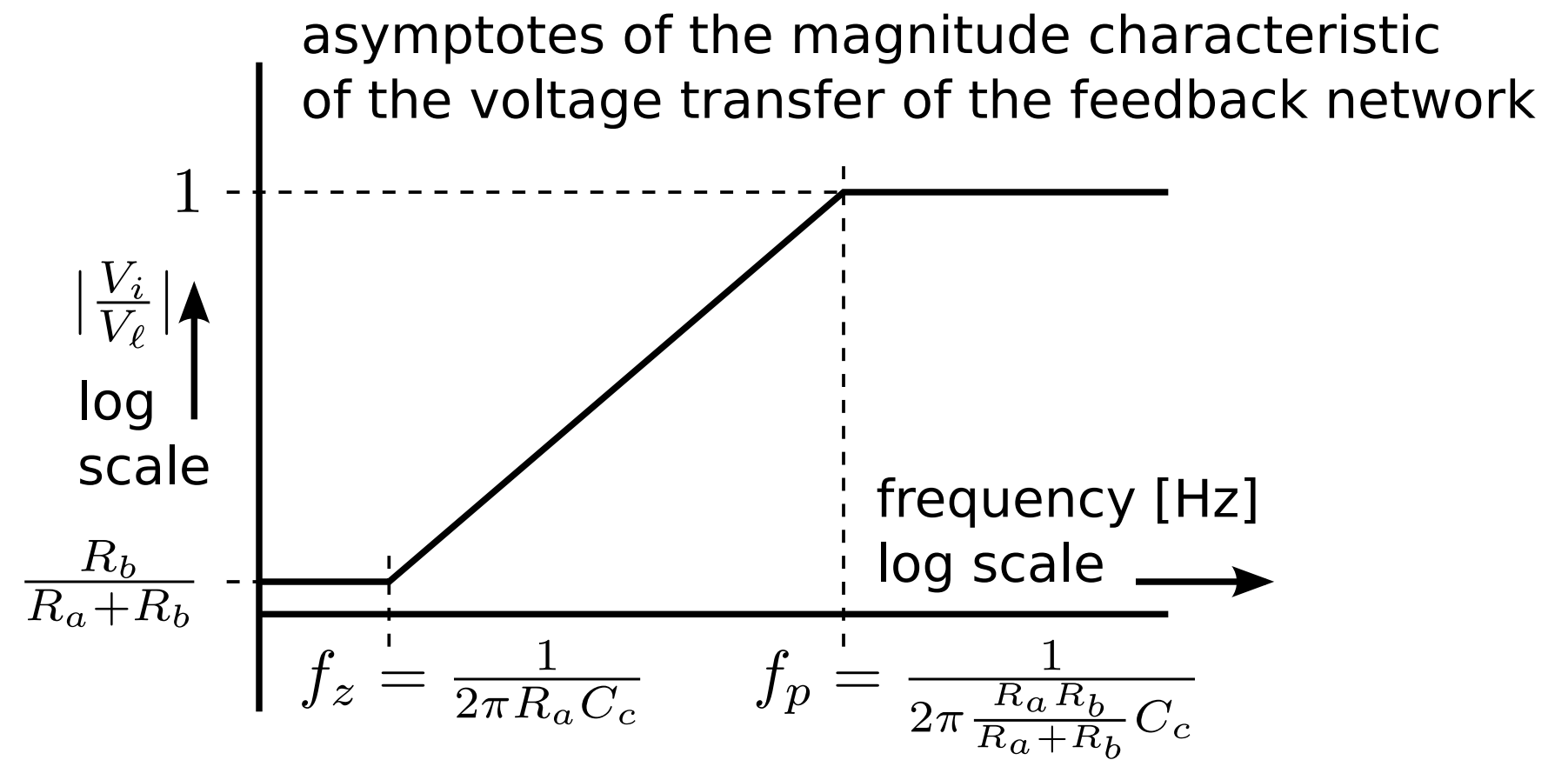
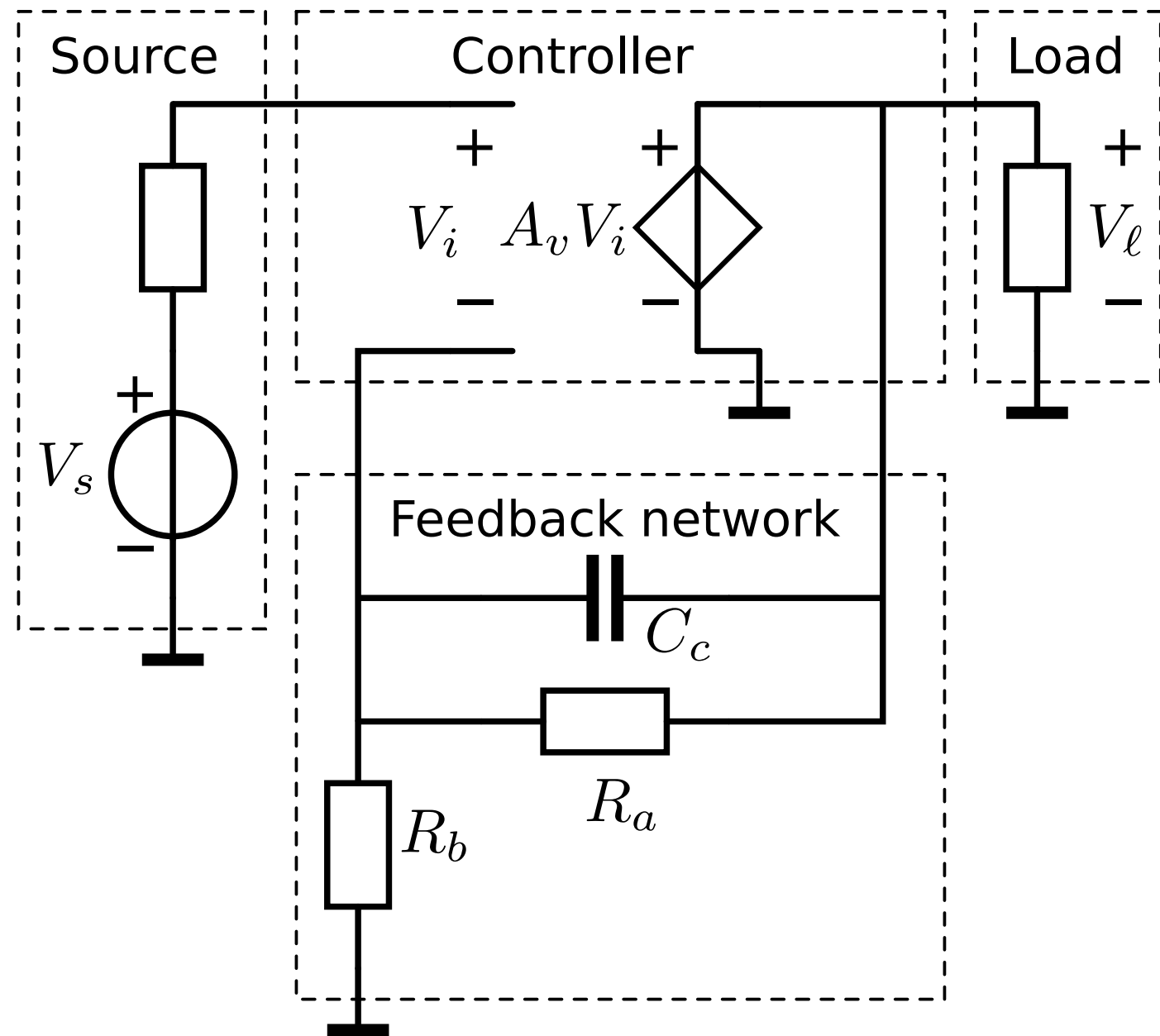
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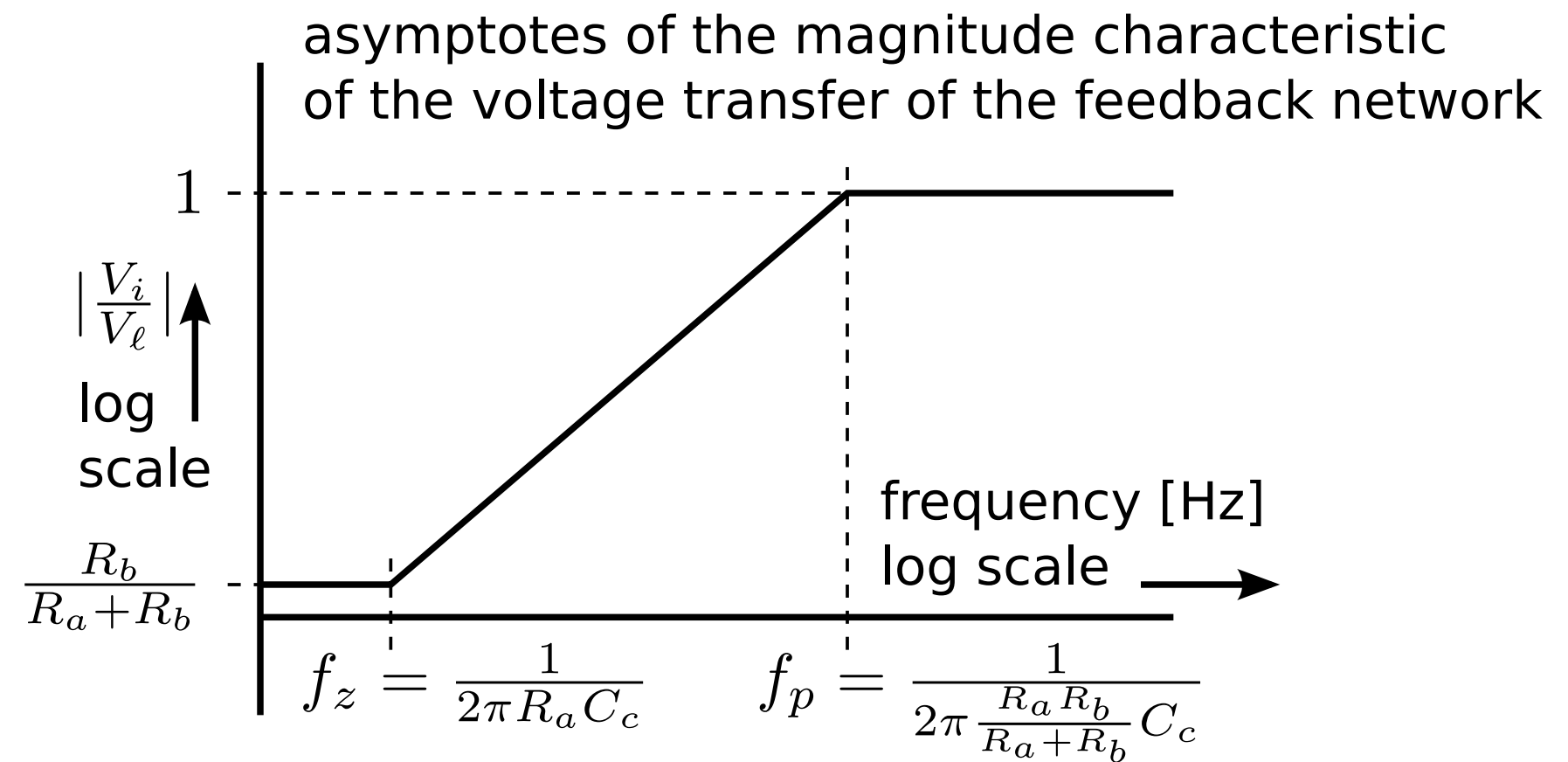
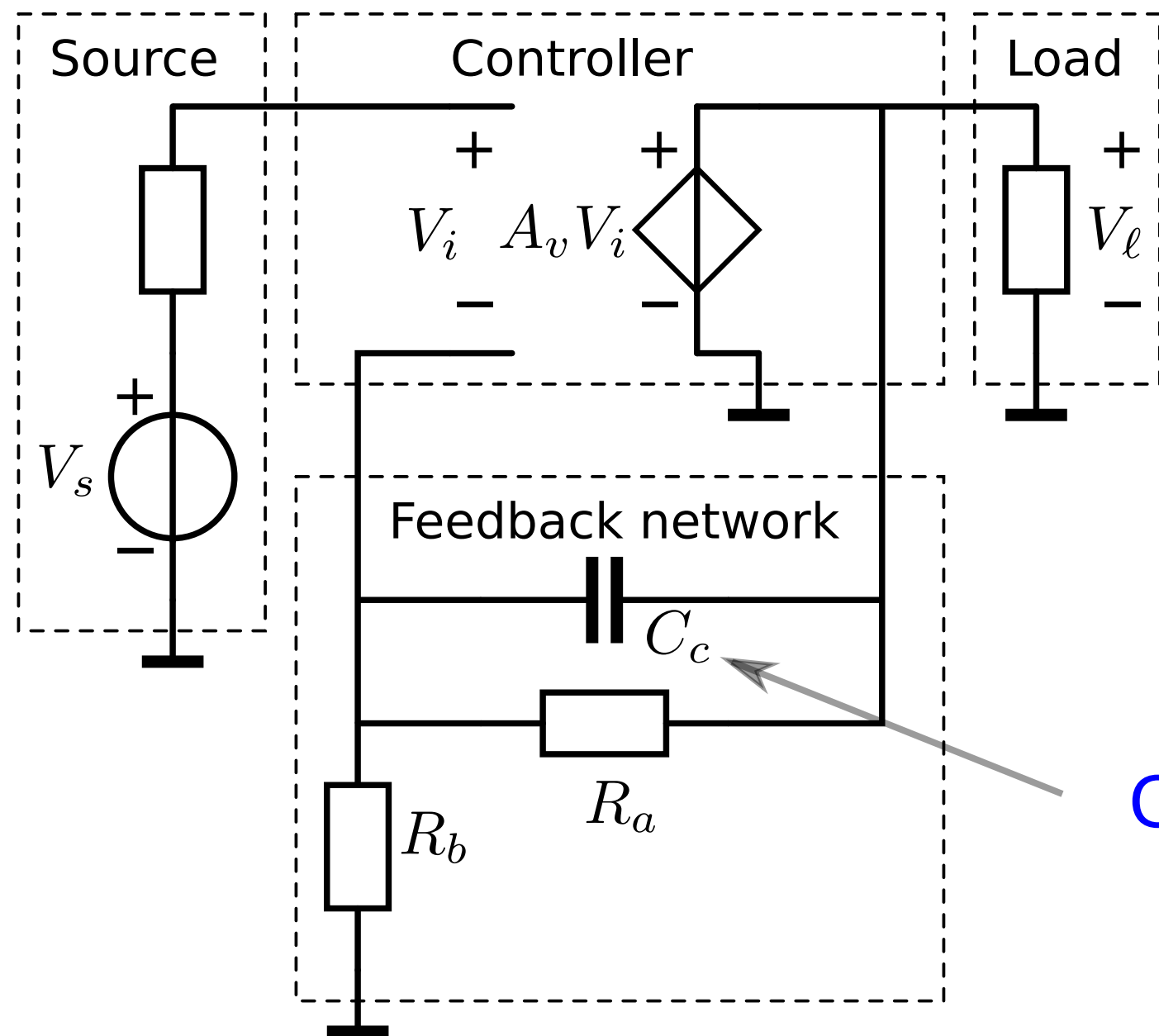




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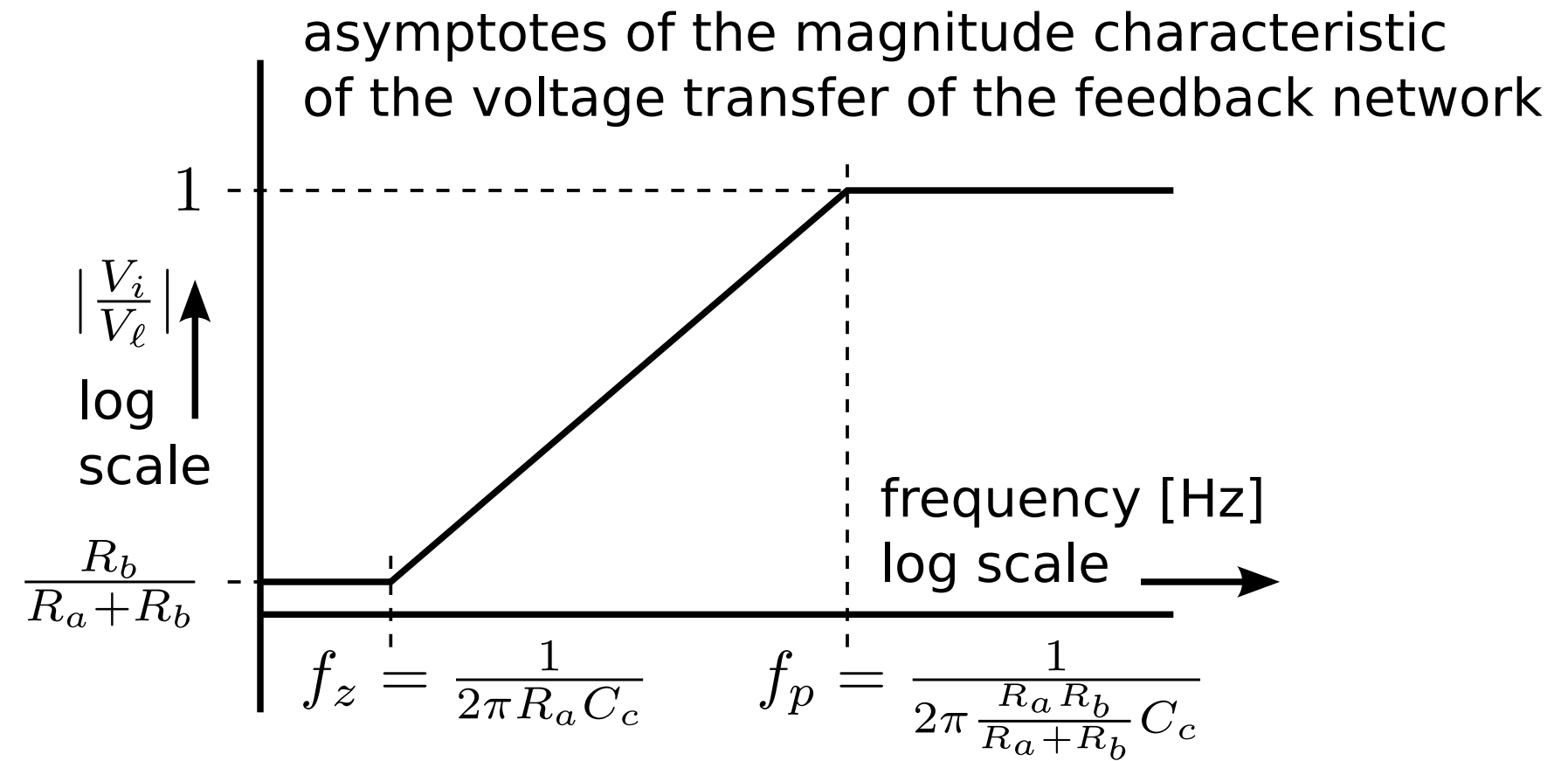
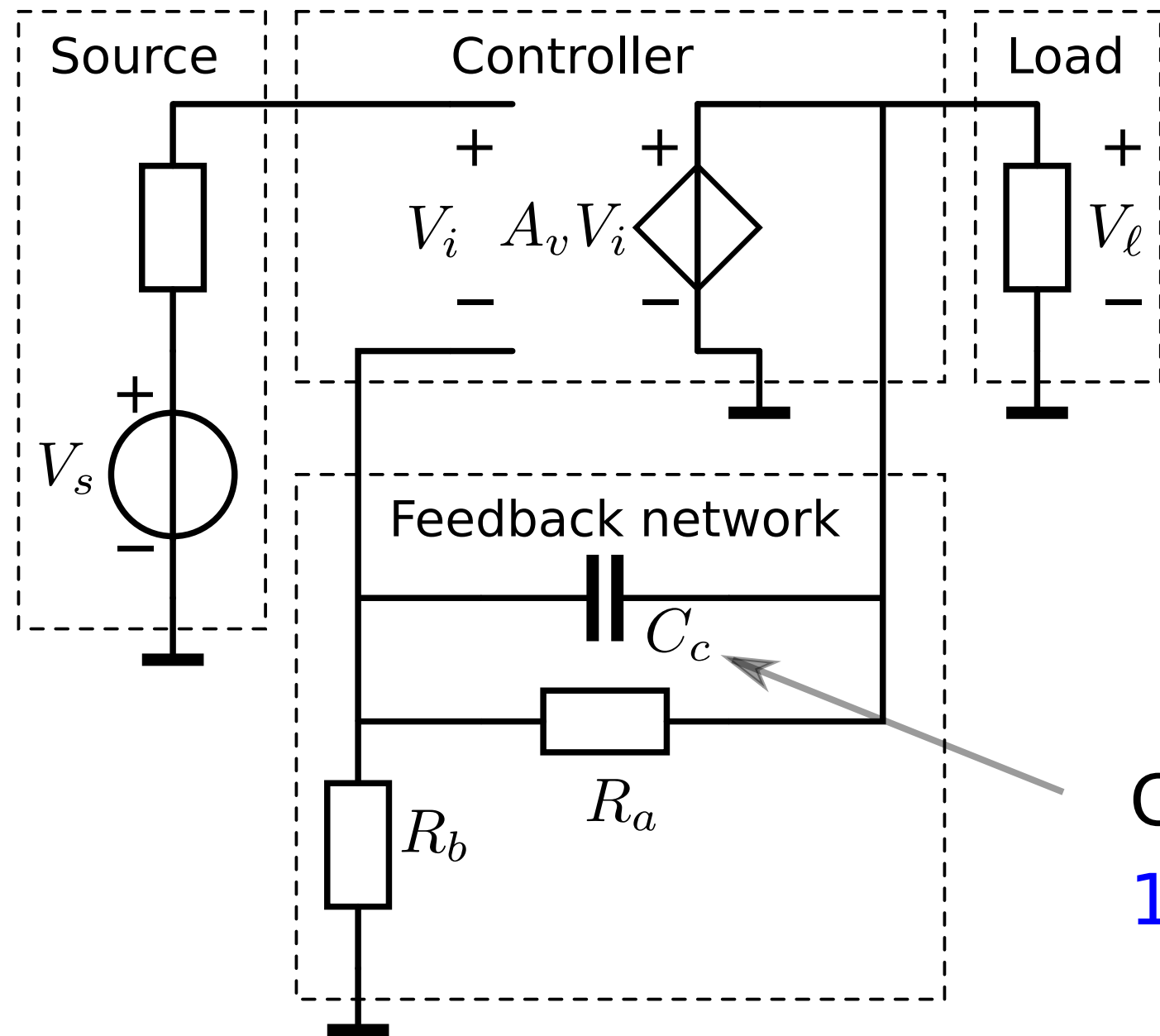


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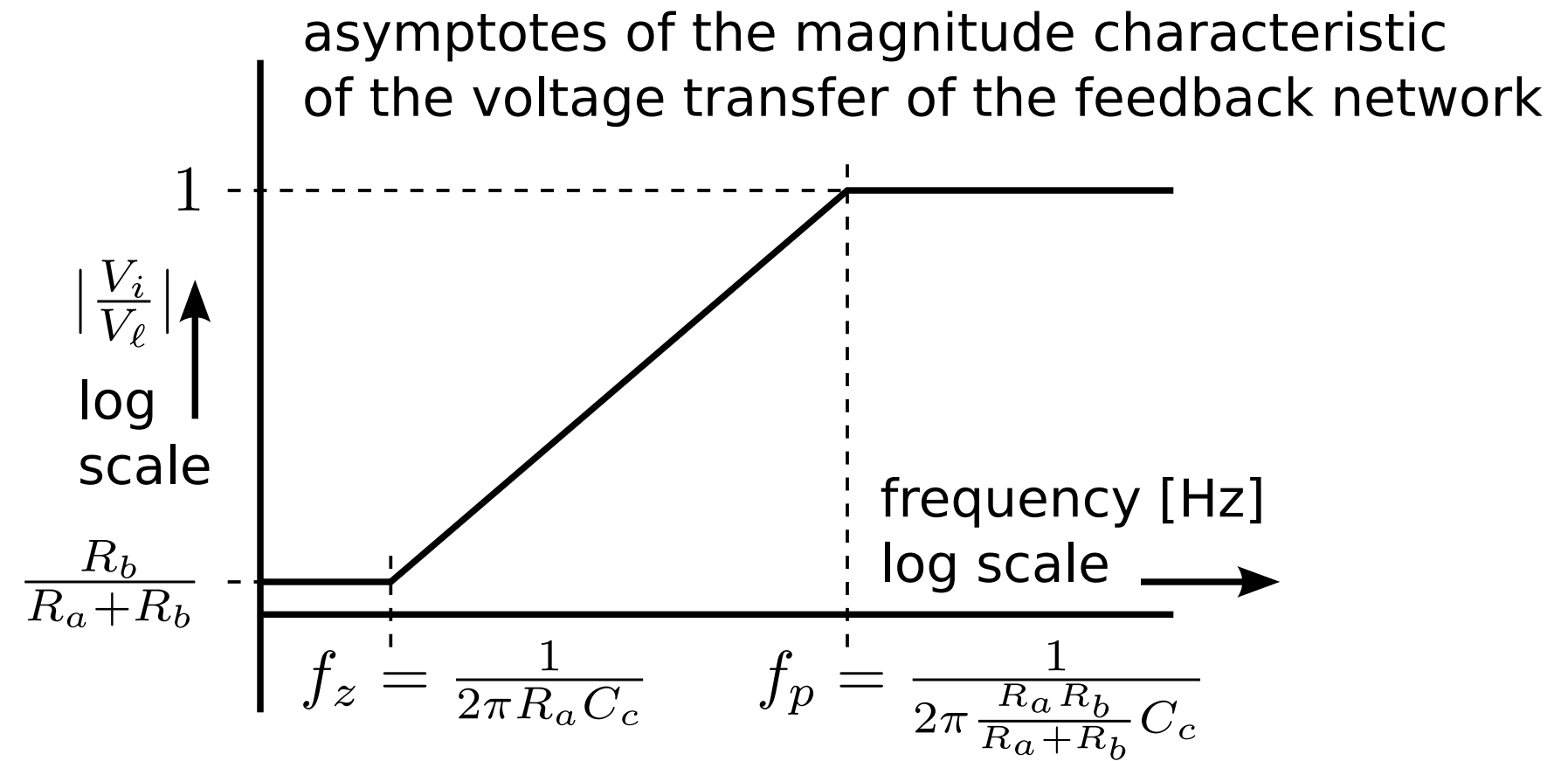
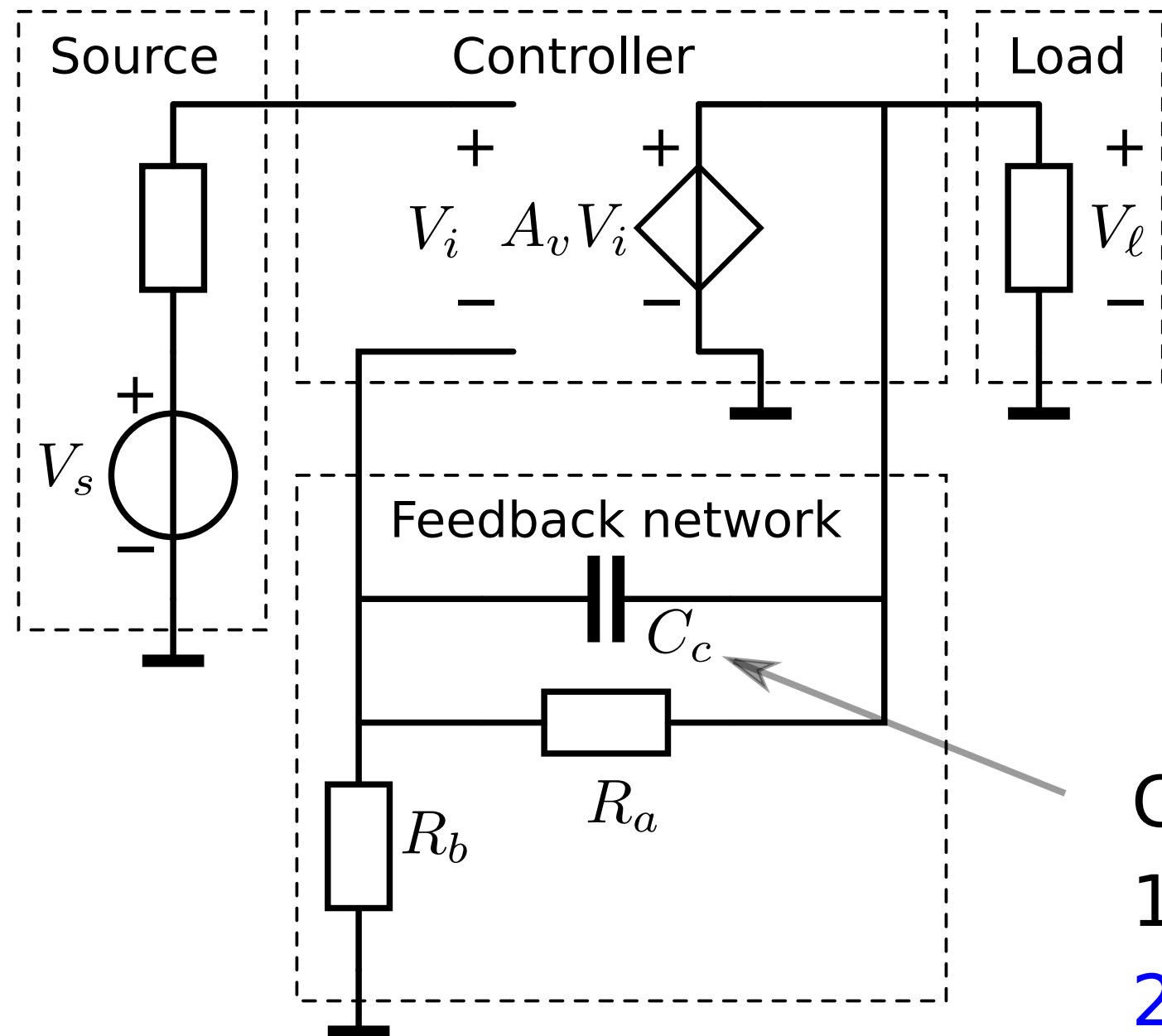
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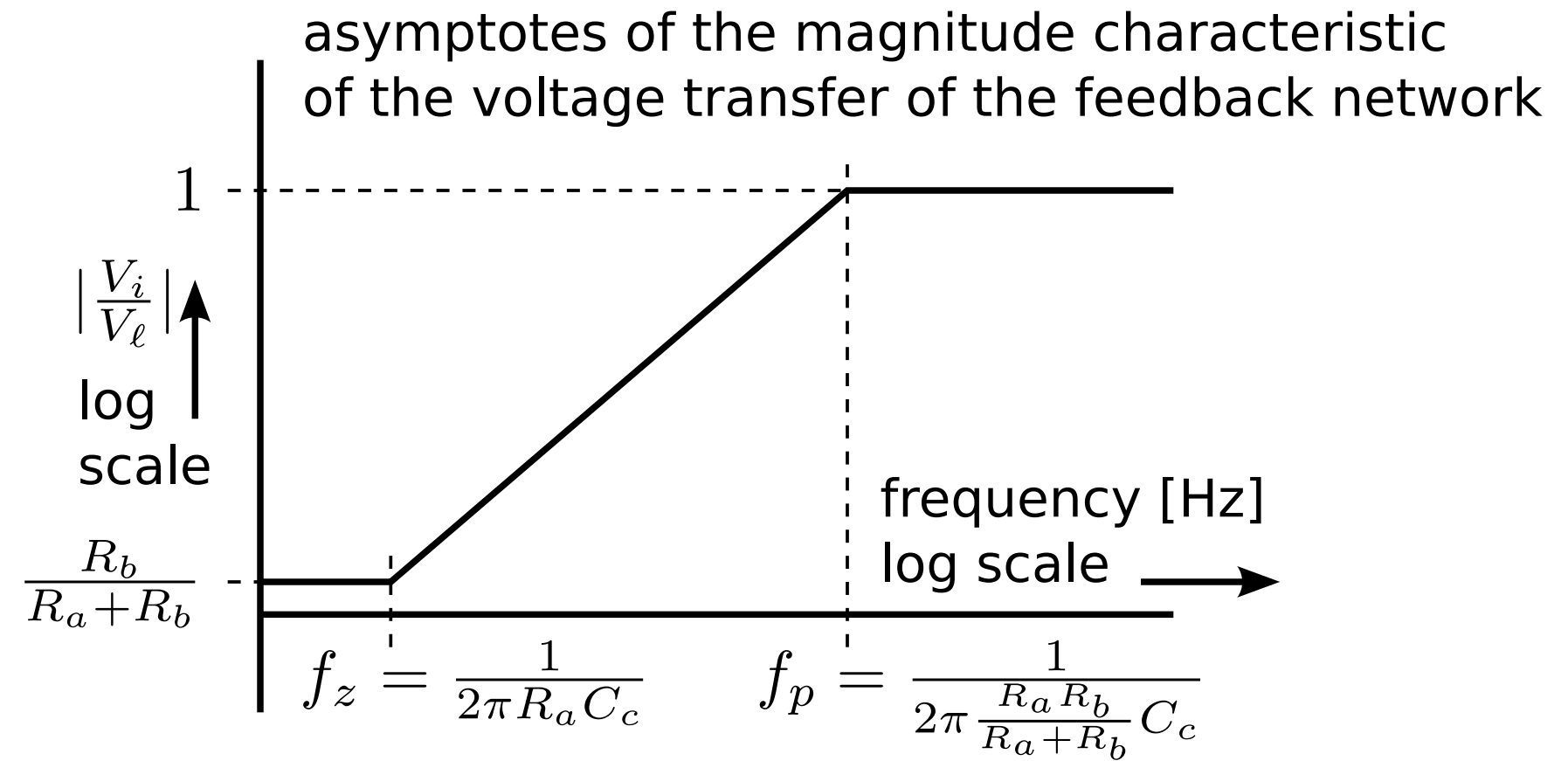
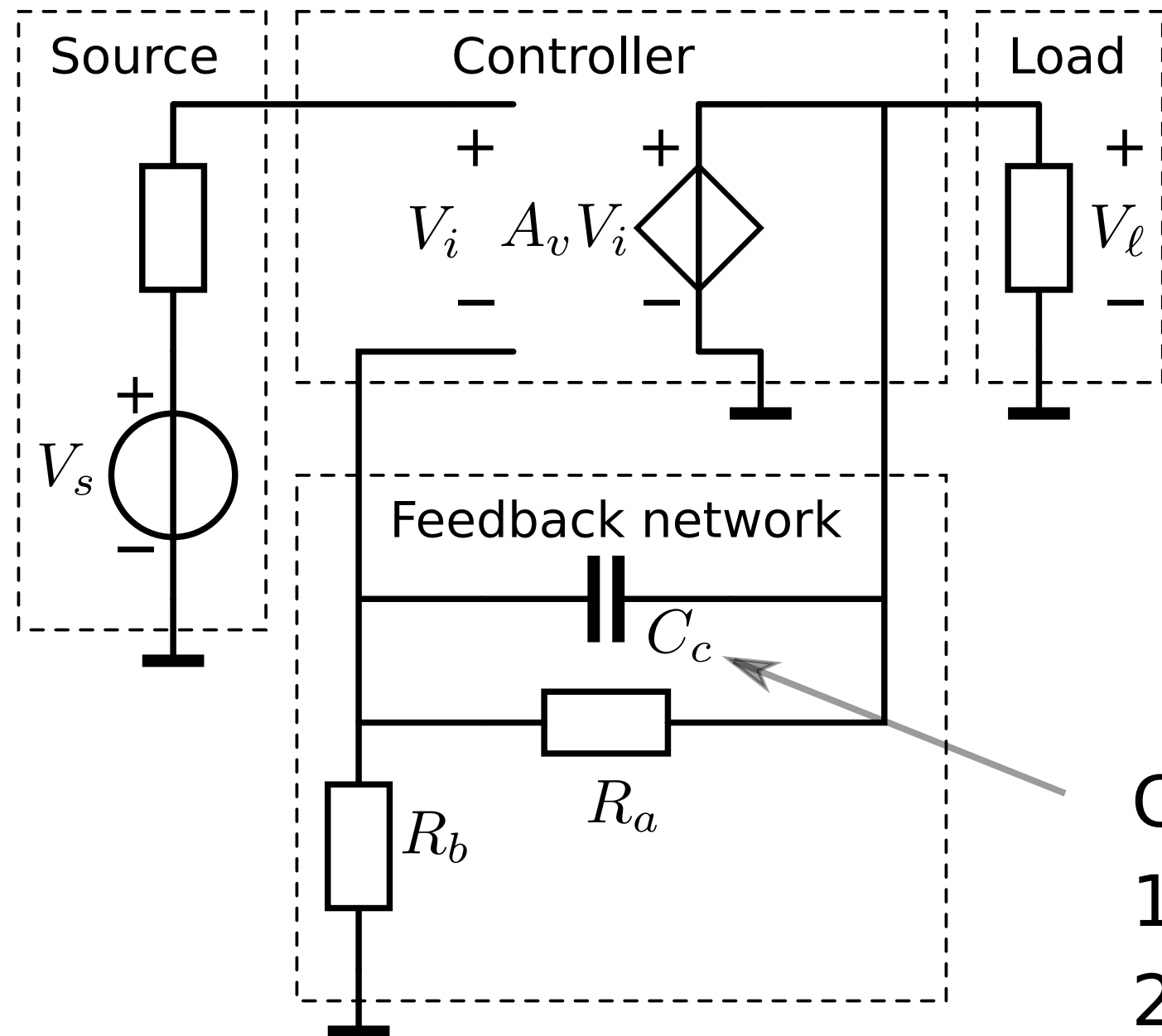
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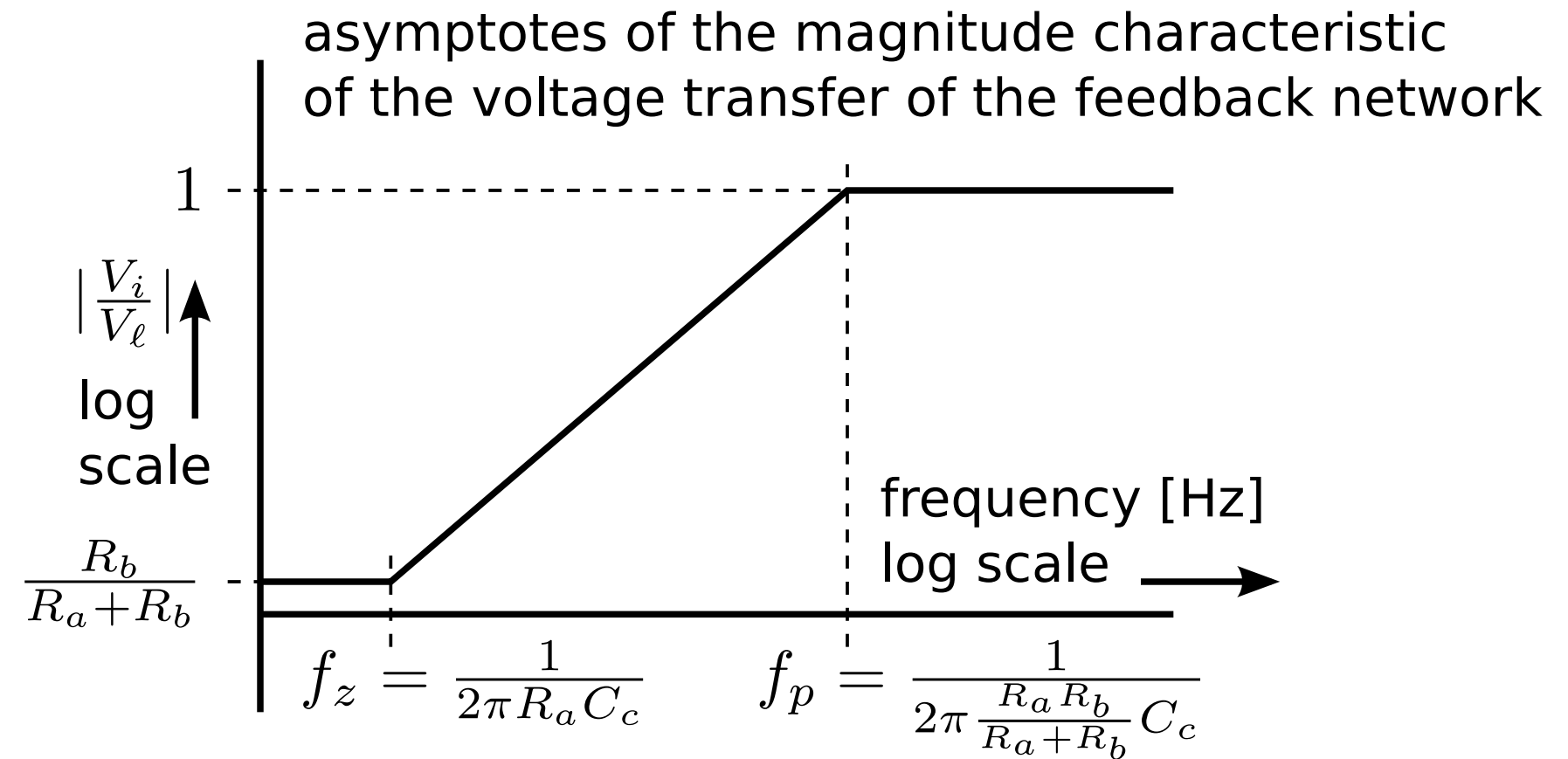
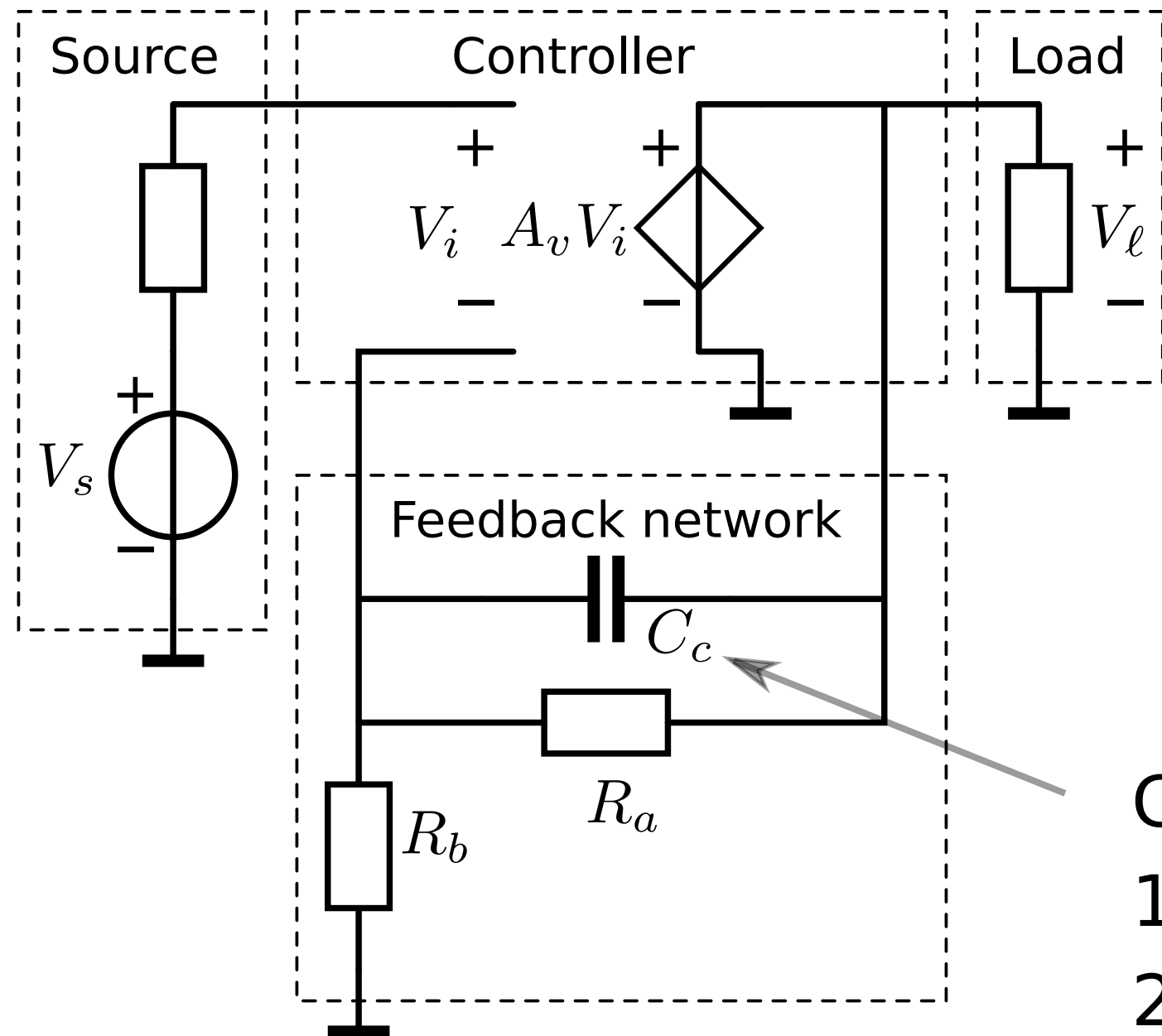


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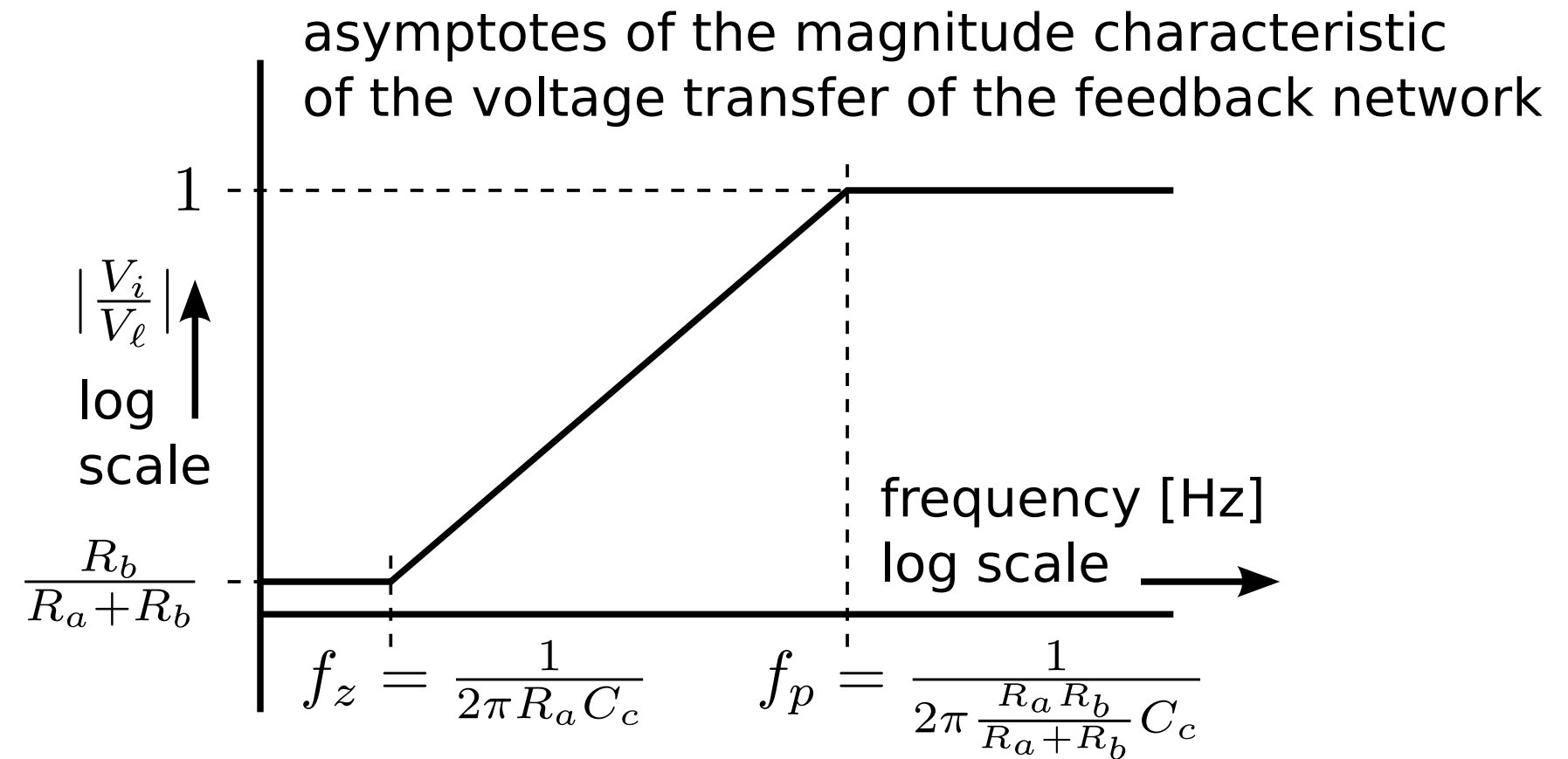
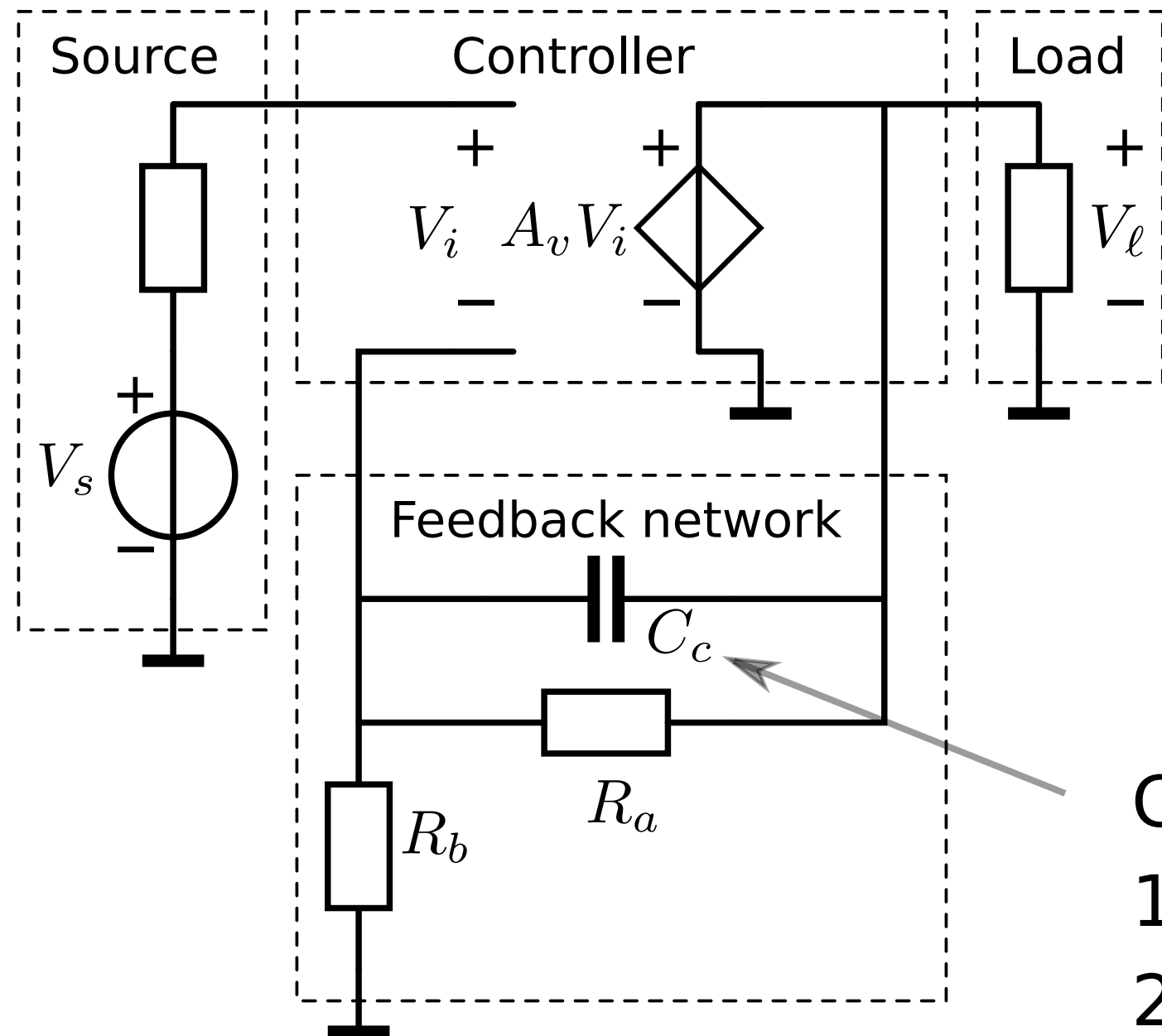


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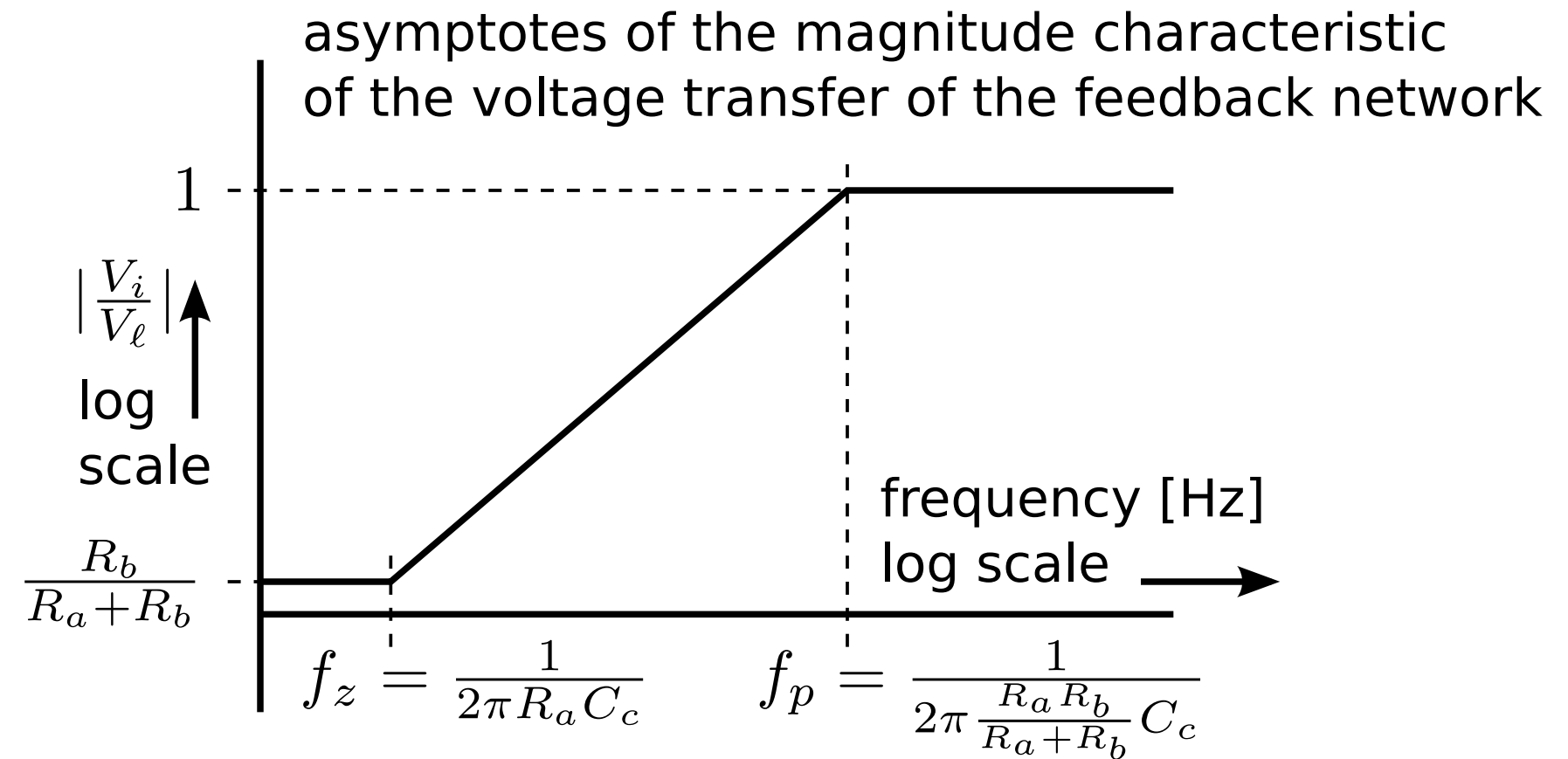
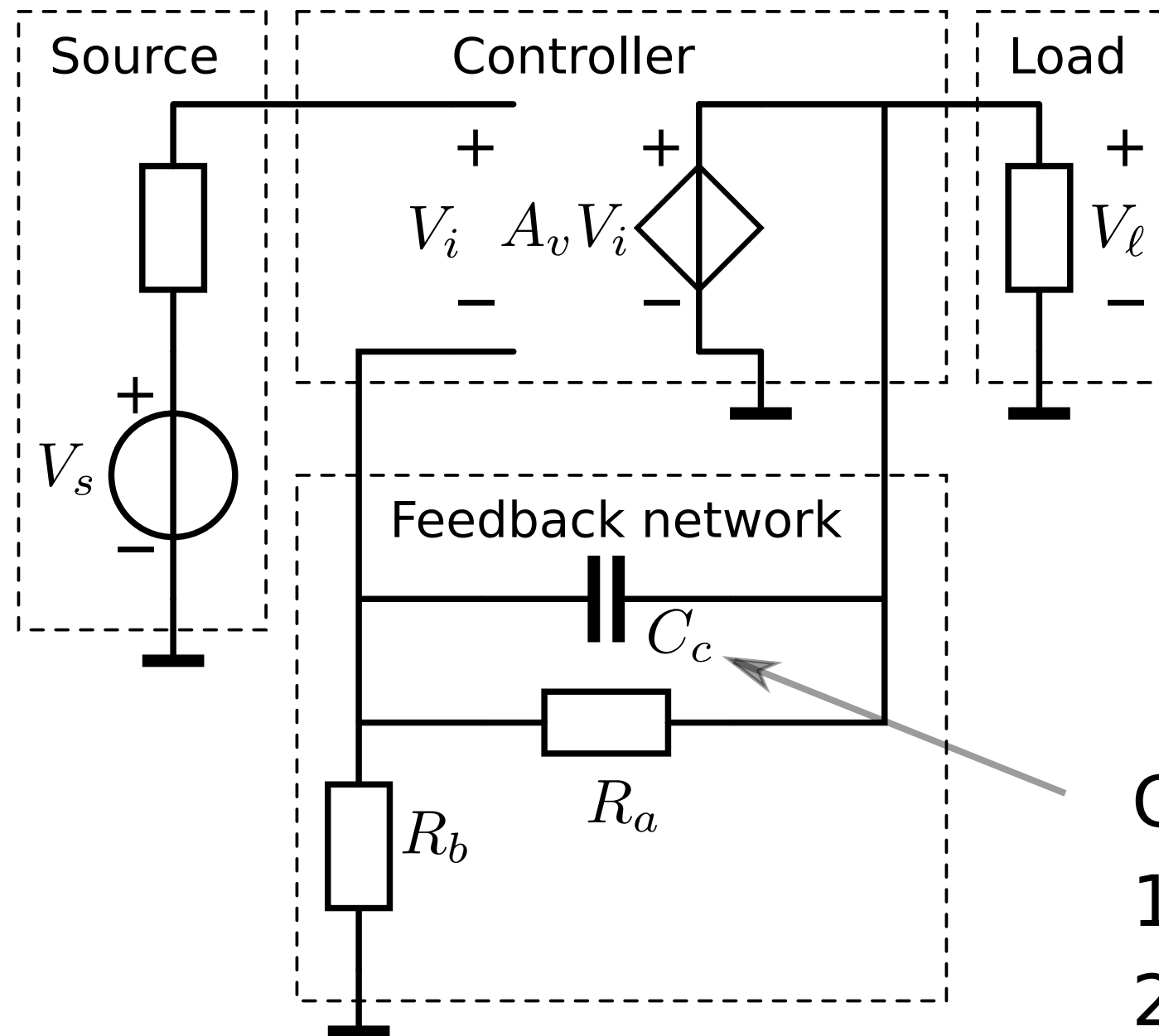


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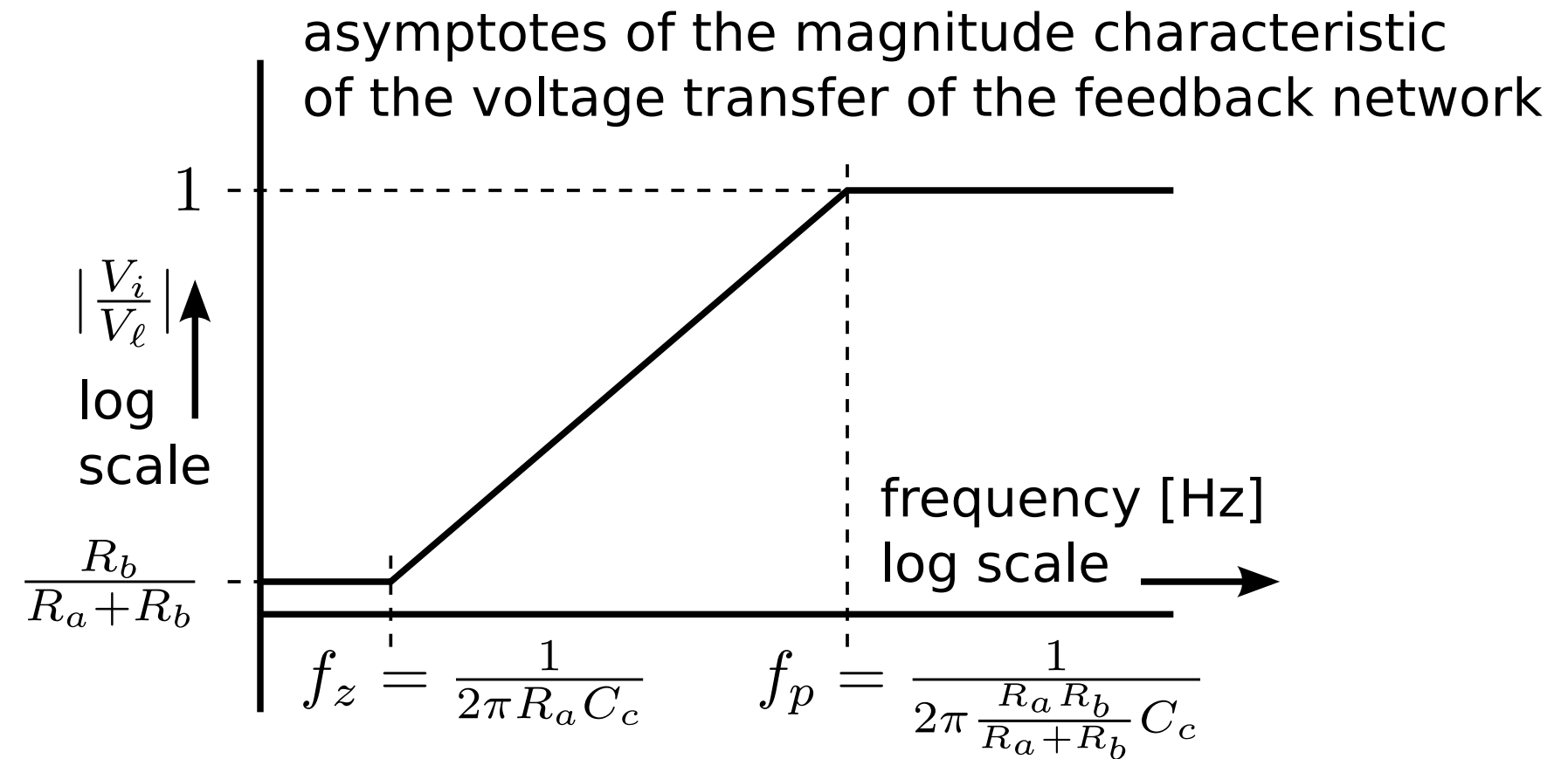
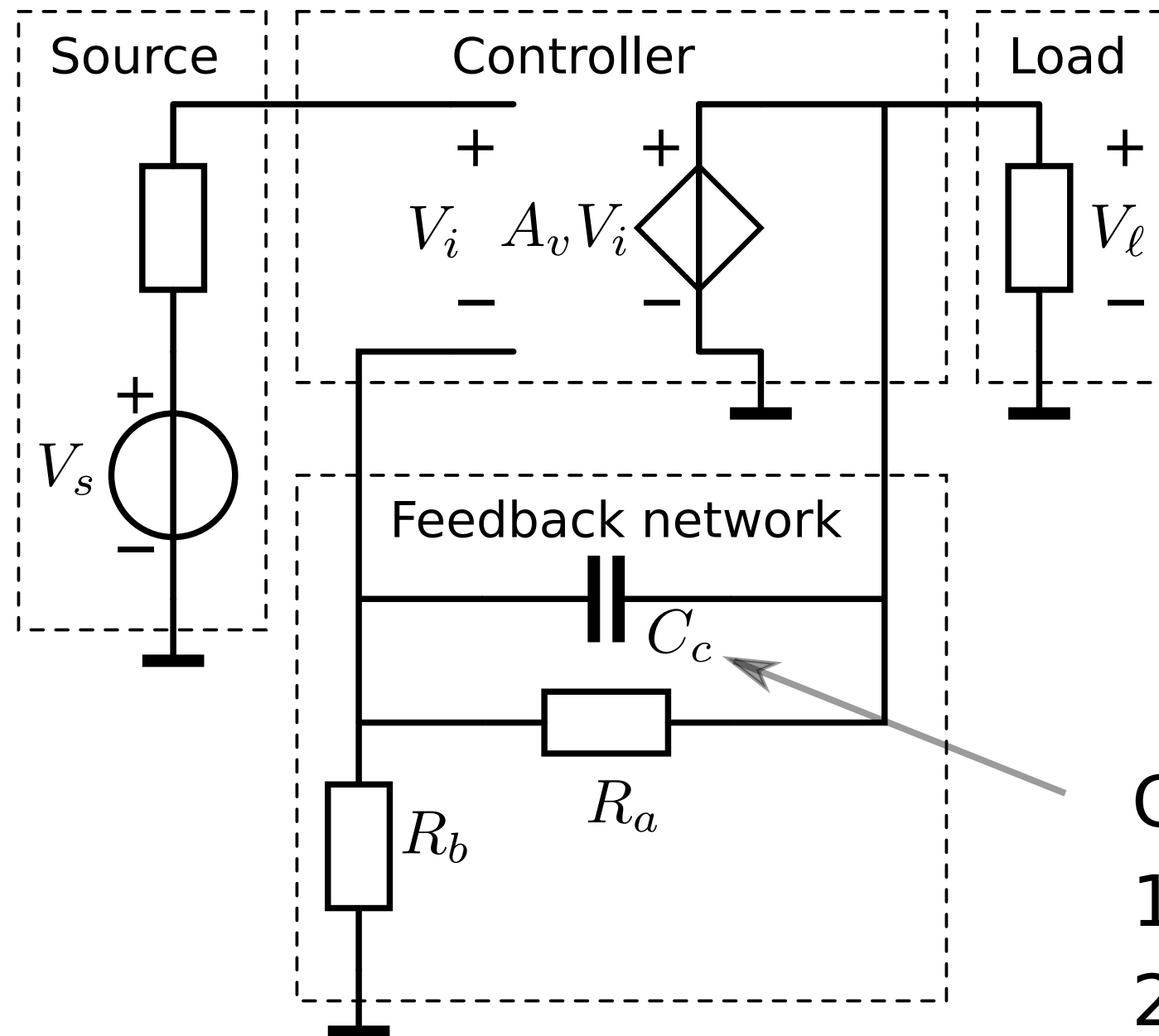
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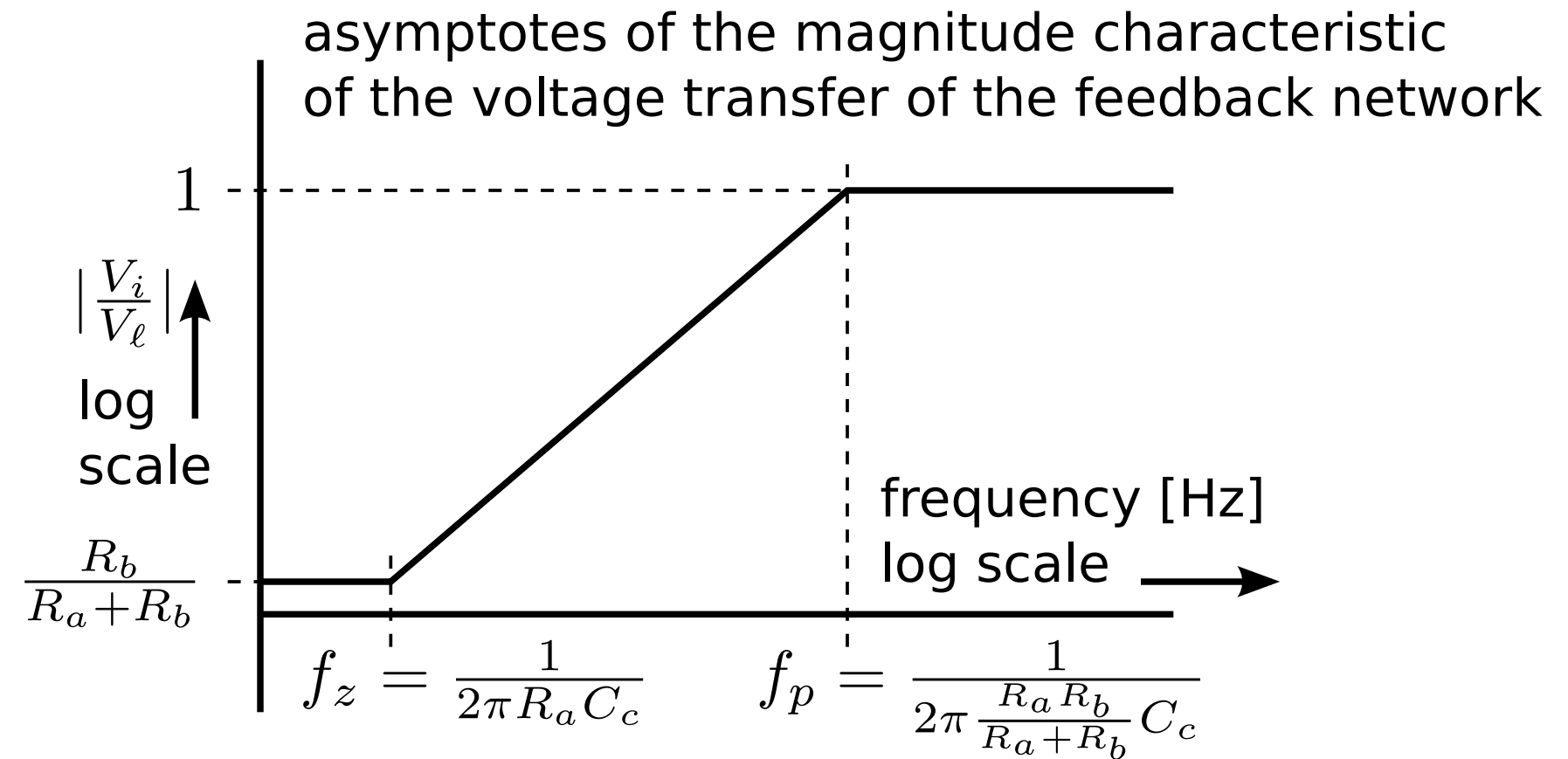
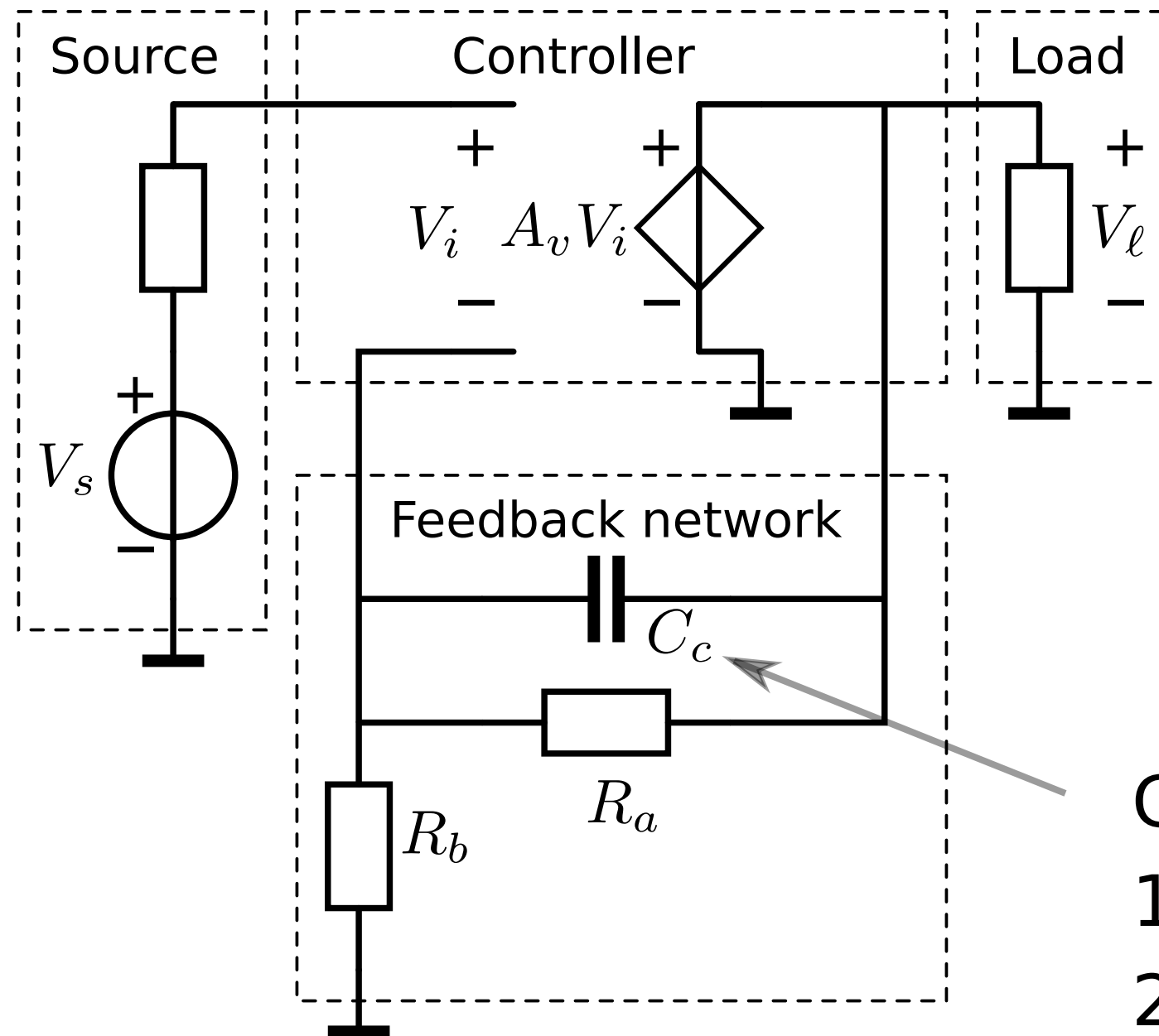
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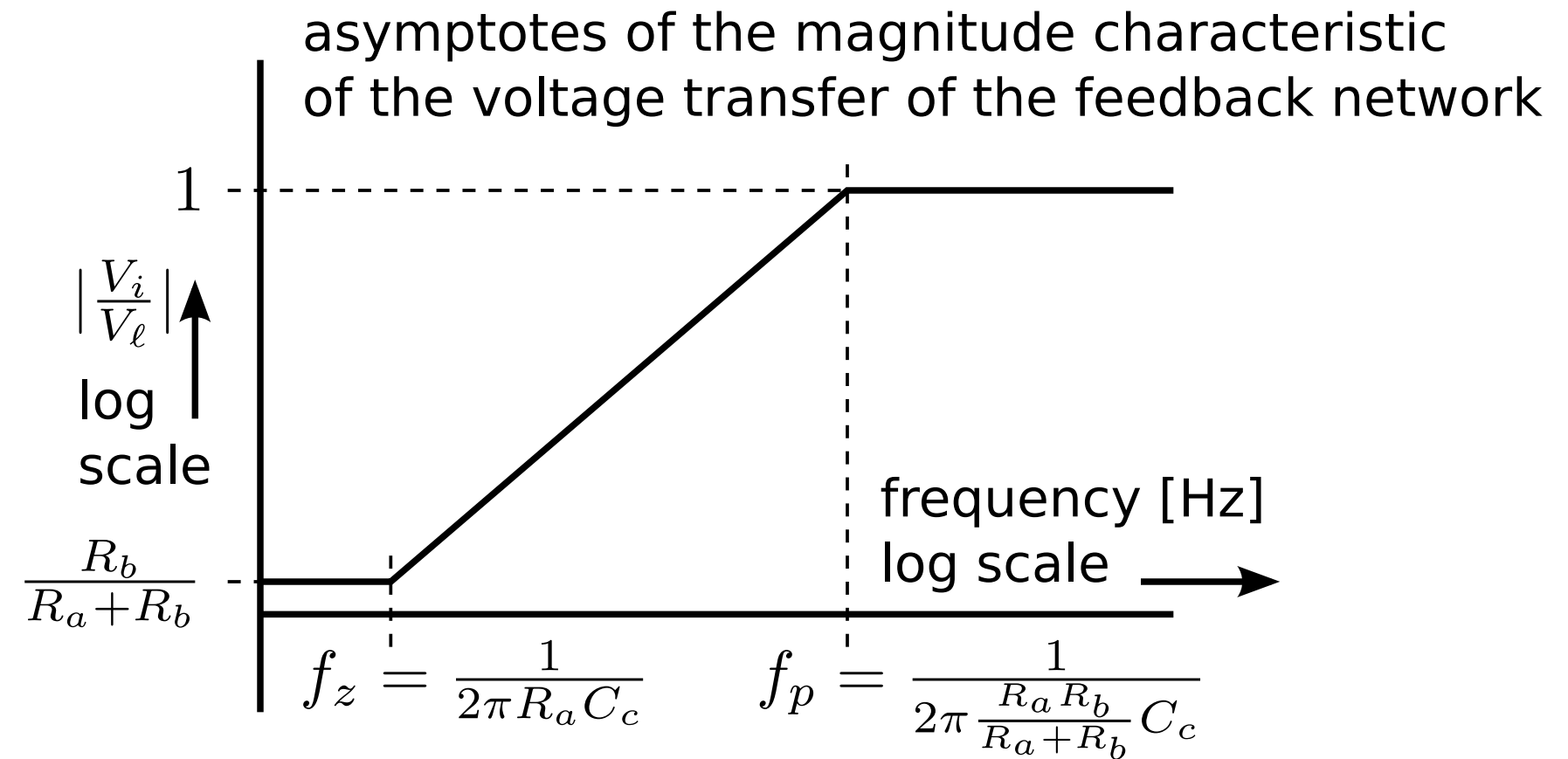
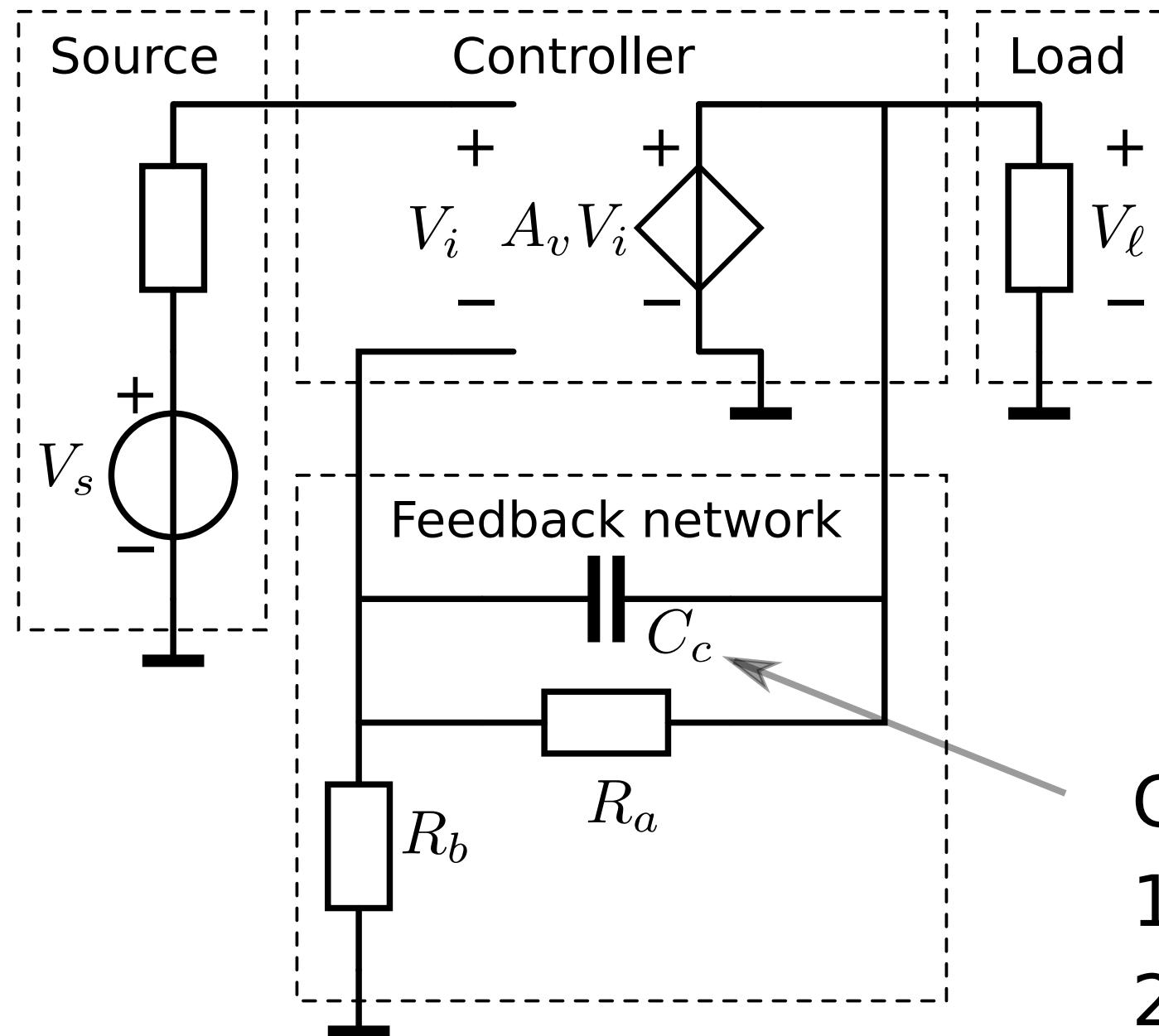
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