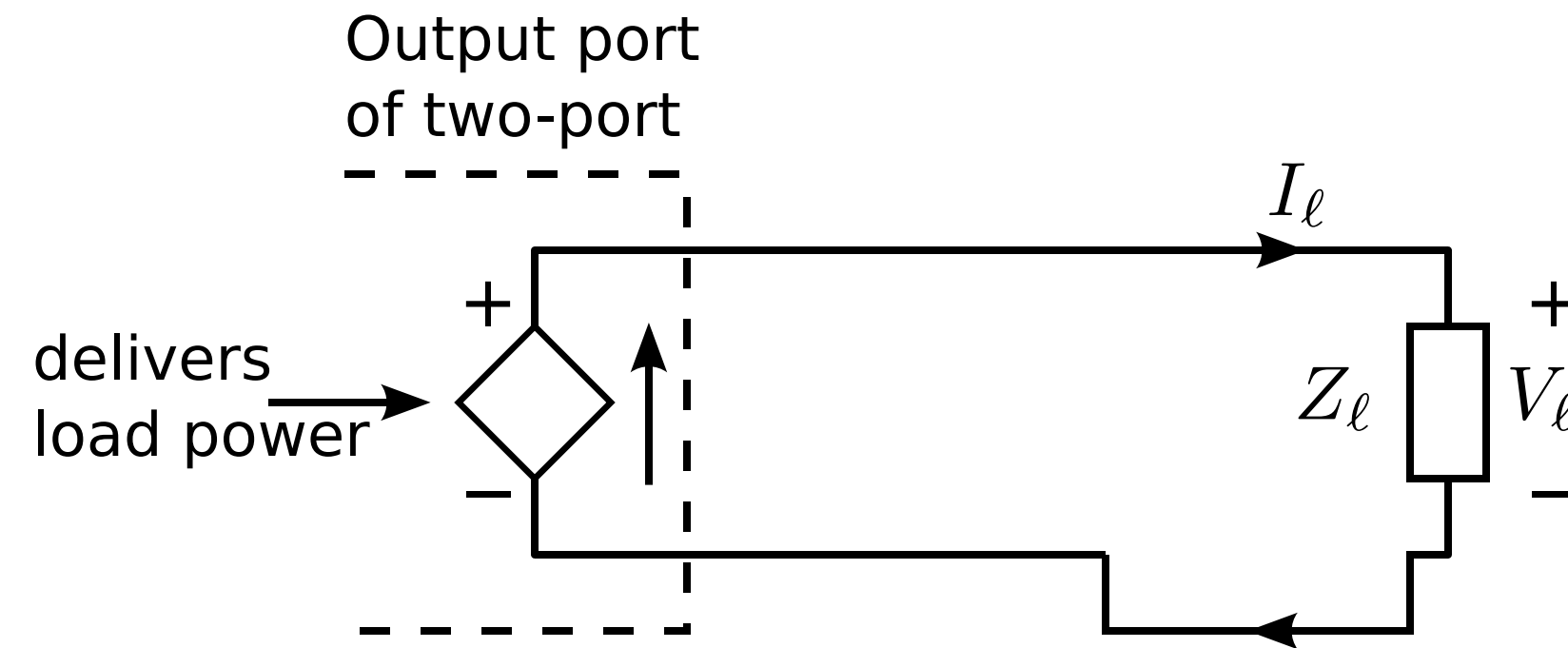


# Structured Electronic Design

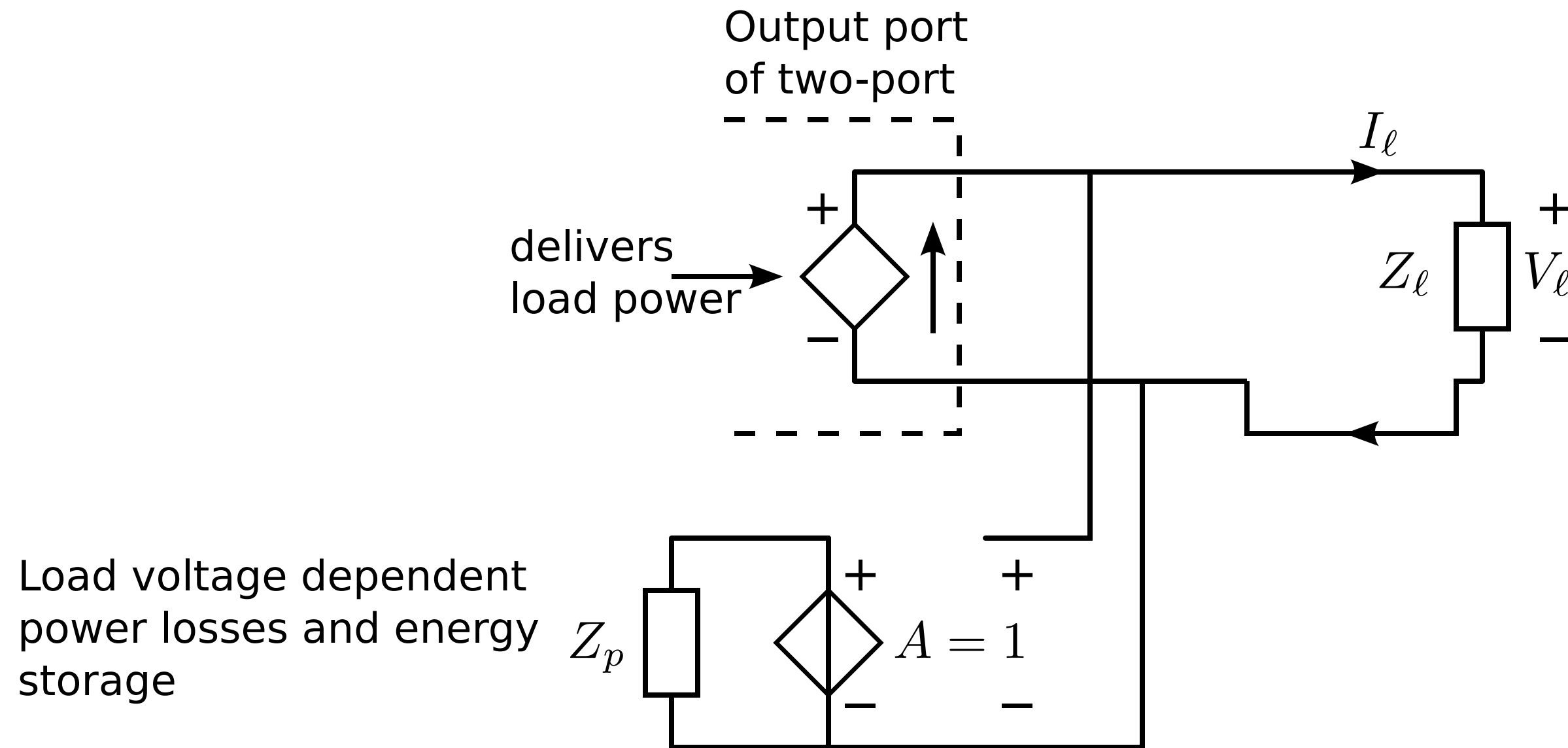
## Amplifiers: modeling of power losses and energy storage

# Power losses and energy storage

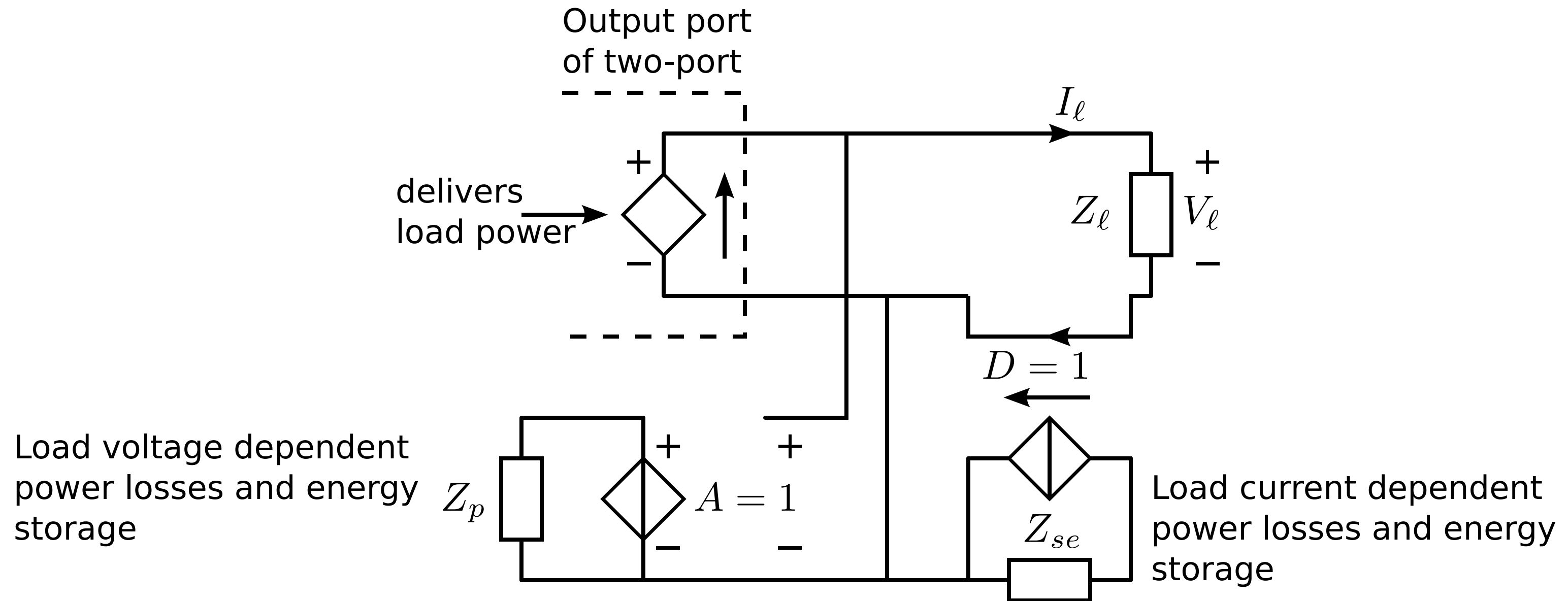
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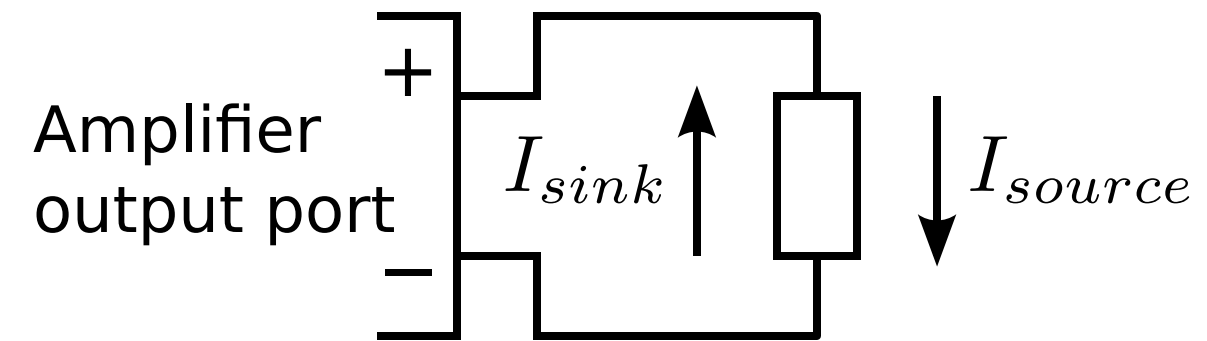
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# Amplifier output stages

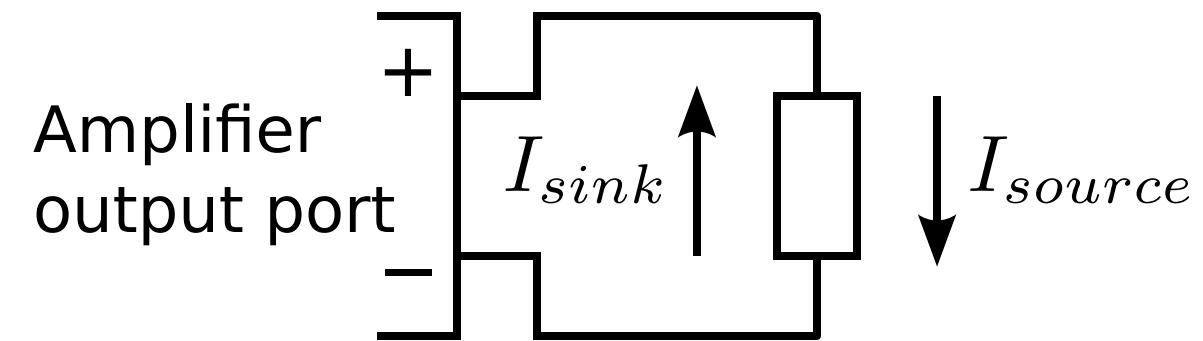
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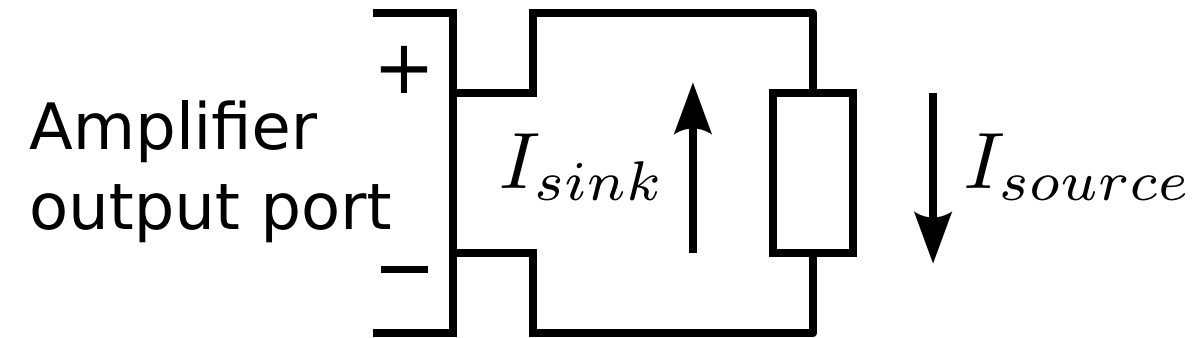


# Amplifier output stages

Amplifier stages constructed with devices that operate in one quadrant of the v-i plane



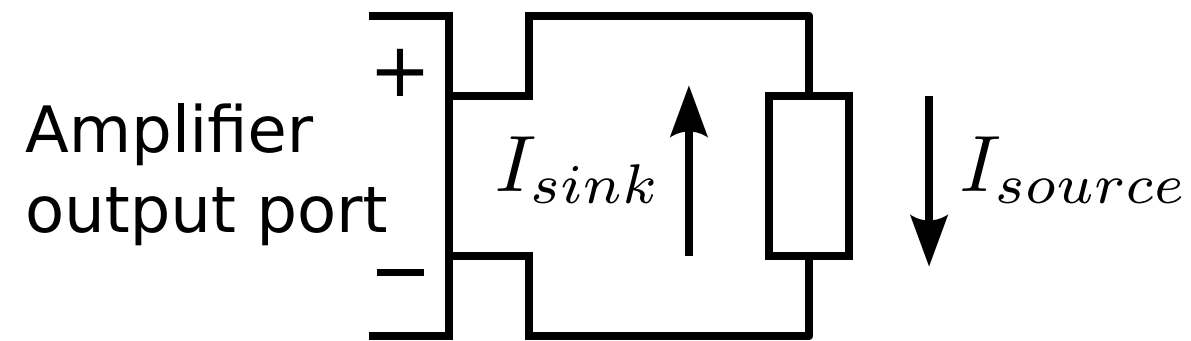
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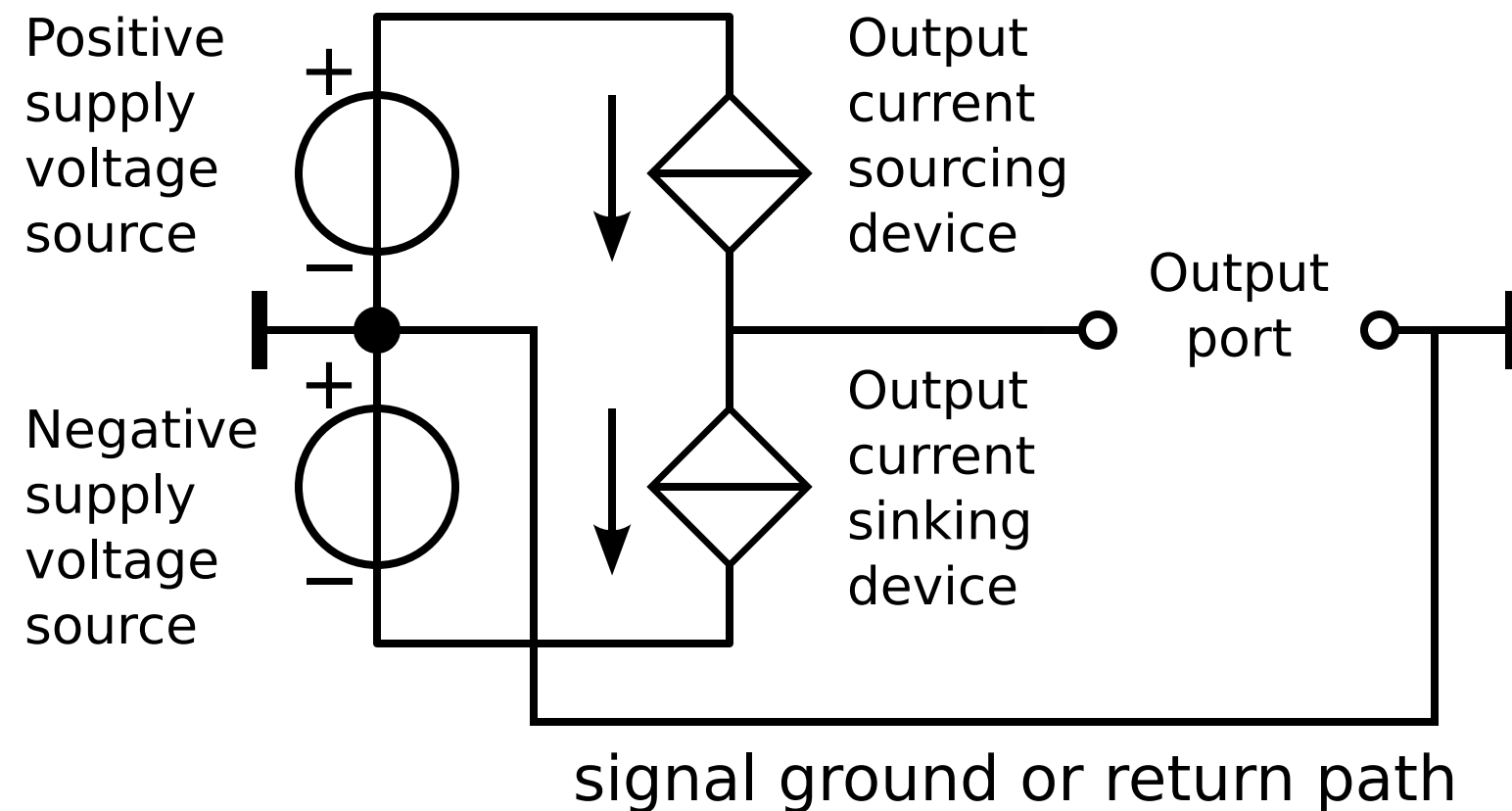
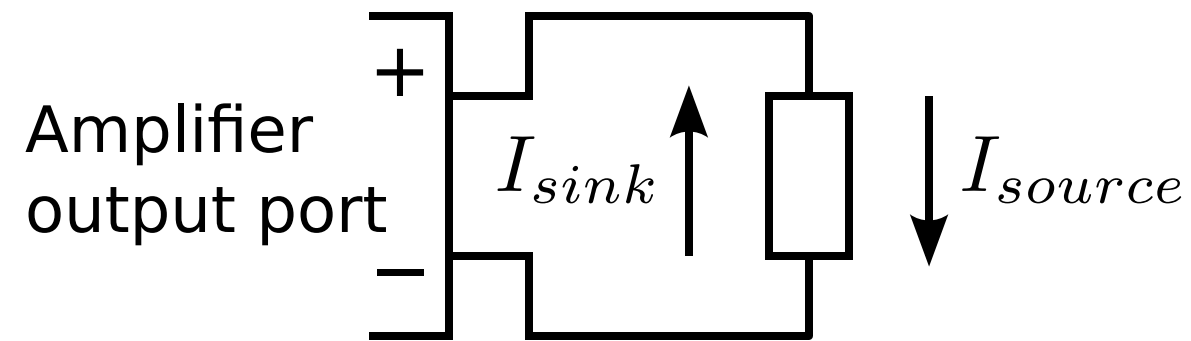
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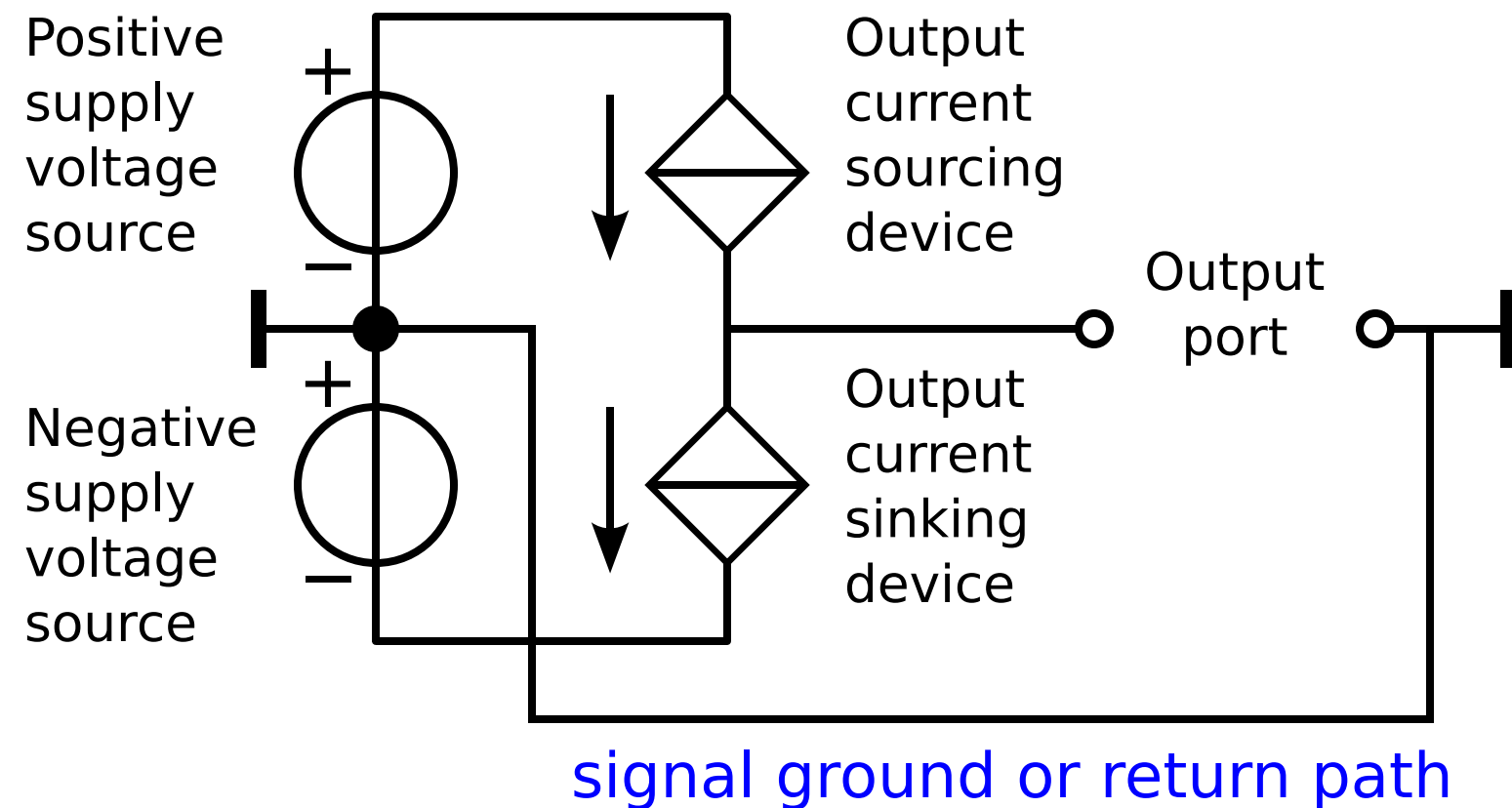
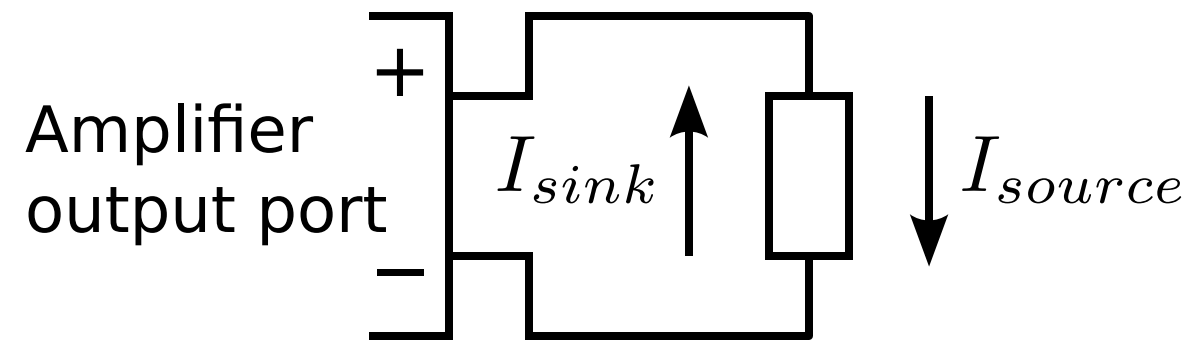


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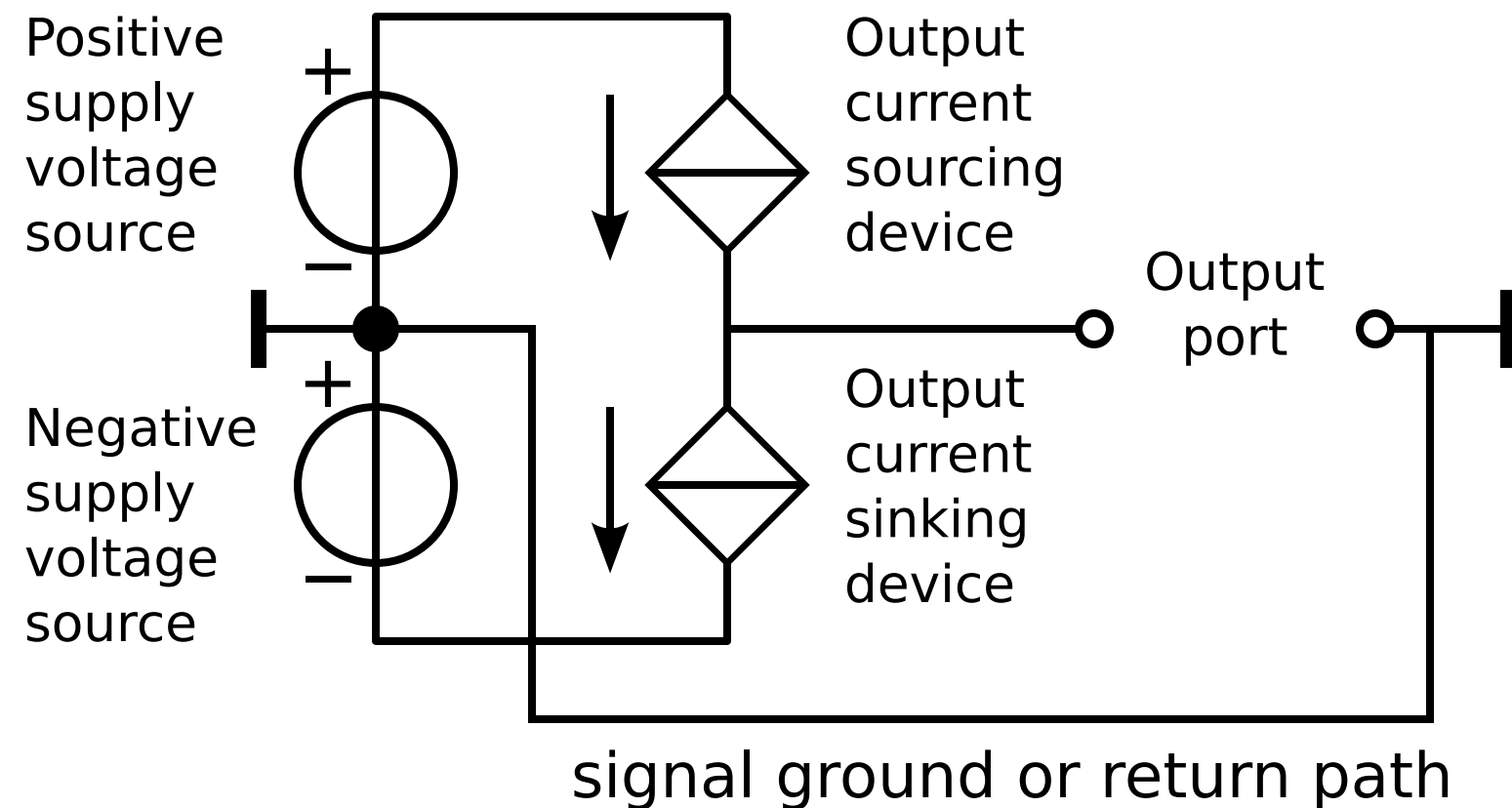
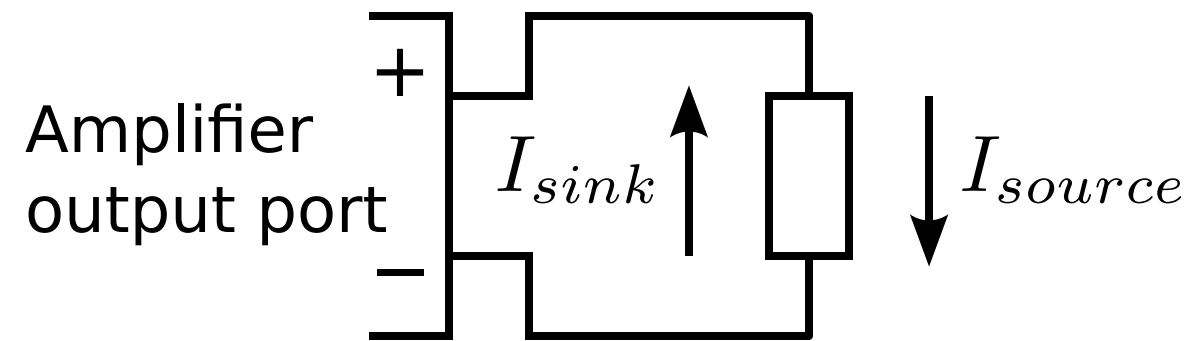


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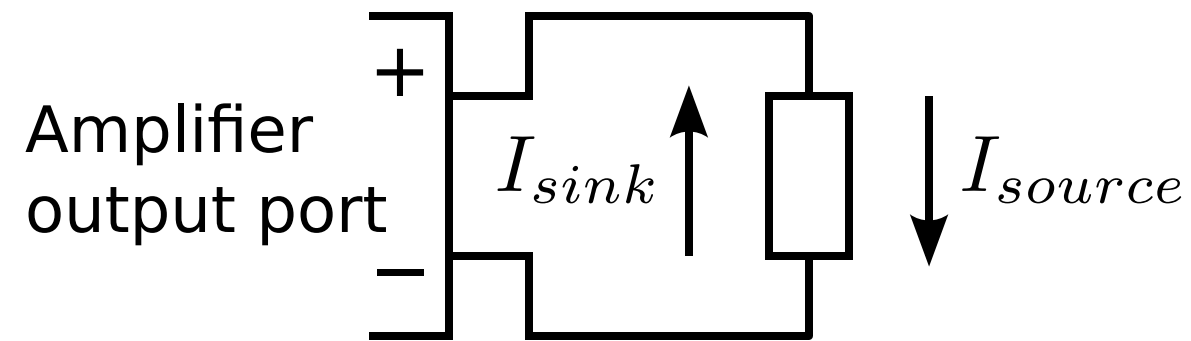
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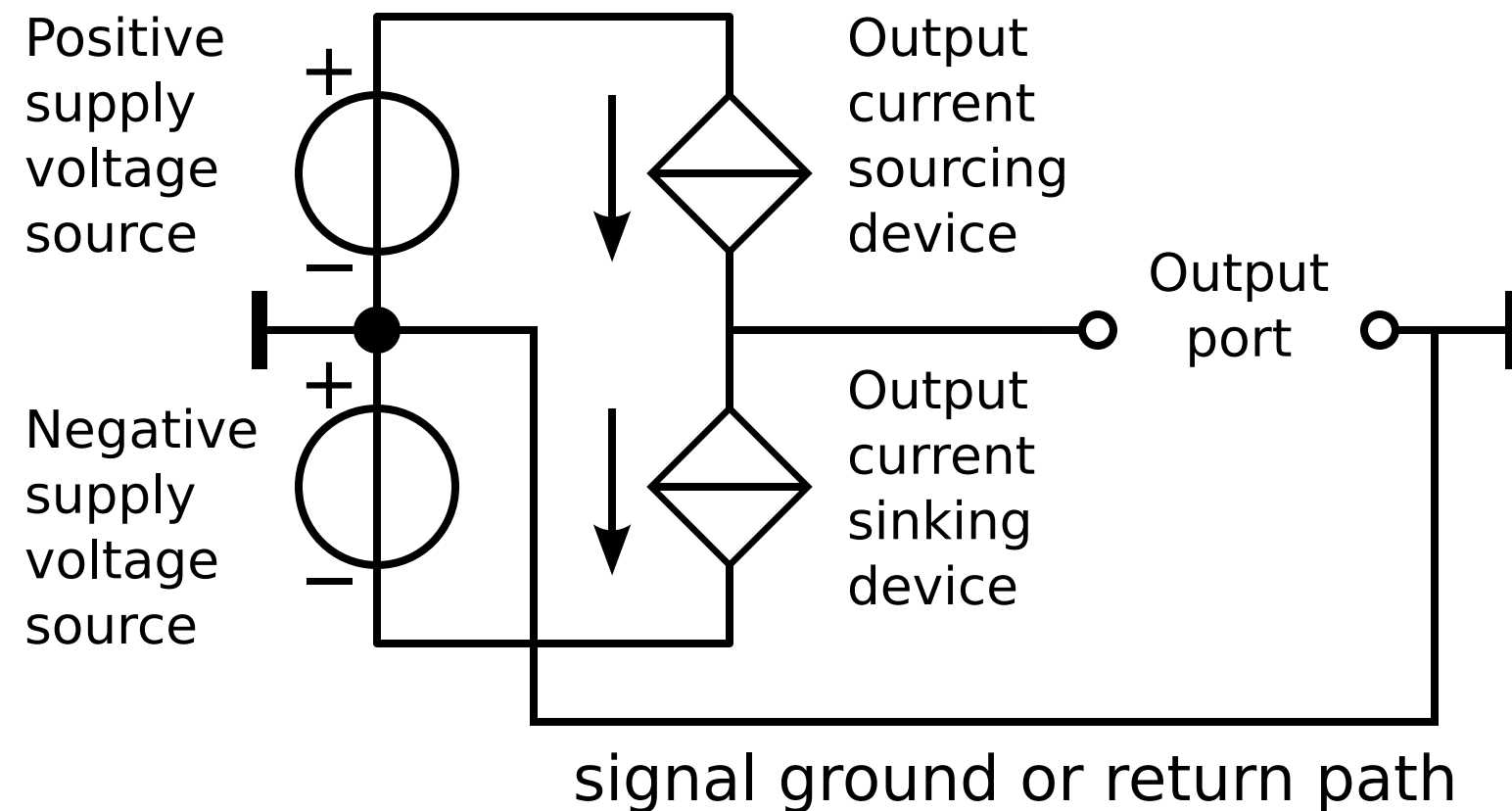
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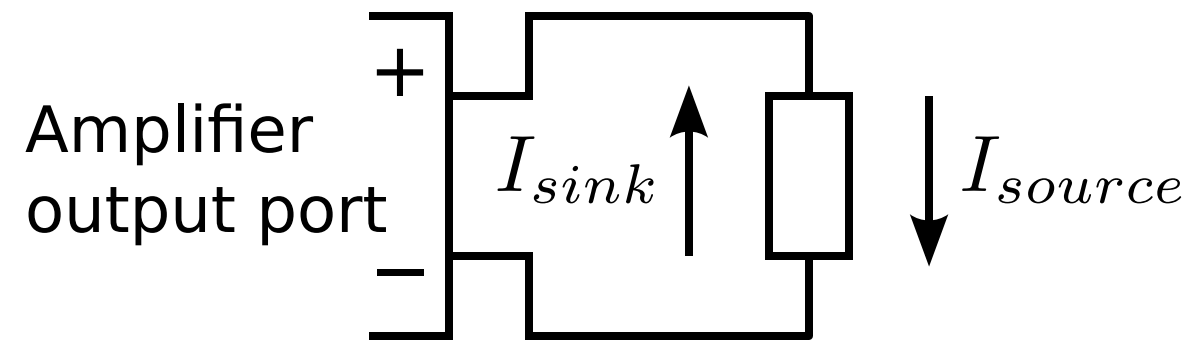
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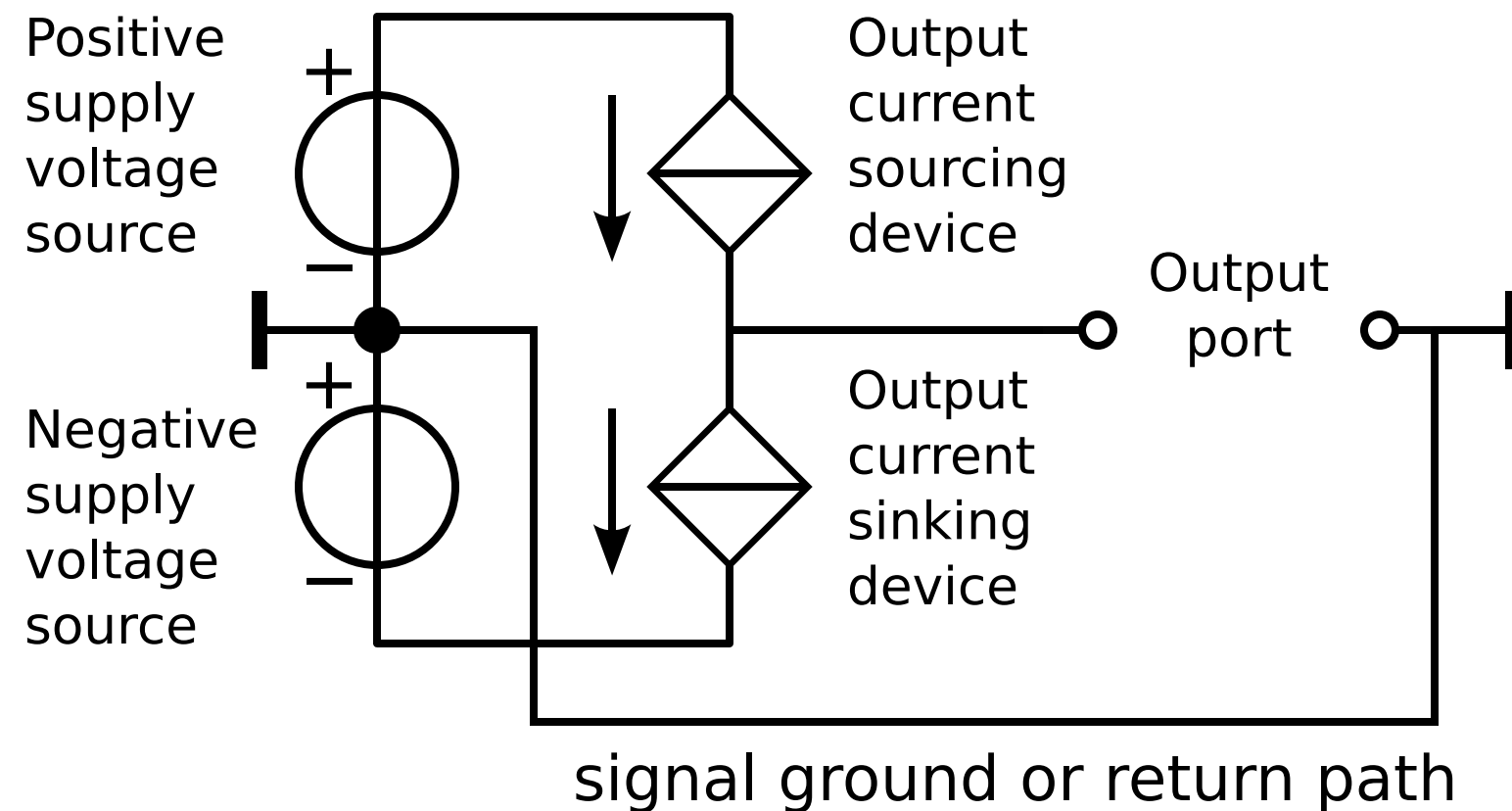
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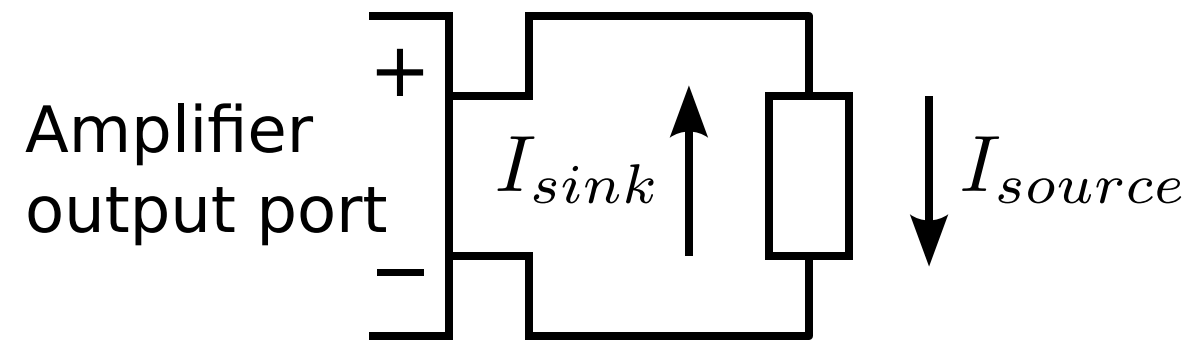
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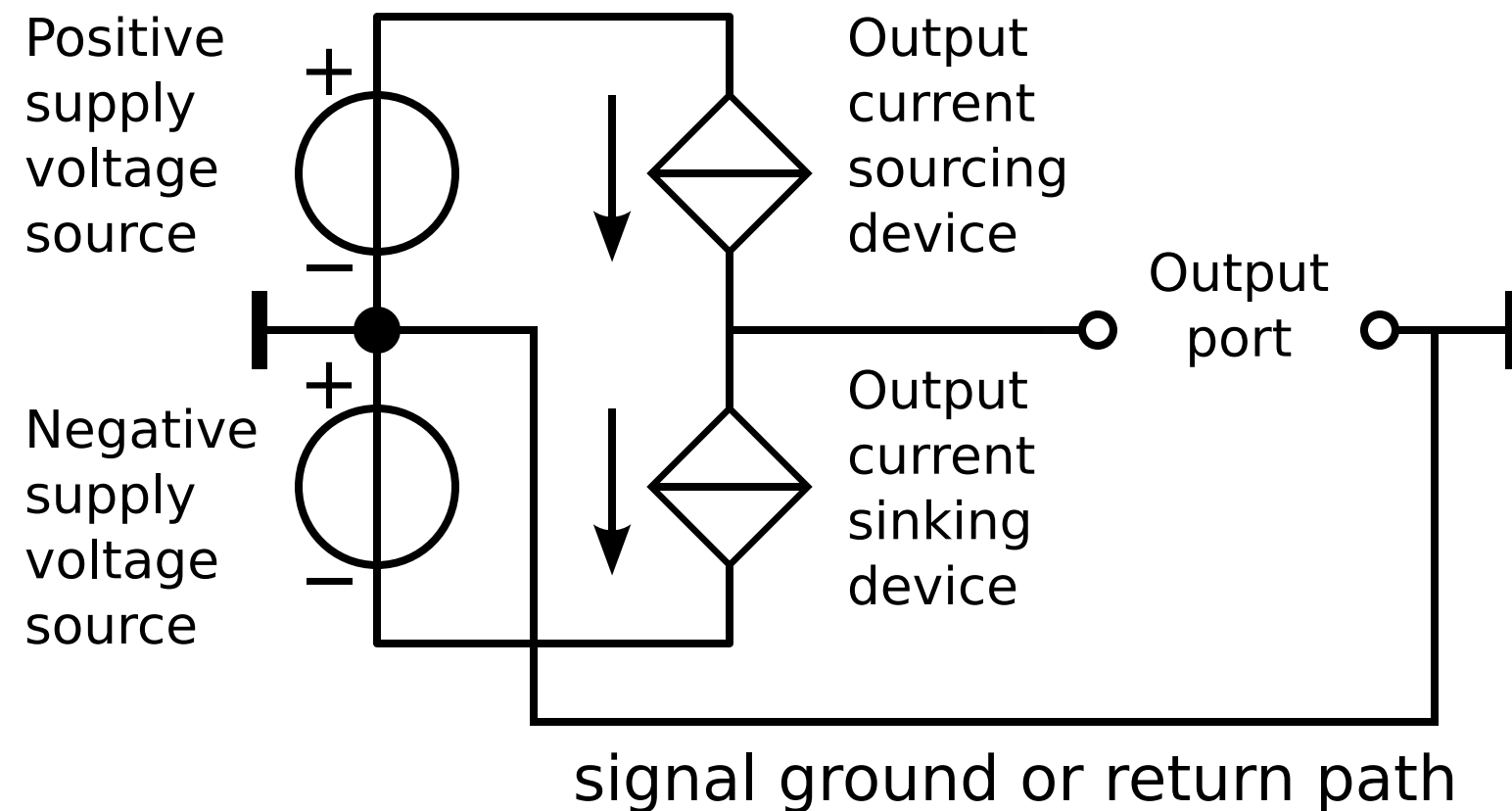
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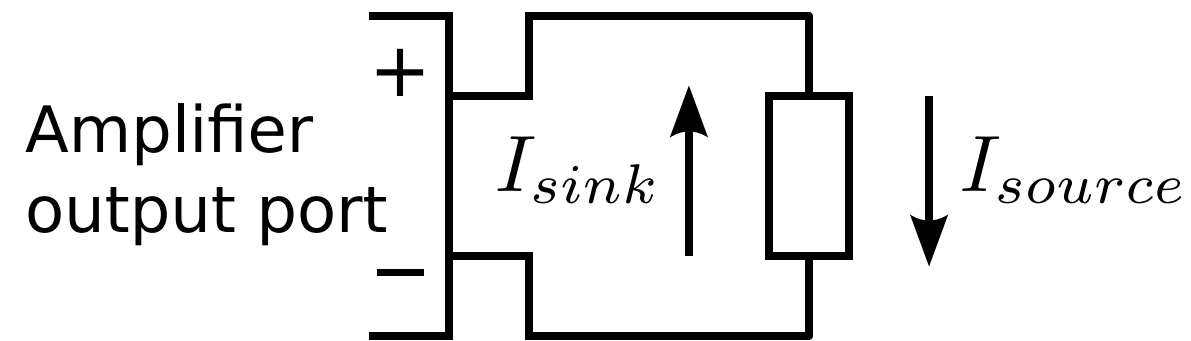
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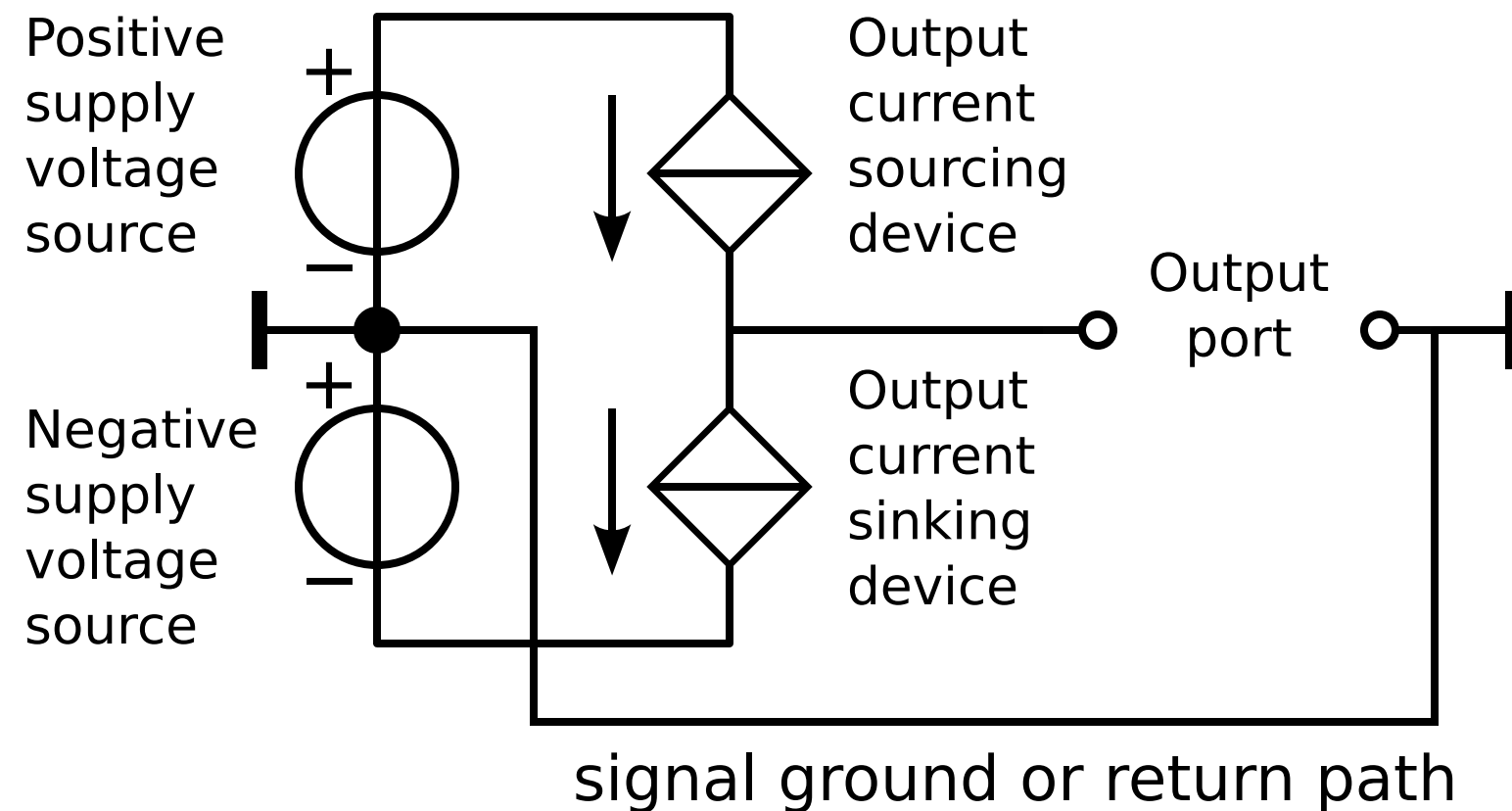
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